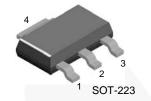
January 2014



FZT749 PNP Low Saturation Transistor

Description

These devices are designed with high-current gain and low saturation voltage with collector currents up to 3 A continuous.



1. Base 2,4. Collector 3. Emitter

Ordering Information

Part Number	Marking	Package	Packing Method	
FZT749	749	SOT-223 4L	Tape and Reel	

Absolute Maximum Ratings^{(1),(2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-25	V
V _{CBO}	Collector-Base Voltage -35		V
V _{EBO}	Emitter-Base Voltage		V
۱ _C	Collector Current - Continuous -3		Α
T _J , T _{STG}	Operating and Storage Junction Temperature Range -55 to +150		°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady state limits. Fairchild Semiconductor should be consulted on application involving pulsed or low-duty cycle operation.

Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
PD	Total Device Dissipation	2	W
R _{θJA}	Thermal Resistance, Junction to Ambient62.5		°C/W

Note:

3. PCB size: FR-4 76 x 114 x 1.57 mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

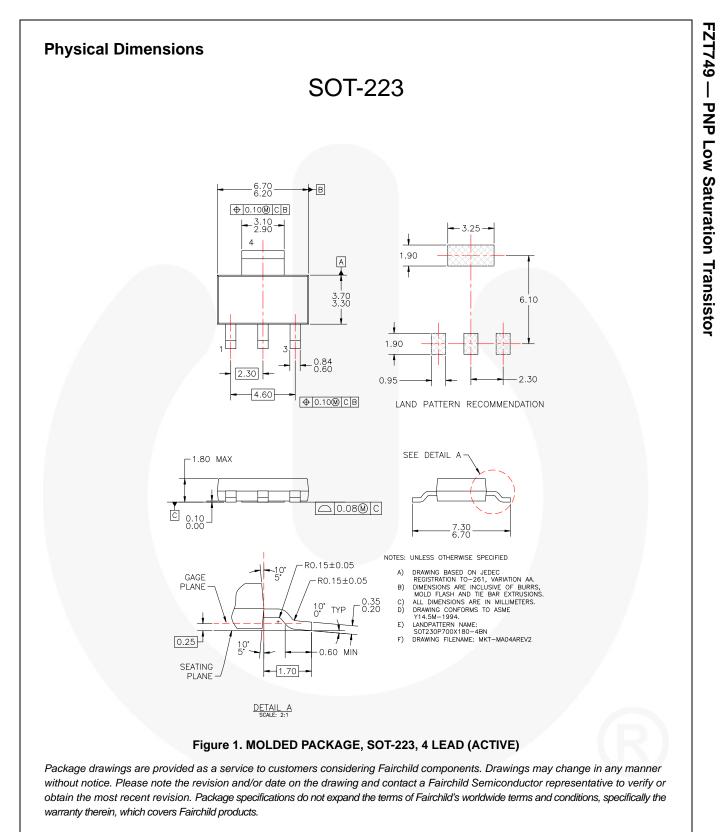
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -10 mA	-25		V
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = -100 μA	-35		V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -100 μA	-5		V
I _{CBO} Collector-Ba	Collector-Base Cut-Off Current	V _{CB} = -30 V		-100	nA
	Collector-base Cut-On Current	$V_{CB} = -30 \text{ V}, \text{ T}_{A} = 100^{\circ}\text{C}$		-10	μA
I _{EBO}	Emitter-Base Cut-Off Current	V _{EB} = -4 V		-100	nA
h _{FE} DC Current ($I_{\rm C}$ = -50 mA, $V_{\rm CE}$ = -2 V	70		
	DC Current $Coin(4)$	$I_{\rm C} = -1$ A, $V_{\rm CE} = -2$ V	100	300	
		$I_{\rm C} = -2$ A, $V_{\rm CE} = -2$ V	75		
		$I_{\rm C} = -6$ A, $V_{\rm CE} = -2$ V	15		
	Collector-Emitter Saturation	I _C = -1 A, I _B = -100 mV		-300	m\/
	/oltage ⁽⁴⁾	I _C = -3 A, I _B = -300 mV		-600	mV
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽⁴⁾	I _C = -1 A, I _B = -100 mV		-1.25	V
V _{BE} (on)	Base-Emitter On Voltage ⁽⁴⁾	I _C = -1 A, V _{CE} = -2 V		-1	V
C _{ob}	Output Capacitance	V _{CB} = -10 V, I _E = 0, f = 1 MHz		100	pF
f _T	Transition Frequency	$I_{C} = -100 \text{ mA}, V_{CE} = -5 \text{ V},$ f = 100 MHz	100		MHz

Note:

4. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2.0%.



Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/dwg/MA/MA04A.pdf.

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