Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V	T_A = +25°C T_A = +70°C	I_D	13 10	A
	T_C = +25°C T_C = +70°C	I_D	48 38	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	110	A
Maximum Continuous Body Diode Forward Current (Note 6)		I_S	2.5	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I_{SM}	110	A
Avalanche Current, L = 0.3mH		I_{AS}	21	A
Avalanche Energy, L = 0.3mH		E_{AS}	66	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	1.8	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\Theta JA}$	68	°C/W
Total Power Dissipation (Note 6)	P_D	2.5	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\Theta JA}$	50	°C/W
Thermal Resistance, Junction to Case (Note 6)	$R_{\Theta JC}$	4	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	100	—	—	V	V_{GS} = 0V, I_D = 1mA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	µA	V_{DS} = 80V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1.3	—	2.5	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	7.1	9	mΩ	V_{GS} = 10V, I_D = 10A
		—	9.7	13.8		V_{GS} = 4.5V, I_D = 6A
Diode Forward Voltage	V_{SD}	—	0.8	1.2	V	V_{GS} = 0V, I_S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	2309	—	pF	V_{DS} = 50V, V_{GS} = 0V, f = 1MHz
Output Capacitance	C_{oss}	—	536	—		
Reverse Transfer Capacitance	C_{rss}	—	13.7	—		
Gate Resistance	R_g	—	1.9	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = 10V)	Q_g	—	40.2	—	nC	V_{DD} = 50V, I_D = 20A
Total Gate Charge (V_{GS} = 4.5V)	Q_g	—	20.2	—		
Gate-Source Charge	Q_{gs}	—	7.0	—		
Gate-Drain Charge	Q_{gd}	—	8.5	—		
Turn-On Delay Time	$t_{D(ON)}$	—	5.4	—	ns	V_{DD} = 50V, V_{GS} = 10V, I_D = 20A, R_g = 3Ω
Turn-On Rise Time	t_R	—	10.6	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	28.3	—		
Turn-Off Fall Time	t_F	—	14.9	—		
Body Diode Reverse Recovery Time	t_{RR}	—	44.3	—	ns	I_F = 20A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	—	65.5	—	nC	

Notes: 5. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

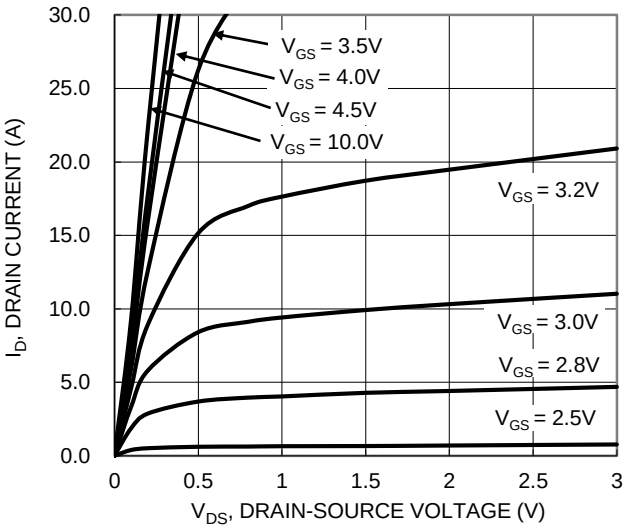


Figure 1. Typical Output Characteristic

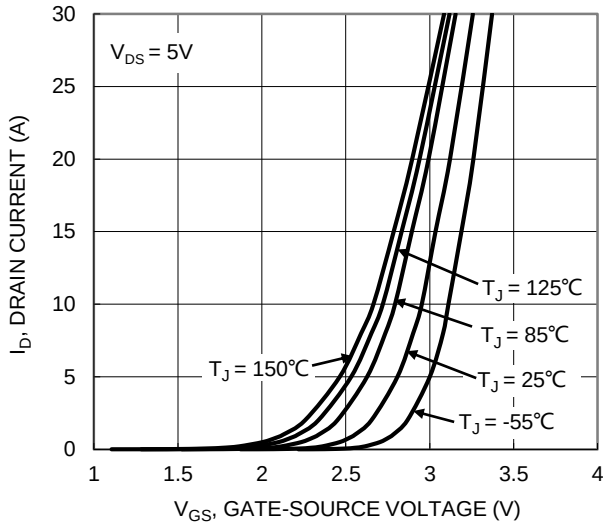


Figure 2. Typical Transfer Characteristic

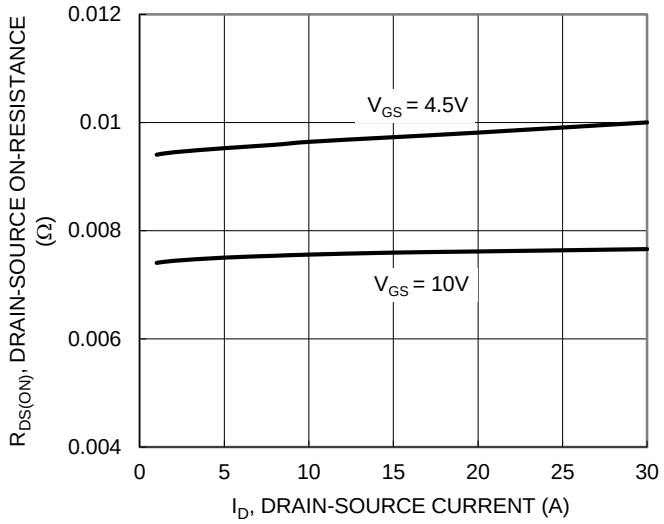


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

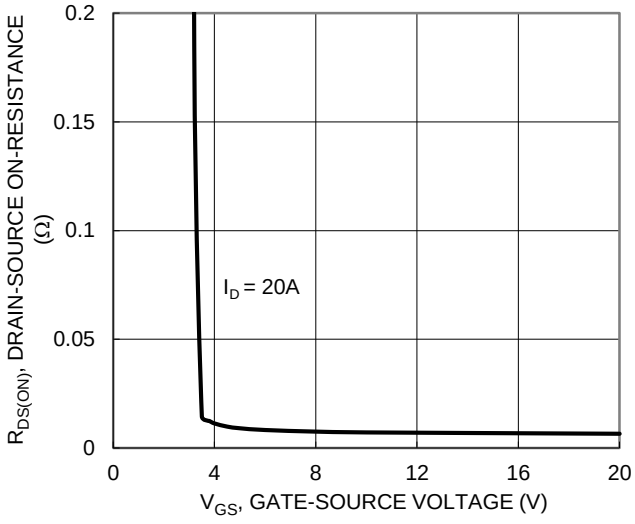


Figure 4. Typical Transfer Characteristic

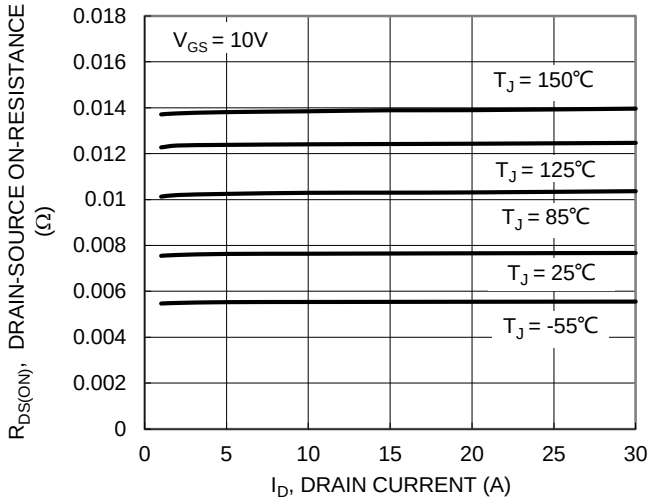


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

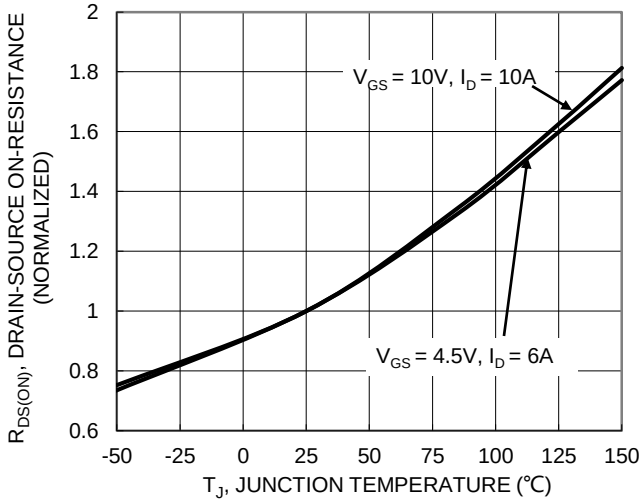


Figure 6. On-Resistance Variation with Junction Temperature

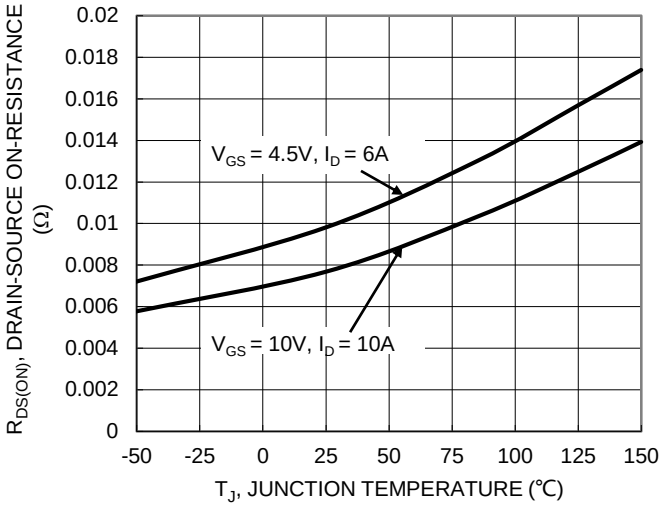


Figure 7. On-Resistance Variation with Junction Temperature

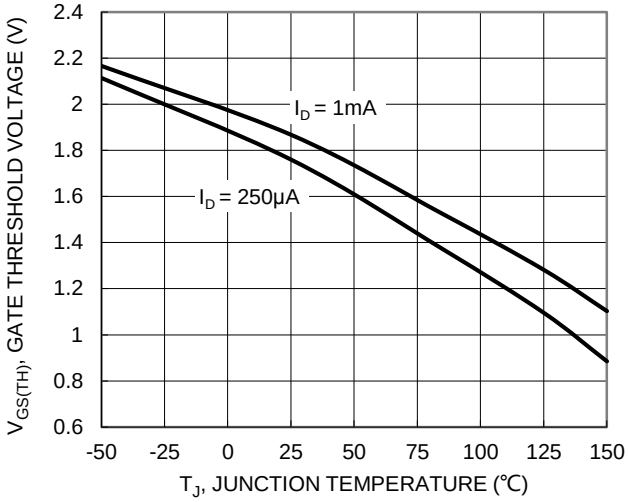


Figure 8. Gate Threshold Variation vs. Junction Temperature

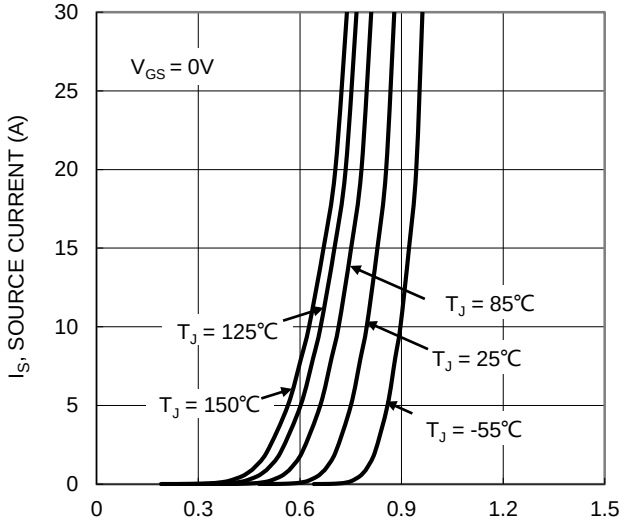


Figure 9. Diode Forward Voltage vs. Current

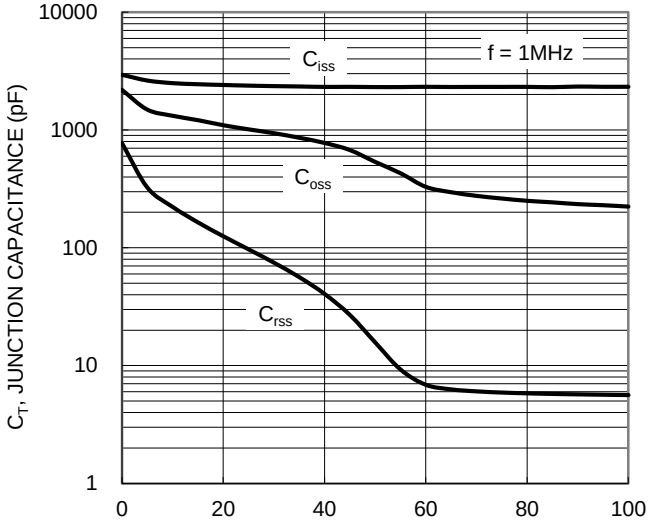


Figure 10. Typical Junction Capacitance

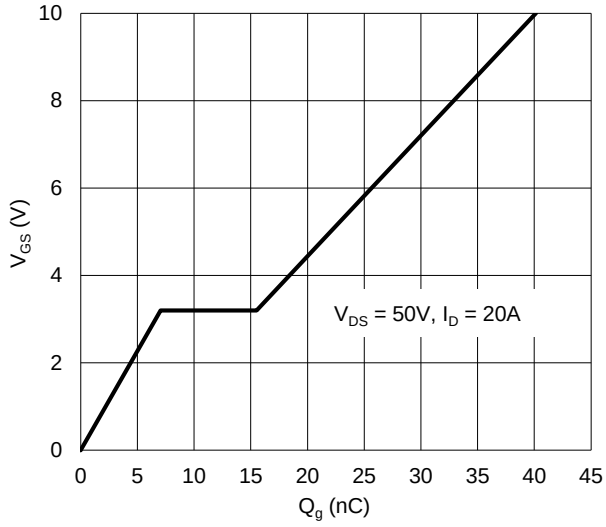


Figure 11. Gate Charge

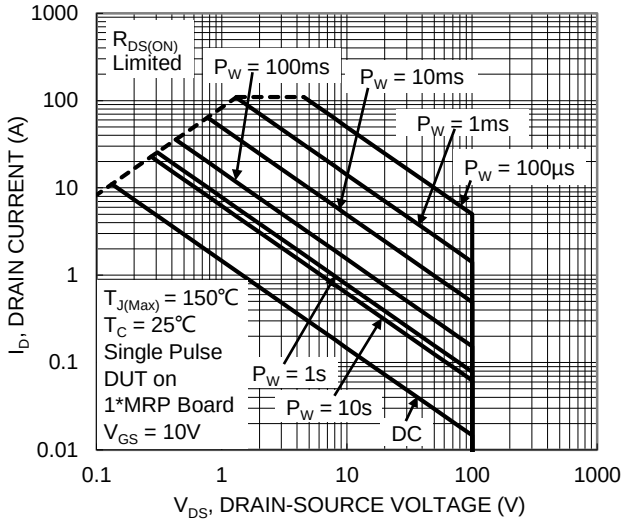


Figure 12. SOA, Safe Operation Area

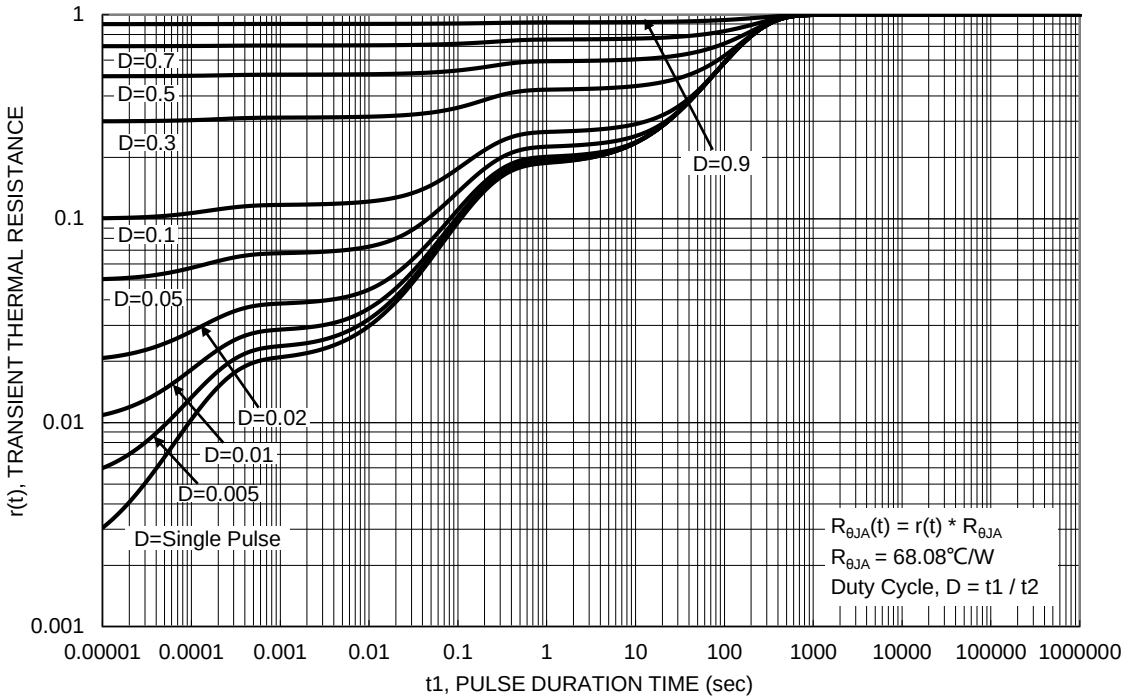
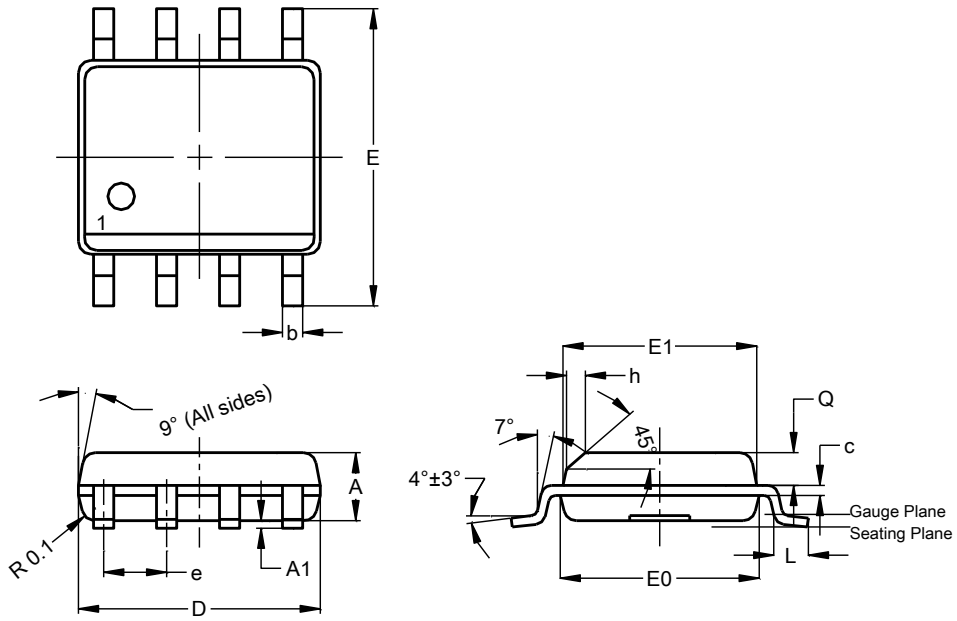


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

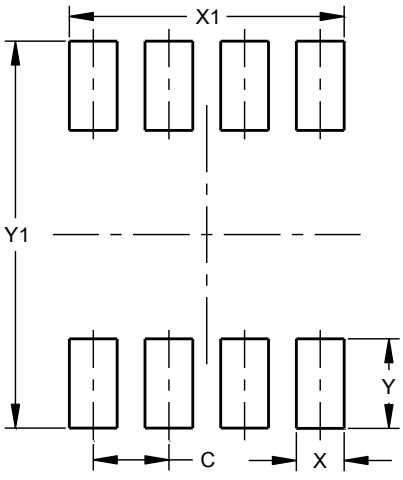


SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	--	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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