

Maximum Ratings $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	7.3 5.8	А
	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	9.2 7.3	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2.5	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	45	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	22	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	24	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	T _A = +25°C	P_{D}	1.1	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	119	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	75		
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	P_{D}	1.6	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	78	°C/W	
mermar Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	49		
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	13.5		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	<u> </u>	l.		l			
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μΑ	V _{DS} = 30V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.4	-	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	В	-	-	20	mΩ	$V_{GS} = 10V, I_D = 11A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	24	mΩ	$V_{GS} = 4.5V, I_D = 9A$	
Diode Forward Voltage	V _{SD}	-	0.70	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	-	1415	-		V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	C _{oss}	-	119	-	pF		
Reverse Transfer Capacitance	C _{rss}	-	82	-			
Gate Resistance	R_{g}	-	2.6	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	11.3	-			
Total Gate Charge (V _{GS} = 10V)	Qg	-	25.1	-	nC	V _{DS} = 15V, I _D = 12A	
Gate-Source Charge	Q _{gs}	-	3.5	-	nc		
Gate-Drain Charge	Q_{gd}	-	3.6	-			
Turn-On Delay Time	t _{D(ON)}	-	4.8	-		$V_{DD} = 15V, V_{GS} = 10V,$ $R_L = 1.25\Omega, R_G = 3\Omega$	
Turn-On Rise Time	t _R	-	16.5	-			
Turn-Off Delay Time	t _{D(OFF)}	-	26.1	-	ns		
Turn-Off Fall Time	t _F	_	5.6	-			
Reverse Recovery Time	t _{RR}	-	12.3	-	ns	1 404 41/41 5004/55	
Reverse Recovery Charge	Q _{rr}	-	10.4	-	nC	$I_F = 12A$, di/dt = 500A/ μ s	

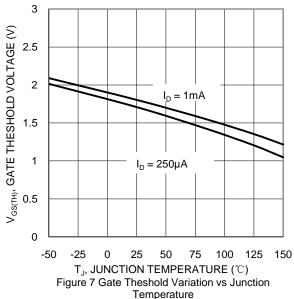
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1in. square copper plate.
 7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}$ C.
- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

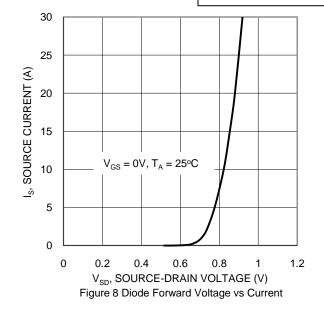
DMN3016LDN 30.0 30 GS = 3.5V V_{DS}= 5.0V 25.0 25 = 4.0V $V_{GS} = 3.0V$ ID, DRAIN CURRENT (A) ID, DRAIN CURRENT (A) 20.0 20 15.0 15 $T_A = 150^{\circ}C$ $V_{GS} = 10.0V$ 10.0 10 = 125°C = 25°C 5.0 5 $V_{GS} = 2.5V$ $= 85^{\circ}C$ = -55°C $V_{GS} = 2.2V$ 0.0 0 0 0.5 0 0.5 1.5 2 2.5 3 3.5 V_{DS}, DRAIN-SOURCE VOLTAGE (V) V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2 Typical Transfer Characteristic Figure 1 Typical Output Characteristic 0.03 0.03 $V_{GS} = 4.5V$ 0.025 R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (Ω) T_A = 150°C R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (Ω) $T_A = 125^{\circ}C$ 0.02 0.02 0.015 $V_{GS} = 4.5V$ $T_{\Delta} = 85^{\circ}C$ 0.01 0.01 $T_A = 25^{\circ}C$ $V_{GS} = 10V$ $T_A = -55$ °C 0.005 0 0 5 15 10 20 10 20 25 30 0 5 15 25 30 0 I_D, DRAIN-SOURCE CURRENT (A) I_D, DRAIN CURRENT (A) Figure 3 Typical On-Resistance vs Drain Current Figure 4 Typical On-Resistance vs Drain Current and Gate Voltage and Temperature 0.024 1.8 0.02 R_{DS(ON)}, DRAIN-SOURCE ON-ESISTANCE (NORMALIZED) 0.016 $V_{GS} = 4.5V, I_{D} = 5A$ $V_{GS} = 4.5V,$ I_D =5A 0.012 $V_{GS} = 10V$, $I_D = 10A$ 0.008 $V_{GS} = 10V, I_{D} = 10A$ 0.004 0.6 0 -50 0 25 50 75 100 125 150 0 25 50 75 100 125 T_J, JUNCTION TEMPERATURE (°C) T_J, JUNCTION TEMPERATURE (°C) Figure 6 On-Resistance Variation with Temperature Figure 5 On-Resistance Variation with

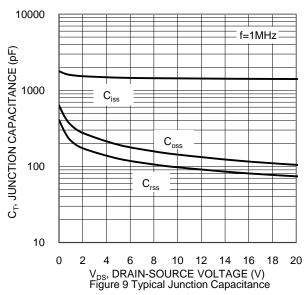
Temperature

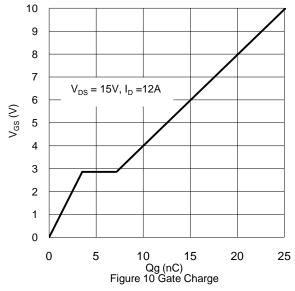


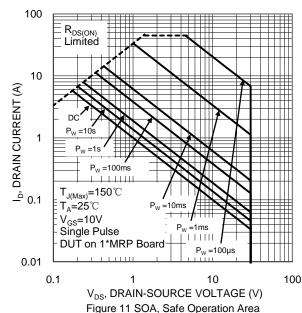
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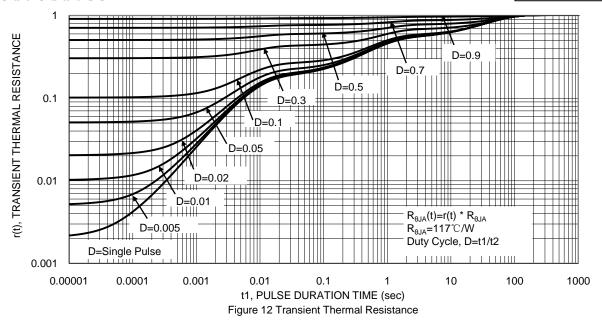








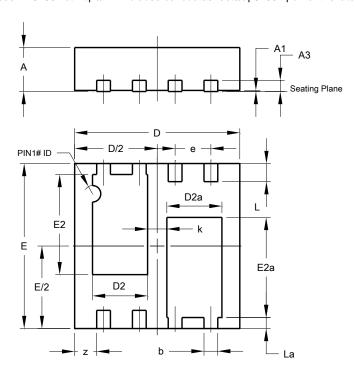






Package Outline Dimensions

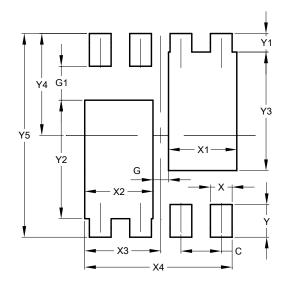
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	V-DFN3030-8 (Type J)					
Dim	Min	Max	Тур			
Α	0.77	0.83	0.80			
A1	0.00	0.05	0.02			
А3	0.203 BSC					
b	0.20	0.30	0.25			
D	2.95	3.050	3.00			
D2	0.90	1.10	1.00			
D2a	0.90	1.10	1.00			
Е	2.95	3.050	3.00			
E2	1.72	1.92	1.82			
E2a	1.72	1.92	1.82			
е	0.65BSC					
L	0.27	0.38	0.33			
La	0.15	0.25	0.20			
k	0.35 TYP					
Z	0.40 BSC					
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value			
Dillielisions	(in mm)			
С	0.650			
G	0.250			
G1	0.550			
X	0.350			
X1	1.100			
X2	1.100			
Х3	1.225			
X4	2.375			
Υ	0.530			
Y1	0.300			
Y2	1.920			
Y3	1.920			
Y4	1.650			
Y5	3.300			



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