

# 

Characteristic			Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	20	V	
Gate-Source Voltage			$V_{GSS}$	±12	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = +25 °C T <sub>A</sub> = +70 °C	I <sub>D</sub>	10.5 8.4	А
	t<10s	T <sub>A</sub> = +25 °C T <sub>A</sub> = +70 °C	I <sub>D</sub>	13.4 10.7	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 2.5V	Steady State	T <sub>A</sub> = +25 °C T <sub>A</sub> = +70 °C	I <sub>D</sub>	8.5 6.8	Α
	t<10s	T <sub>A</sub> = +25 °C T <sub>A</sub> = +70 °C	I <sub>D</sub>	10.9 8.7	А
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	6.0	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I <sub>DM</sub>	45.0	Α
Avalanche Current (Note 7) L = 0.1mH			I <sub>AS</sub>	7.8	Α
Avalanche Energy (Note 7) L = 0.1mH			E <sub>AS</sub>	3.0	mJ

# Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)		$P_{D}$	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	82	°C/W
Thermal nesistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	48	°C/W
Total Power Dissipation (Note 6)		$P_{D}$	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	n	60	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	37	°C/W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	6.4	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to 150	°C

## **Electrical Characteristics** (@T<sub>A</sub> = +25 °C, unless otherwise specified.)

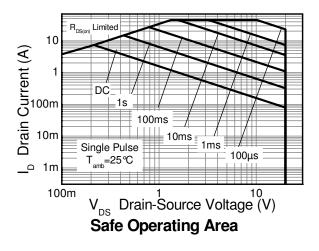
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	1.0	μΑ	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	0.7	_	1.3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	_	12.5	mΩ	$V_{GS} = 4.5V, I_D = 9.4A$	
Static Drain-Source On-nesistance	R <sub>DS(ON)</sub>	_	_	19	11122	$V_{GS} = 2.5V, I_D = 8.3A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.3	V	$V_{GS} = 0V, I_{S} = 1.3A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>	_	1,000	_		V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	$C_{oss}$	_	166	_	pF		
Reverse Transfer Capacitance	$C_{rss}$	_	158	_			
Gate Resistance	$R_g$	_	1.51	3.2	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 2.5V)	$Q_g$	_	7.0	_		V 40V I 0.44	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qq	_	11.6	_	nC		
Gate-Source Charge	Q <sub>gs</sub>	_	2.7	_	IIC	$V_{DS} = 10V, I_{D} = 9.4A$	
Gate-Drain Charge	$Q_{gd}$	_	3.4	_			
Turn-On Delay Time	t <sub>D(on)</sub>	_	11.67	_		$V_{GS} = 4.5V, V_{DS} = 10V,$ $R_{G} = 6\Omega, I_{D} = 1A$	
Turn-On Rise Time	t <sub>r</sub>	_	12.49	_			
Turn-Off Delay Time	t <sub>D(off)</sub>	_	35.89	_	ns		
Turn-Off Fall Time	t <sub>f</sub>	_	12.33	_			
Reverse Recovery Time	T <sub>rr</sub>	_	10.8	_	ns	1 40A di/db 500A/c	
Reverse Recovery Charge	Q <sub>rr</sub>	_	5.8	_	nC	$I_F = 12A$ , di/dt = 500A/ $\mu$ s	

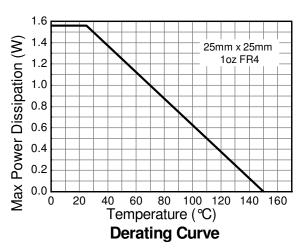
Notes:

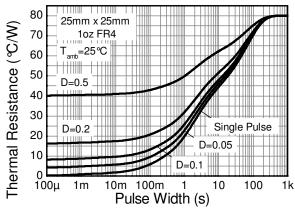
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7.  $I_{AS}$  and  $E_{AS}$  rating are based on low frequency and duty cycles to keep  $T_J = 25 \,^{\circ}\text{C}$ .
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.

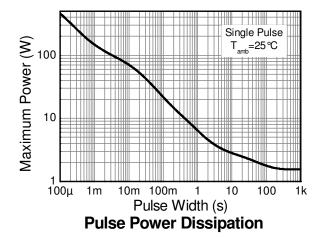


### **Thermal Characteristics**

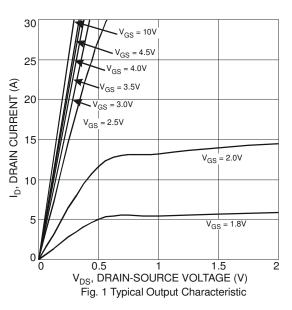


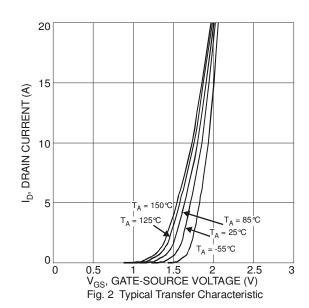




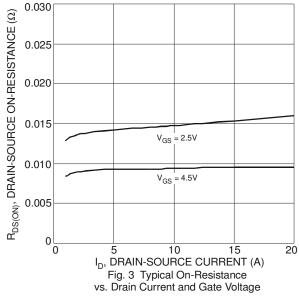


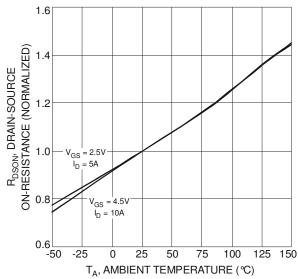
**Transient Thermal Impedance** 

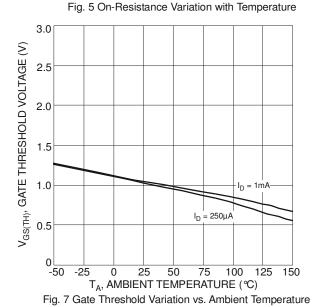


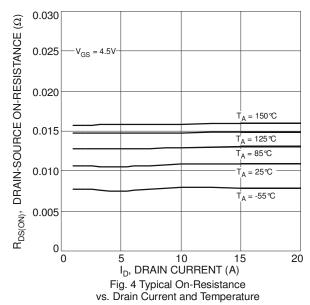












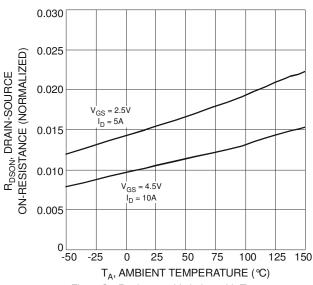


Fig. 6 On-Resistance Variation with Temperature

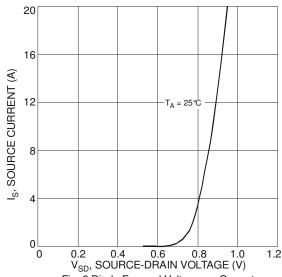
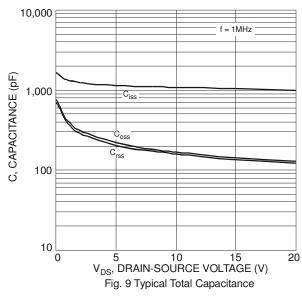
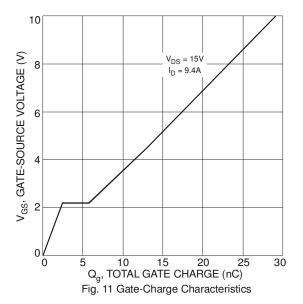
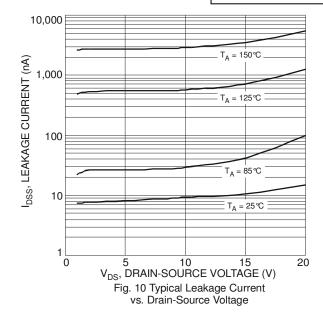


Fig. 8 Diode Forward Voltage vs. Current





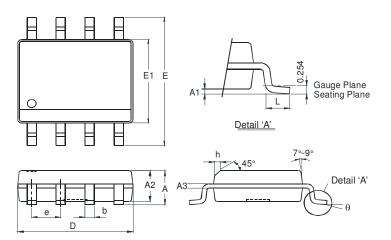






### **Package Outline Dimensions**

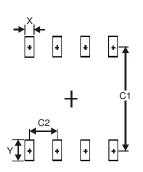
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	<b>e</b> 1.27 Typ			
h	1	0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



**SO-8** 

<b>Dimensions</b>	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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