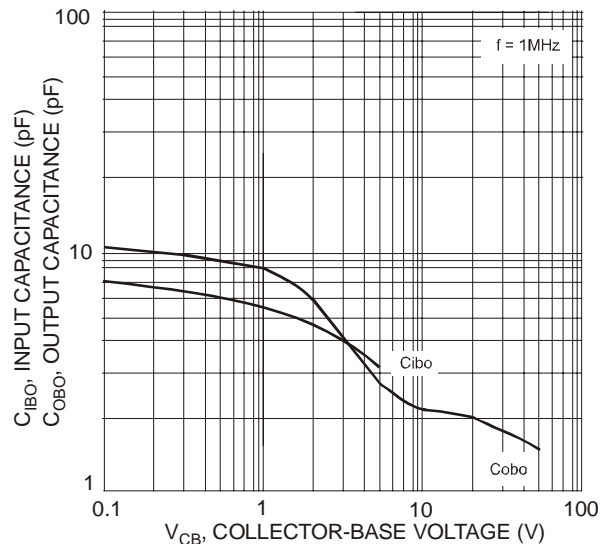
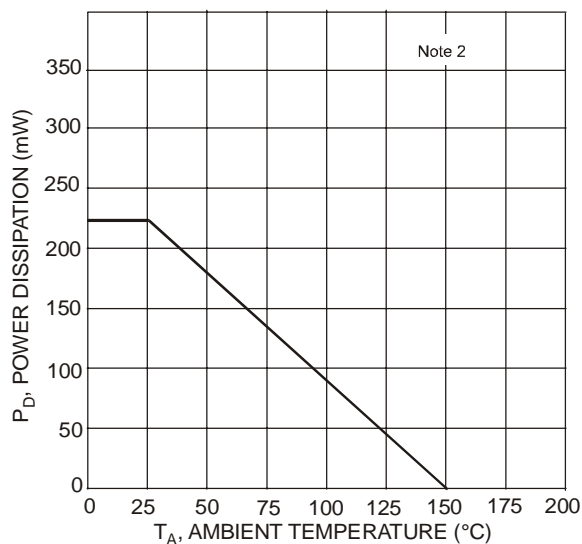


Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)					
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-40	—	V	I _C = -10μA, I _E = 0
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-40	—	V	I _C = -1.0mA, I _B = 0
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5.0	—	V	I _E = -10μA, I _C = 0
Collector Cutoff Current	I _{CEX}	—	-50	nA	V _{CE} = -30V, V _{EB(OFF)} = -3.0V
Base Cutoff Current	I _{BL}	—	-50	nA	V _{CE} = -30V, V _{EB(OFF)} = -3.0V
ON CHARACTERISTICS (Note 6)					
DC Current Gain (Note 7)	h _{FE}	60	—	—	I _C = -100μA, V _{CE} = -1.0V
		80	—		I _C = -1.0mA, V _{CE} = -1.0V
		100	300		I _C = -10mA, V _{CE} = -1.0V
		60	—		I _C = -50mA, V _{CE} = -1.0V
		30	—		I _C = -100mA, V _{CE} = -1.0V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	—	-0.25 -0.40	V	I _C = -10mA, I _B = -1.0mA I _C = -50mA, I _B = -5.0mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	-0.65 —	-0.85 -0.95	V	I _C = -10mA, I _B = -1.0mA I _C = -50mA, I _B = -5.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{obo}	—	4.5	pF	V _{CB} = -5.0V, f = 1.0MHz, I _E = 0
Input Capacitance	C _{ibo}	—	10	pF	V _{EB} = -0.5V, f = 1.0MHz, I _C = 0
Input Impedance	h _{ie}	2.0	12	kΩ	V _{CE} = 10V, I _C = 1.0mA, f = 1.0kHz
Voltage Feedback Ratio	h _{re}	0.1	10	x 10 ⁻⁴	
Small Signal Current Gain	h _{fe}	100	400	—	
Output Admittance	h _{oe}	3.0	60	μS	V _{CE} = -20V, I _C = -10mA, f = 100MHz
Current Gain-Bandwidth Product	f _T	250	—	MHz	
Noise Figure	NF	—	4.0	dB	V _{CE} = -5.0V, I _C = -100μA, R _S = 1.0kΩ, f = 1.0kHz
SWITCHING CHARACTERISTICS					
Delay Time	t _d	—	35	ns	V _{CC} = -3.0V, I _C = -10mA, V _{BE(off)} = 0.5V, I _{B1} = -1.0mA
Rise Time	t _r	—	35	ns	
Storage Time	t _s	—	225	ns	V _{CC} = -3.0V, I _C = -10mA, I _{B1} = I _{B2} = -1.0mA
Fall Time	t _f	—	75	ns	

- Notes: 6. Short duration pulse test used to minimize self-heating effect.
7. The DC current gain, h_{FE}, is matched at I_C = -10mA and V_{CE} = -1.0V with typical matched tolerances of 1% and maximum of 2%.



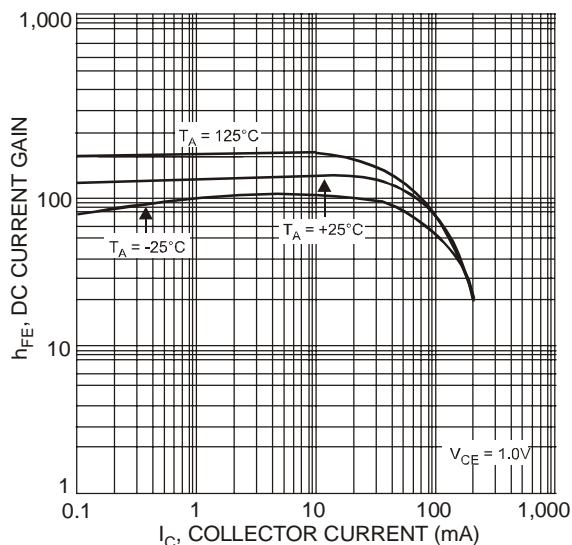


Fig. 3, Typical DC Current Gain vs. Collector Current

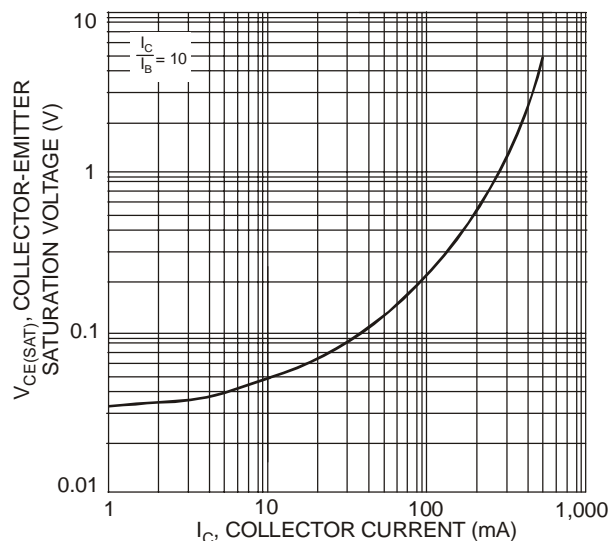


Fig. 4, Typical Collector-Emitter Saturation Voltage vs. Collector Current

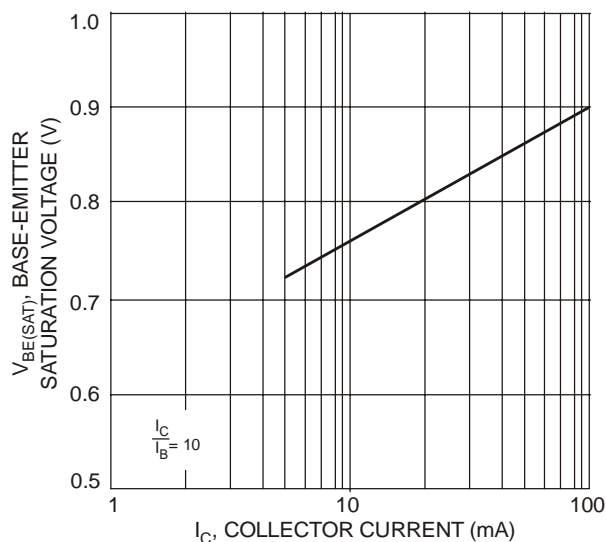


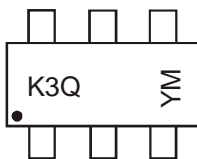
Fig. 5, Typical Base-Emitter Saturation Voltage vs. Collector Current

Ordering Information (Note 5 & 8)

Device	Packaging	Shipping
DMMT3906-7-F	SOT-26	3000/Tape & Reel

Notes: 8. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



K3Q = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: T = 2006
 M = Month ex: 9 = September

Date Code Key

Year	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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