

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	24	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	6.5 5.2	А
Continuous Drain Current (Note 5) V _{GS} = 2.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	5.6 4.5	А
Pulsed Drain Current (Note 6)			I _{DM}	70	Α

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P_{D}	0.98	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	126.5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

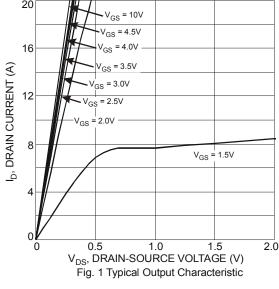
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	24	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.6	0.9	1.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
		_	11	15	- mΩ	$V_{GS} = 4.5V, I_D = 6.5A$	
Static Drain-Source On-Resistance	В	_	12	17		V _{GS} = 4V, I _D = 5.6A V _{GS} = 3.1V, I _D = 5.6A	
Static Dialii-Source Oil-Resistance	R _{DS (ON)}	_	13	18			
		_	14	20		$V_{GS} = 2.5V, I_D = 5.6A$	
Forward Transfer Admittance	Y _{fs}	_	17	_	S	$V_{DS} = 5V, I_{D} = 6.5A$	
Diode Forward Voltage	V _{SD}	_	0.6	0.9	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	1066.4			V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	132.0	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	127.1	_			
Gate Resistance	R_g	_	1.47	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Q_g	_	14.5	_	V _{GS} = 4.5V, V _{DS} = 15V, I _D = 5		
Total Gate Charge V _{GS} = 10V	Qg	_	31.3	_	nC	10)/)/ 45)/	
Gate-Source Charge	Qgs	_	2.0	_	IIC	$V_{GS} = 10V, V_{DS} = 15V,$ $I_{D} = 5.8A$	
Gate-Drain Charge	Q_{gd}	_	3.1	_			
Turn-On Delay Time	t _{D(on)}	_	3.69	_	ns	$V_{GS} = 10V, V_{DS} = 15V,$ $R_{L} = 2.1\Omega, R_{G} = 3\Omega$	
Turn-On Rise Time	t _r	_	13.43	_	ns		
Turn-Off Delay Time	t _{D(off)}	_	32.18	_	ns		
Turn-Off Fall Time	t _f	_	22.45	_	ns		

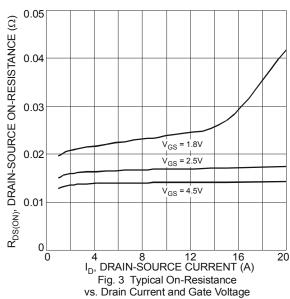
Notes:

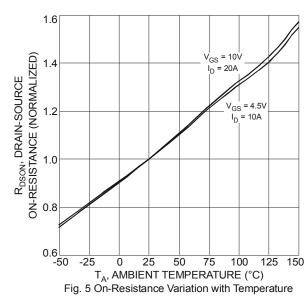
- 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided. 6. Repetitive rating, pulse width limited by junction temperature.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.

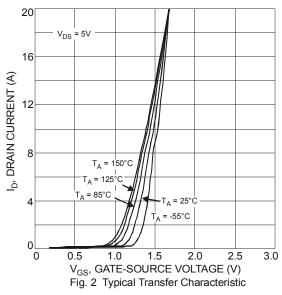


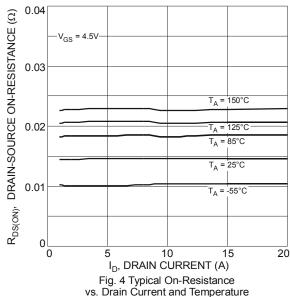












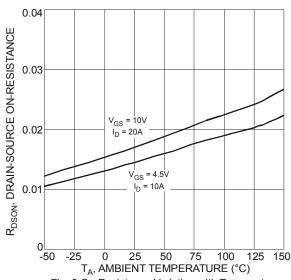
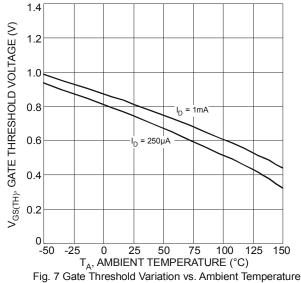
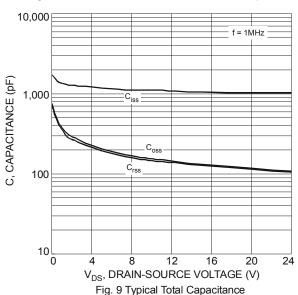


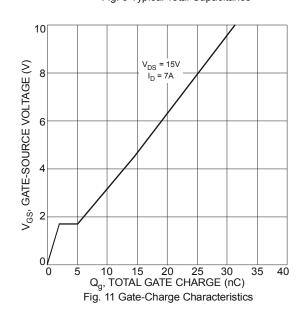
Fig. 6 On-Resistance Variation with Temperature

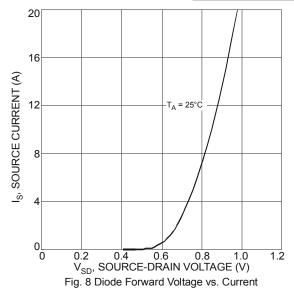


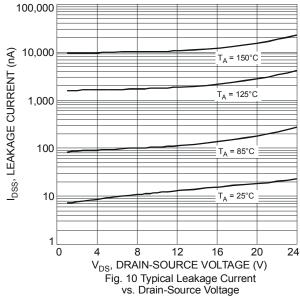


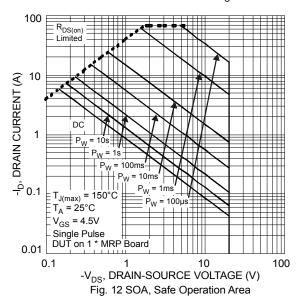




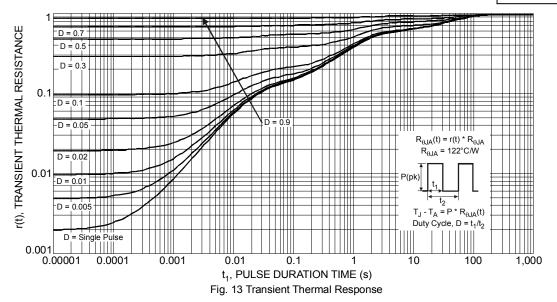






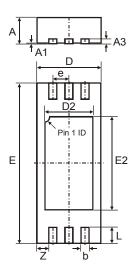






Package Outline Dimensions

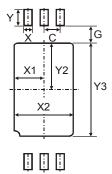
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



W-DFN5020-6					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0	0.05	0.02		
A3	-	-	0.15		
b	0.20	0.30	0.25		
D	1.90	2.10	2.00		
D2	1.40	1.60	1.50		
е	_	_	0.50		
Е	4.90	5.10	5.00		
E2	2.80	3.00	2.90		
L	0.35	0.65	0.50		
Z	_	_	0.375		
All	All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.50
G	0.35
Х	0.35
X1	0.90
X2	1.80
Υ	0.70
Y2	1.60
Y3	3.20



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