

DYNAMIC CHARACTERISTICS

APT25GP120B

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{ies}	Input Capacitance	Capacitance $V_{GE} = 0V, V_{CE} = 25V$ $f = 1 \text{ MHz}$		2090		pF
C_{oes}	Output Capacitance			200		
C_{res}	Reverse Transfer Capacitance			40		
V_{GEP}	Gate-to-Emitter Plateau Voltage	Gate Charge $V_{GE} = 15V$ $V_{CE} = 600V$ $I_C = 25A$		7.5		V
Q_g	Total Gate Charge ^③			110		nC
Q_{ge}	Gate-Emitter Charge			15		
Q_{gc}	Gate-Collector ("Miller") Charge			50		
RBSOA	Reverse Bias Safe Operating Area	$T_J = 150^\circ C, R_G = 5\Omega, V_{GE} = 15V, L = 100\mu H, V_{CE} = 960V$	90			A
$t_{d(on)}$	Turn-on Delay Time	Inductive Switching (25°C) $V_{CLAMP(Peak)} = 600V$ $V_{GE} = 15V$ $I_C = 25A$ $R_G = 5\Omega$ $T_J = +25^\circ C$		12		ns
t_r	Current Rise Time			14		
$t_{d(off)}$	Turn-off Delay Time			70		
t_f	Current Fall Time			39		
E_{on1}	Turn-on Switching Energy ^④			500		μJ
E_{on2}	Turn-on Switching Energy (Diode) ^⑤			1092		
E_{off}	Turn-off Switching Energy ^⑥			438		
$t_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C) $V_{CLAMP(Peak)} = 600V$ $V_{GE} = 15V$ $I_C = 25A$ $R_G = 5\Omega$ $T_J = +125^\circ C$		12		ns
t_r	Current Rise Time			14		
$t_{d(off)}$	Turn-off Delay Time			109		
t_f	Current Fall Time			88		
E_{on1}	Turn-on Switching Energy ^④			500		μJ
E_{on2}	Turn-on Switching Energy (Diode) ^⑤			1577		
E_{off}	Turn-off Switching Energy ^⑥			1187		

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case (IGBT)			.30	$^\circ C/W$
$R_{\theta JC}$	Junction to Case (DIODE)			N/A	
W_T	Package Weight			5.90	gm

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② For Combi devices, I_{ces} includes both IGBT and FRED leakages
- ③ See MIL-STD-750 Method 3471.
- ④ E_{on1} is the clamped inductive turn-on-energy of the IGBT only, without the effect of a commutating diode reverse recovery current adding to the IGBT turn-on loss. (See Figure 24.)
- ⑤ E_{on2} is the clamped inductive turn-on energy that includes a commutating diode reverse recovery current in the IGBT turn-on switching loss. (See Figures 21, 22.)
- ⑥ E_{off} is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1. (See Figures 21, 23.)
- APT Reserves the right to change, without notice, the specifications and information contained herein.**

TYPICAL PERFORMANCE CURVES

APT25GP120B

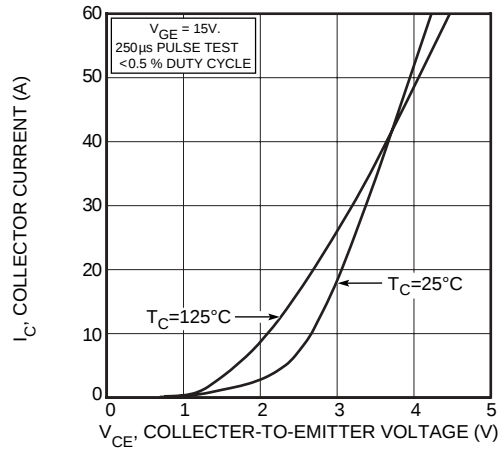


FIGURE 1, Output Characteristics ($V_{GE} = 15V$)

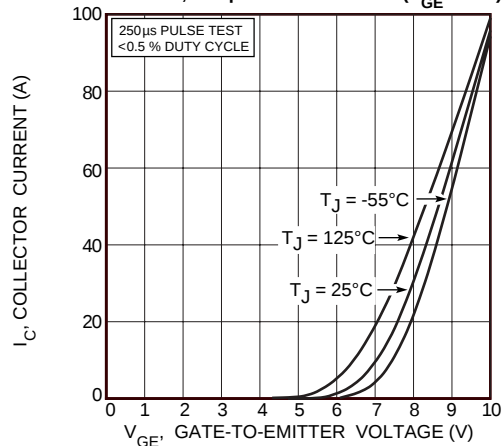


FIGURE 3, Transfer Characteristics

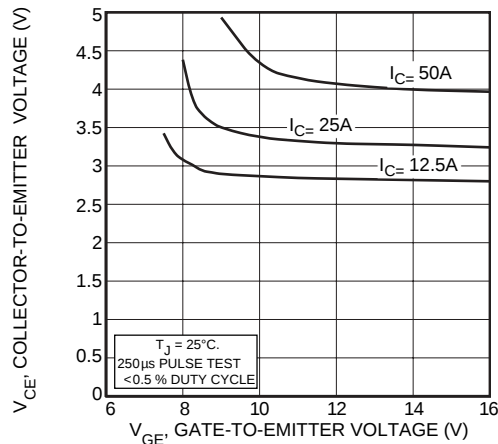


FIGURE 5, On State Voltage vs Gate-to-Emitter Voltage

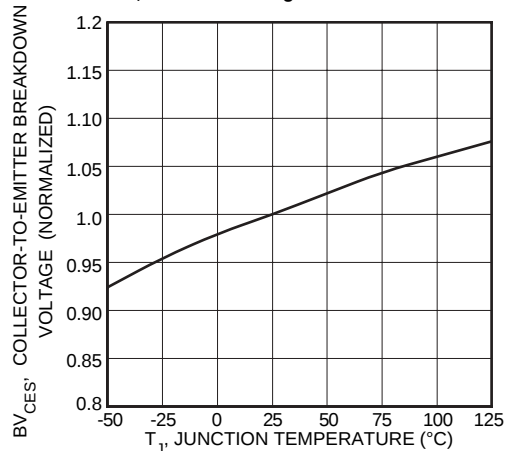


FIGURE 7, Breakdown Voltage vs. Junction Temperature

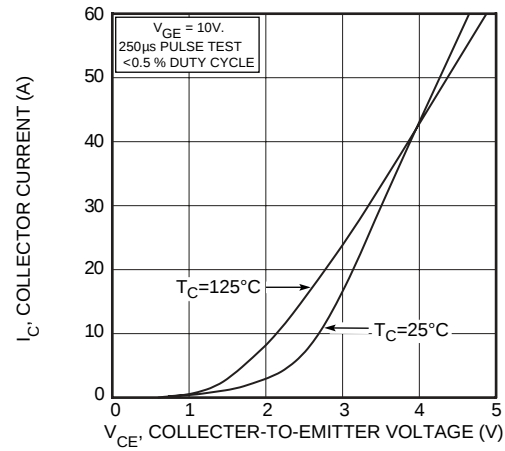


FIGURE 2, Output Characteristics ($V_{GE} = 10V$)

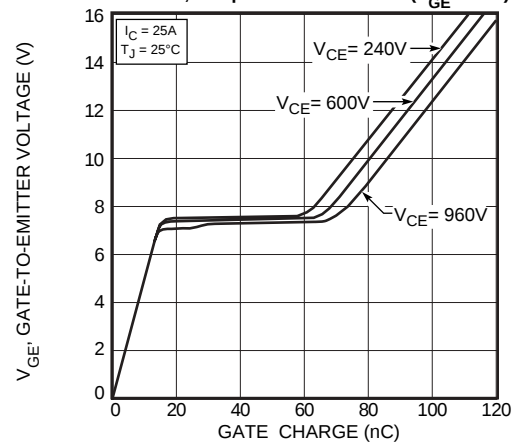


FIGURE 4, Gate Charge

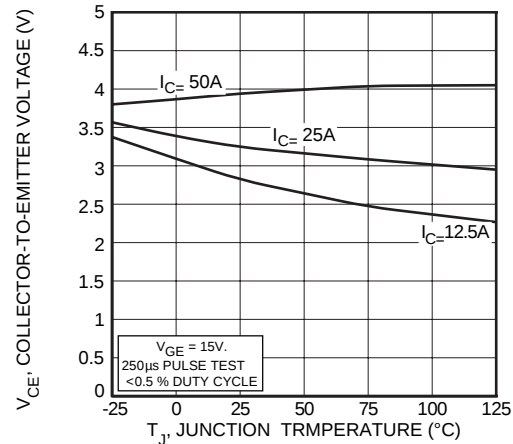


FIGURE 6, On State Voltage vs Junction Temperature

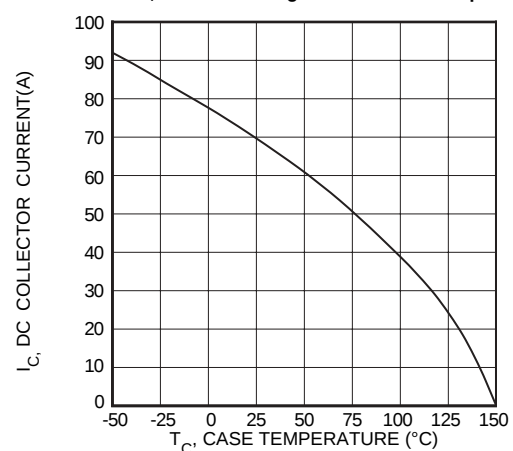


FIGURE 8, DC Collector Current vs Case Temperature

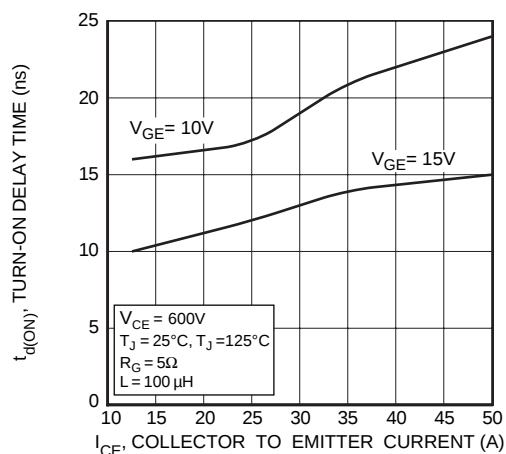


FIGURE 9, Turn-On Delay Time vs Collector Current

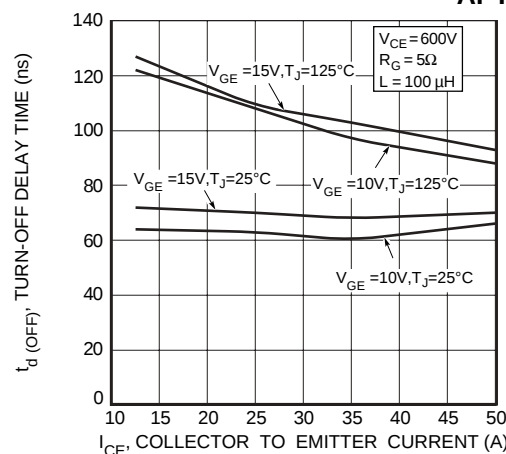


FIGURE 10, Turn-Off Delay Time vs Collector Current

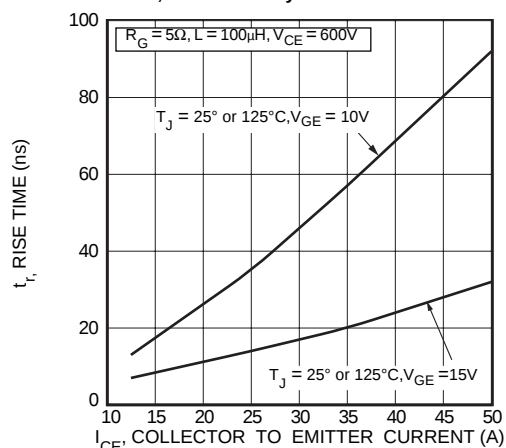


FIGURE 11, Current Rise Time vs Collector Current

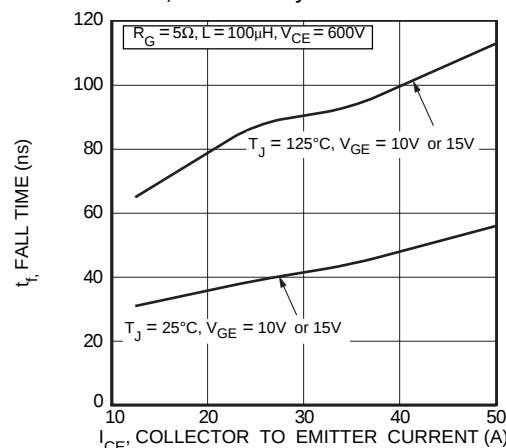


FIGURE 12, Current Fall Time vs Collector Current

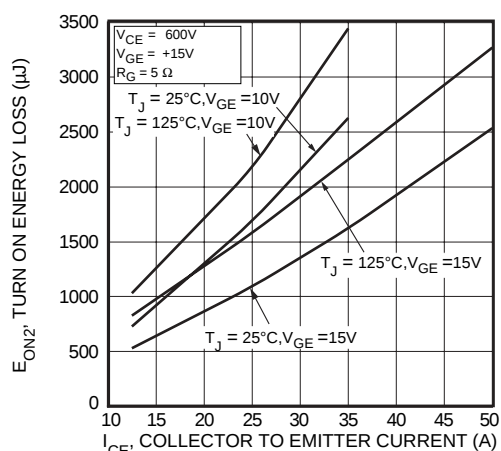


FIGURE 13, Turn-On Energy Loss vs Collector Current

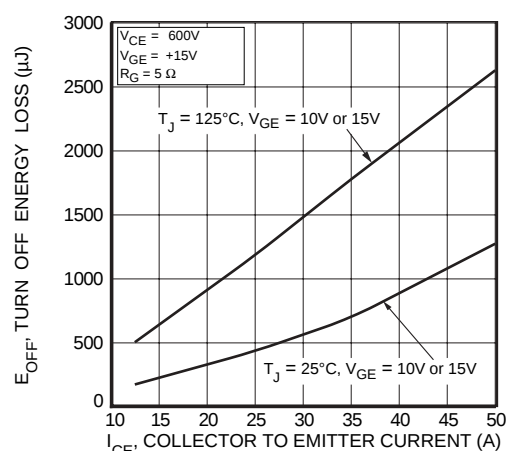


FIGURE 14, Turn Off Energy Loss vs Collector Current

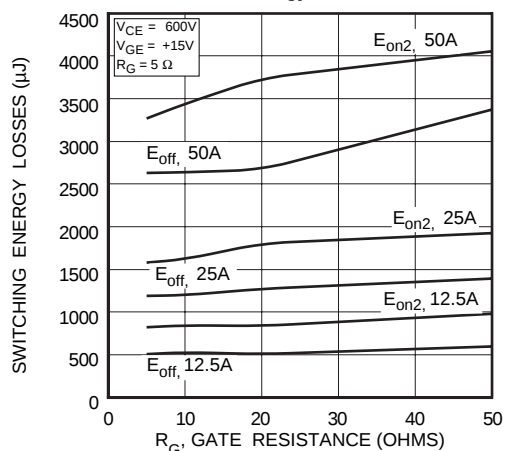


FIGURE 15, Switching Energy Losses vs. Gate Resistance

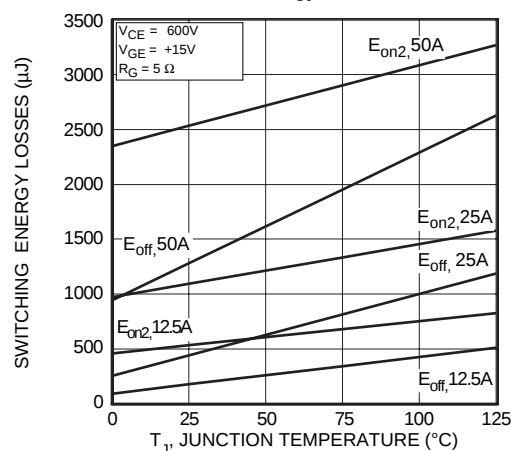


FIGURE 16, Switching Energy Losses vs Junction Temperature

TYPICAL PERFORMANCE CURVES

APT25GP120B

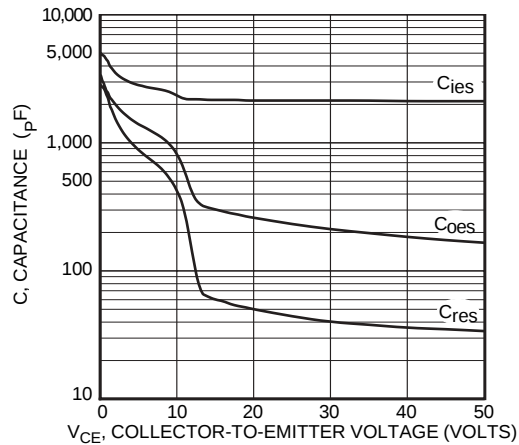


Figure 17, Capacitance vs Collector-To-Emitter Voltage

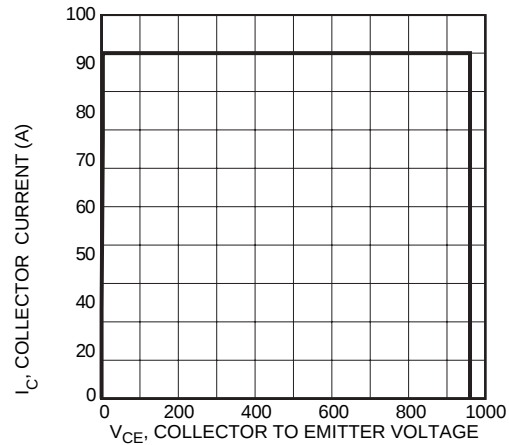


Figure 18, Minimum Switching Safe Operating Area

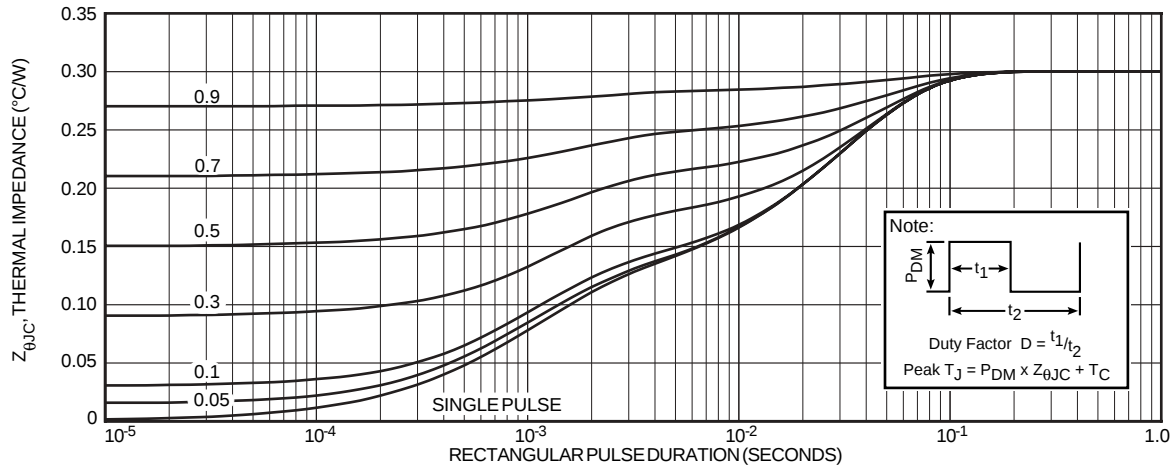


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

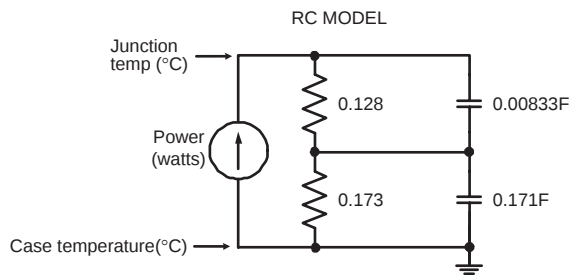


FIGURE 19B, TRANSIENT THERMAL IMPEDANCE MODEL

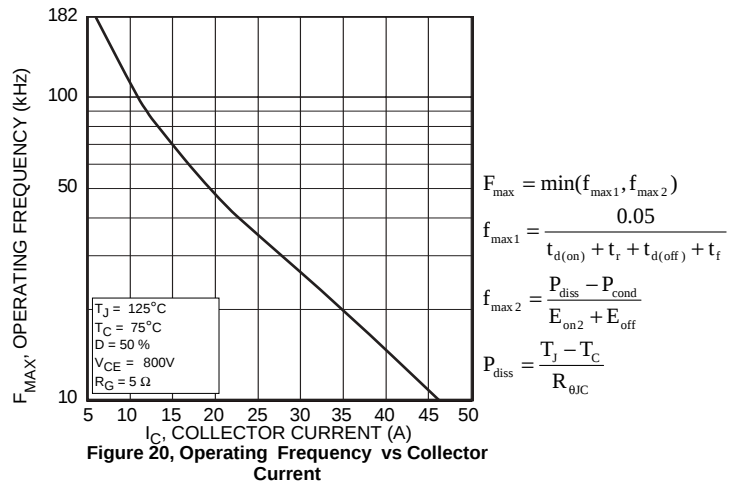


Figure 20, Operating Frequency vs Collector Current

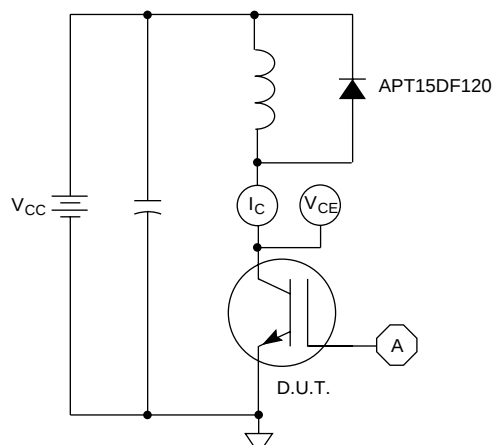


Figure 21, Inductive Switching Test Circuit

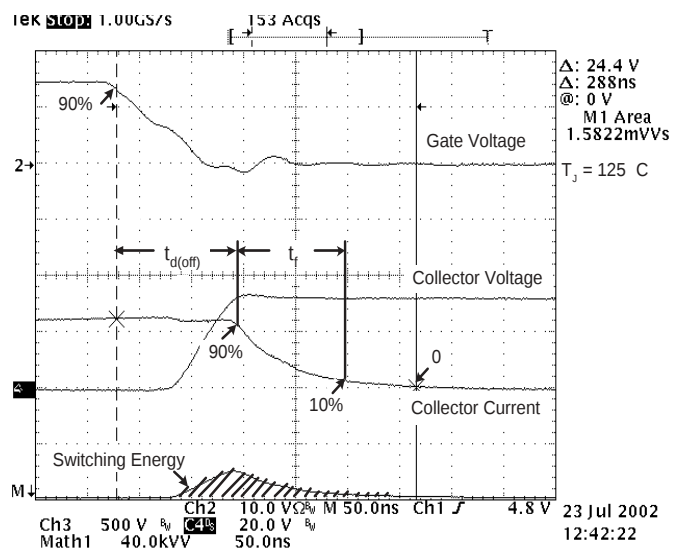


Figure 23, Turn-off Switching Waveforms and Definitions

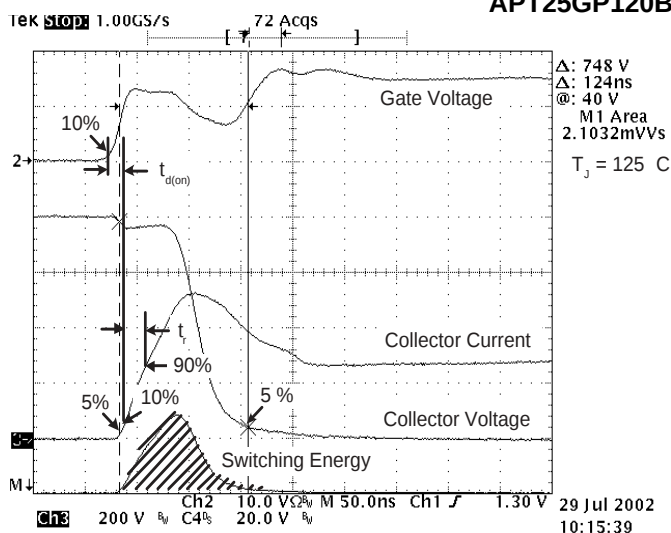
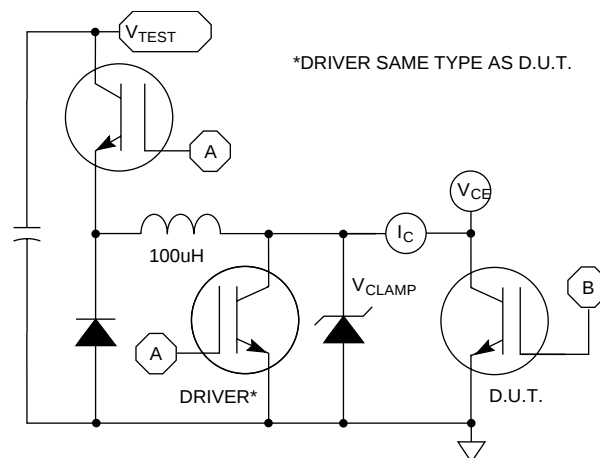
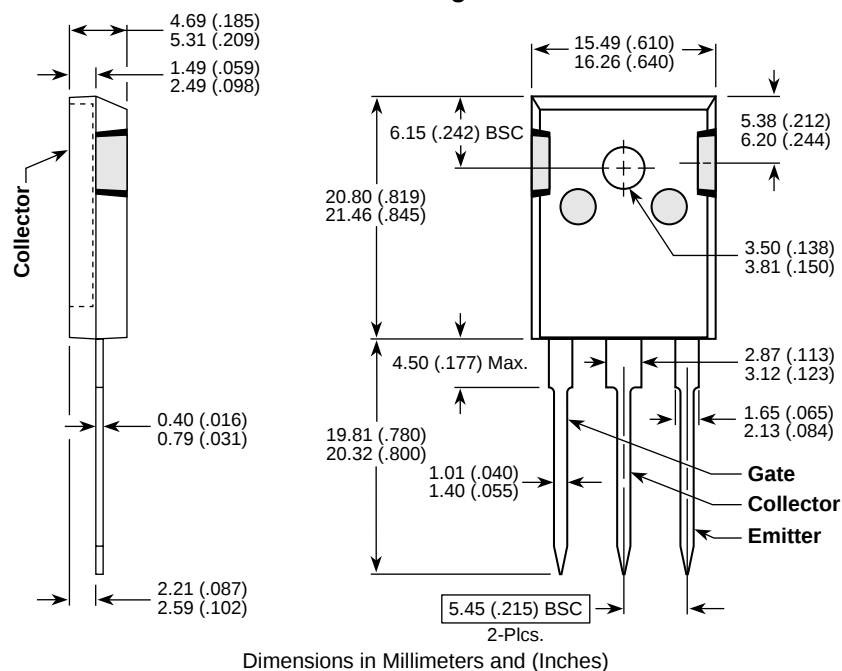


Figure 22, Turn-on Switching Waveforms and Definitions

Figure 24, E_{ON1} Test Circuit

T0-247 Package Outline



Dimensions in Millimeters and (Inches)

APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522

5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. US and Foreign patents pending. All Rights Reserved.