

# ADM8828/ADM8829—SPECIFICATIONS (V<sub>IN</sub> = +5 V, C1, C2 = 1 $\mu$ F,<sup>1</sup> T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub> unless otherwise noted)

Parameter	Min	Typ	Max	Units	Test Conditions/Comments
Input Voltage, IN	1.5		5.5	V	R <sub>L</sub> = 10 k $\Omega$
Supply Current		600	1000	$\mu$ A	Unloaded
Output Current	25			mA	
Output Resistance	18		28	$\Omega$	I <sub>L</sub> = 5 mA
Output Ripple	25			mV p-p	I <sub>L</sub> = 5 mA
	130			mV p-p	I <sub>L</sub> = 25 mA
Charge-Pump Frequency	50	120	190	kHz	
	65			kHz	V <sub>IN</sub> = +2.25 V
Power Efficiency		90		%	R <sub>L</sub> = 200 $\Omega$
		87		%	R <sub>L</sub> = 1 k $\Omega$
Voltage Conversion Efficiency	99.5	99.96		%	No Load
		98		%	R <sub>L</sub> = 1 k $\Omega$
		91		%	R <sub>L</sub> = 200 $\Omega$
Shutdown Supply Current, I <sub>SHDN</sub>		0.02	2	$\mu$ A	SHDN = IN
Shutdown Input Voltage, V <sub>SHDN</sub>	2.0		0.8	V	SHDN High = Disabled
Shutdown Exit Time		175		$\mu$ s	SHDN Low = Enabled
					I <sub>L</sub> = 5 mA

## NOTES

<sup>1</sup>C1 and C2 are low ESR (<0.2  $\Omega$ ) electrolytic capacitors. High ESR will degrade performance.

Specifications subject to change without notice.

## ABSOLUTE MAXIMUM RATINGS\*

(T<sub>A</sub> = +25°C unless otherwise noted)

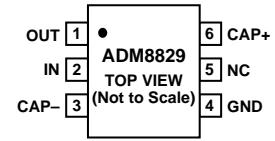
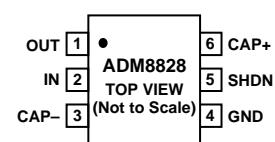
Input Voltage (IN to GND) .....	-0.3 V to +6 V
OUT to GND .....	-6.0 V to +0.3 V
OUT, IN Output Current (Continuous) .....	50 mA
Output Short Circuit Duration to GND .....	10 secs
Power Dissipation, RT-6 .....	570 mW
(Derate 8.3 mW/ $^{\circ}$ C above +70°C)	
$\theta_{JA}$ , Thermal Impedance .....	120 $^{\circ}$ C/W
Operating Temperature Range	
Industrial (A Version) .....	-40 $^{\circ}$ C to +85 $^{\circ}$ C
Storage Temperature Range .....	-65 $^{\circ}$ C to +150 $^{\circ}$ C
Lead Temperature Range (Soldering 10 sec) .....	+300 $^{\circ}$ C
Vapor Phase (70 sec) .....	+215 $^{\circ}$ C
Infrared (15 sec) .....	+220 $^{\circ}$ C
ESD Rating .....	>3500 V

\*This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## PIN FUNCTION DESCRIPTIONS

Mnemonic	Function
CAP+	Positive Charge-Pump Capacitor Terminal.
GND	Power Supply Ground.
CAP-	Negative Charge-Pump Capacitor Terminal.
OUT	Output, Negative Voltage.
SHDN	Shutdown Control Input. This input, when high, is used to disable the charge pump thereby reducing the power consumption.
IN	Positive Power Supply Input.

## PIN CONFIGURATIONS



NC = NO CONNECT

## Typical Performance Characteristics—ADM8828/ADM8829

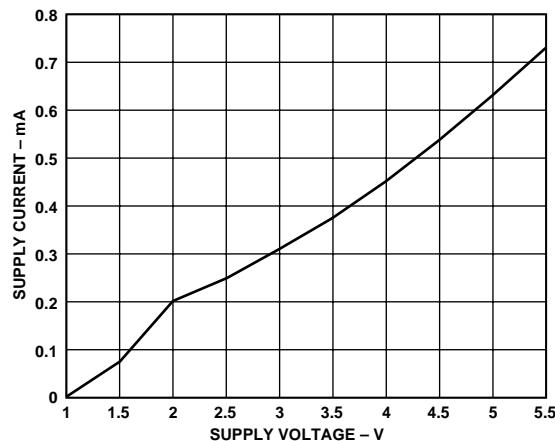


Figure 2. Power Supply Current vs. Voltage

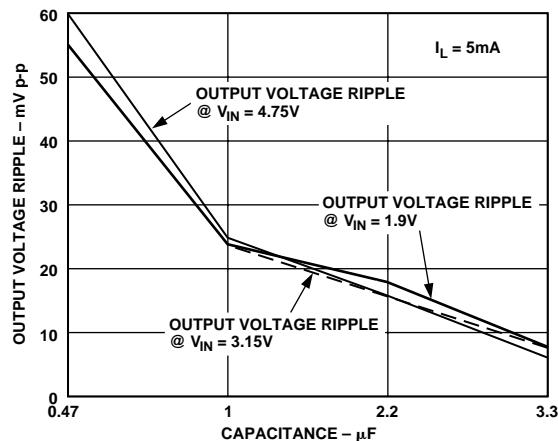


Figure 5. Output Voltage Ripple vs. Capacitance

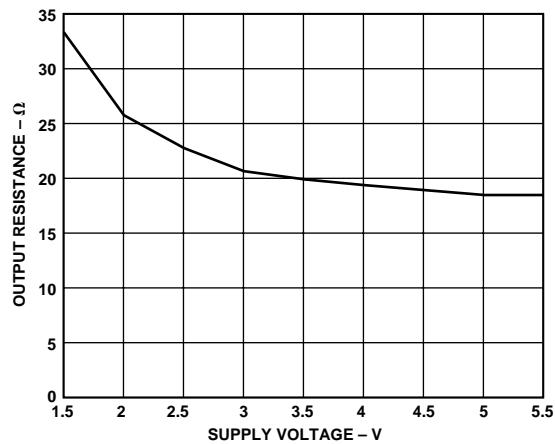


Figure 3. Output Source Resistance vs. Supply Voltage

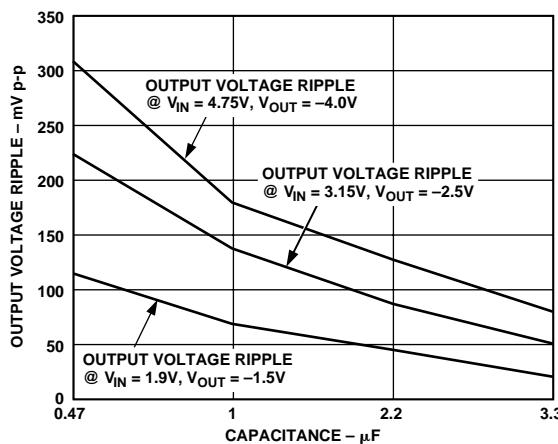


Figure 6. Output Voltage Ripple vs. Capacitance

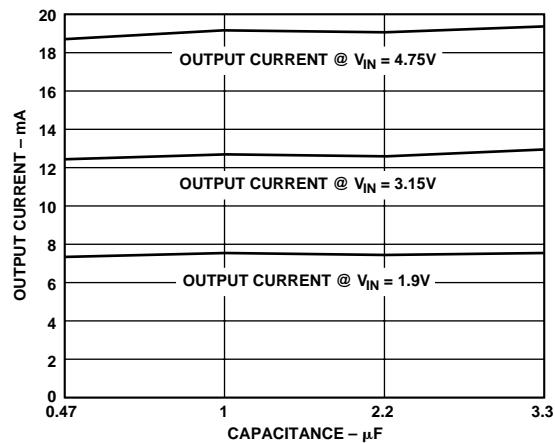


Figure 4. Output Current vs. Capacitance

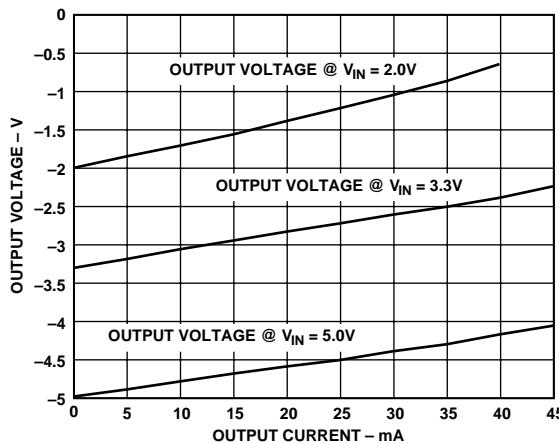


Figure 7. Output Voltage vs. Output Current

# ADM8828/ADM8829

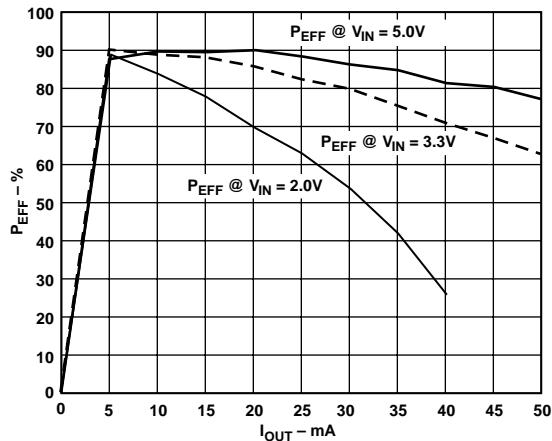


Figure 8. Power Efficiency vs. Output Current

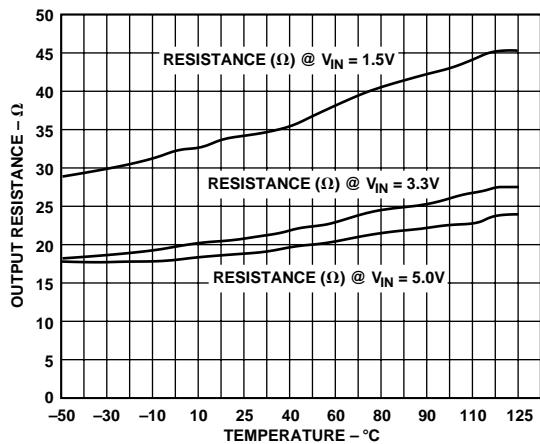


Figure 9. Output Resistance vs. Temperature

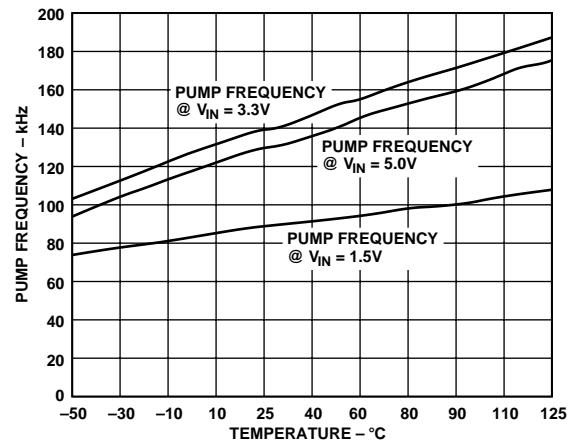


Figure 10. Charge Pump Frequency vs. Temperature

**GENERAL INFORMATION**

The ADM8828/ADM8829 is a switched capacitor voltage converter that can be used to invert the input supply voltage.

The voltage conversion task is achieved using a switched capacitor technique using two external charge storage capacitors. An on-chip oscillator and switching network transfers charge between the charge storage capacitors. The basic principle behind the voltage conversion scheme is illustrated below.

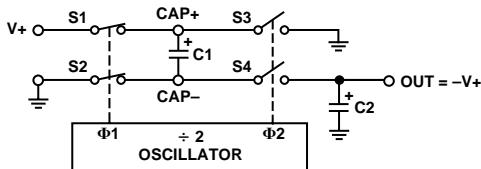


Figure 11. Voltage Inversion Principle

An oscillator generating antiphase signals  $\phi_1$  and  $\phi_2$  controls switches S1, S2 and S3, S4. During  $\phi_1$ , switches S1 and S2 are closed while S3 and S4 are open, thereby charging C1 up to the voltage at V+. During  $\phi_2$ , S1 and S2 open and S3 and S4 close. The positive terminal of C1 is connected to GND via S3 during this phase and the negative terminal of C1 connects to  $V_{OUT}$  via S4. The net result is voltage inversion at  $V_{OUT}$  wrt GND. Charge on C1 is transferred to C2 during  $\phi_2$ . Capacitor C2 maintains this voltage during  $\phi_1$ . The charge transfer efficiency depends on the on-resistance of the switches, the frequency at which they are being switched and also on the equivalent series resistance (ESR) of the external capacitors. For maximum efficiency, capacitors with low ESR are, therefore, recommended.

**Shutdown Input**

The ADM8828 contains a shutdown input that can be used to disable the device and hence reduce the power consumption. A logic high level on the SHDN input shuts the device down reducing the quiescent current to 0.02  $\mu$ A. During shutdown the output voltage discharges to 0 V. Therefore, ground referenced loads are not powered during this state. When exiting shutdown, it takes several cycles (approximately 175  $\mu$ s) for the charge pump to reach its final value. If the shutdown function is not being used, SHDN should be hardwired to GND.

**Capacitor Selection**

The flying capacitor C1 can be increased to reduce the output resistance.

The output capacitor size C2 affects the output ripple. Increasing the capacitor size reduces the peak-peak ripple. The ESR affects both the output impedance and the output ripple.

Reducing the ESR reduces the output impedance and ripple. For convenience it is recommended that both C1 and C2 be the same value.

The ac impedance of the ADM8828/ADM8829 may be reduced by using a bypass capacitor on the input supply. This capacitor should be connected between the input supply and GND. It will provide instantaneous current surges as required. Suitable capacitors of 1  $\mu$ F or greater may be used.

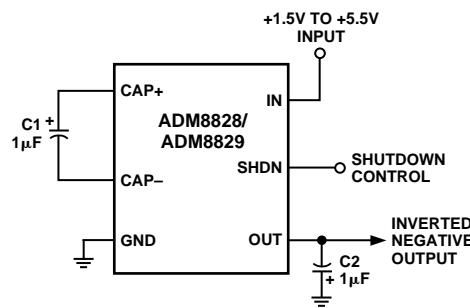
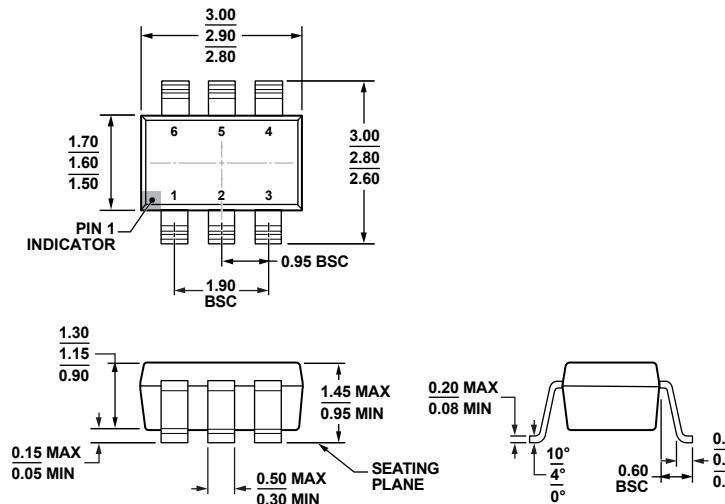


Figure 12. Typical Circuit Configuration

## OUTLINE DIMENSIONS



COMPLIANT TO JEDEC STANDARDS MO-178-AB

Figure 13. 6-Lead Small Outline Transistor Package [SOT-23]

(RJ-6)

Dimensions shown in millimeters

12-16-2008-A

## ORDERING GUIDE

Model <sup>1</sup>	Temperature Range	Package Description	Package Option	Marking Code
ADM8828ARTZ-REEL7	-40°C to +85°C	6-Lead Small Outline Transistor Package [SOT-23]	RJ-6	M58
ADM8829ARTZ-REEL	-40°C to +85°C	6-Lead Small Outline Transistor Package [SOT-23]	RJ-6	MN0
ADM8829ARTZ-REEL7	-40°C to +85°C	6-Lead Small Outline Transistor Package [SOT-23]	RJ-6	

<sup>1</sup> Z = RoHS Compliant Part.

**REVISION HISTORY****5/2019—Rev. A to Rev. B**

Updated Outline Dimensions .....	6
Changes to Ordering Guide .....	6

**11/2006—Rev. 0 to Rev. A**