SUM110N06-3m9H

Vishay Siliconix



| Parameter | Symbol | Test Conditions | Min | Тур | Max | Unit |
|---|----------------------|--|-----|---------|----------|------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | $V_{GS} = 0 V, I_D = 250 \mu A$ | 60 | | | V |
| Gate-Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \ \mu A$ | 3.4 | | 4.5 | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 20 V$ | | | 100 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | | | 1 | μA |
| | | $V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$ | | | 50 | |
| | | $V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$ | | | 250 | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$ | 120 | | | А |
| Drain-Source On-State Resistance ^a | r _{DS(on)} | V _{GS} = 10 V, I _D = 30 A | | 0.00325 | 0.0039 | Ω |
| | | V_{GS} = 10 V, I _D = 30 A, T _J = 125 °C | | | 0.0063 | |
| | | V_{GS} = 10 V, I _D = 30 A, T _J = 175 °C | | | 0.0082 | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 15 V, I _D = 30 A | 30 | | | S |
| Dynamic ^b | | | | | | |
| Input Capacitance | C _{iss} | V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz | | 15 800 | | pF |
| Output Capacitance | C _{oss} | | | 1050 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 600 | | |
| Gate Resistance | Rg | f = 1 MHz | 0.6 | 1.2 | 1.8 | Ω |
| Total Gate Charge ^c | Qg | V _{DS} = 30 V, V _{GS} = 10 V, I _D = 110 A | | 200 | 300 | nC |
| Gate-Source Charge ^c | Q _{gs} | | | 80 | | |
| Gate-Drain Charge ^c | Q _{gd} | | | 45 | | |
| Turn-On Delay Time ^c | t _{d(on)} | $V_{DD} = 30 \text{ V}, \text{ R}_{L} = 0.27 \Omega$ $I_{D} \cong 110 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{g} = 2.5 \Omega$ | | 45 | 70 | ns |
| Rise Time ^c | t _r | | | 160 | 240 | |
| Turn-Off Delay Time ^c | t _{d(off)} | | | 75 | 115 | |
| Fall Time ^c | t _f | | 14 | 25 | | |
| Source-Drain Diode Ratings and Cha | aracteristics | (T _C = 25 °C) ^b | | | <u> </u> | |
| Continuous Current | ا _S | | | | 110 | A |
| Pulsed Current | I _{SM} | | | | 240 | |
| Forward Voltage ^a | V _{SD} | I _F = 85 A, V _{GS} = 0 V | | 1.1 | 1.5 | V |
| Reverse Recovery Time | t _{rr} | I _F = 85 A, di/dt = 100 A/μs | | 65 | 100 | ns |
| Peak Reverse Recovery Current | I _{RM(REC)} | | | 4.4 | 6.6 | А |
| Reverse Recovery Charge | Q _{rr} | | | 143 | 330 | nC |

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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55 °C

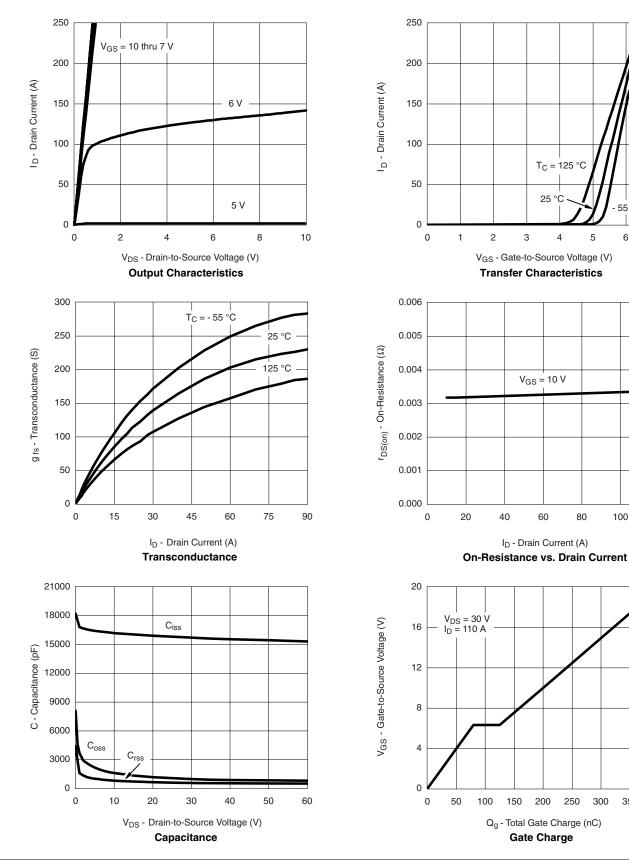
6

100

120

7

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

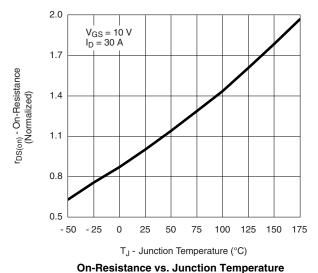


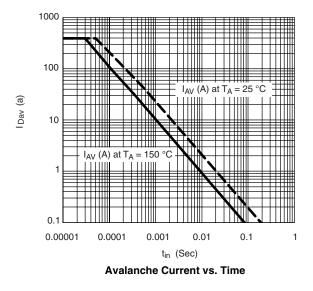
Document Number: 73236 S-70534-Rev. C, 26-Mar-07 350

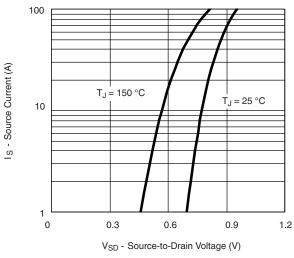
400

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

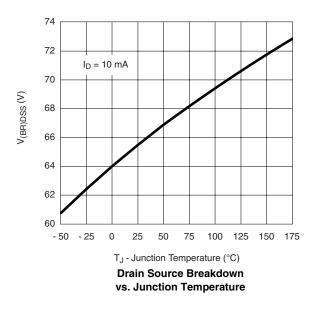


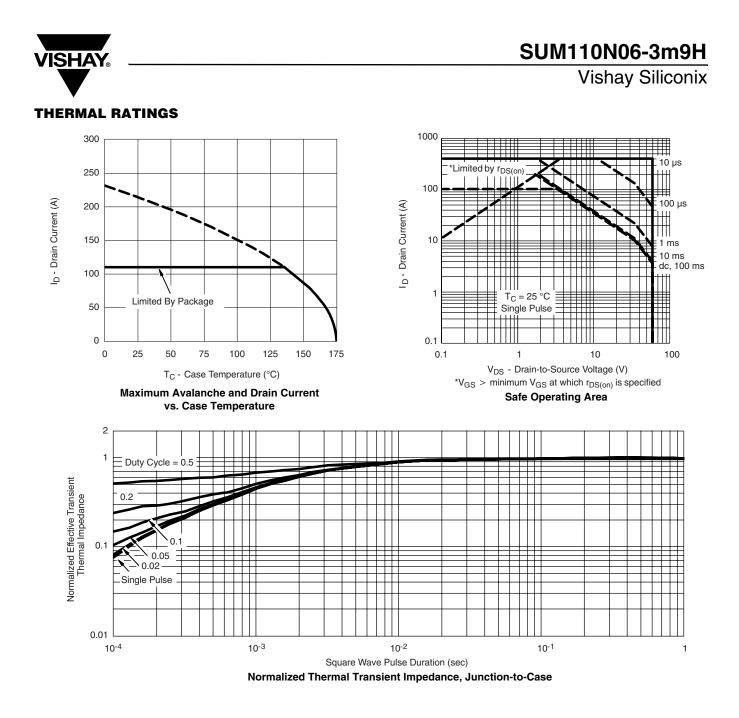




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Source-Drain Diode Forward Voltage





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73236.



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