

ELECTRICAL SPECIFICATIONS

Input		
Input voltage range	(See Note 3)	2.95 - 3.65 V
Input current	No load	10 mA typical
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		2.8 - 2.95 Vdc typical
Track input voltage	Pin 8 (See Note 6, 7)	±0.3 Vin
Output		
Voltage adjustability	(See Note 4)	0.8 - 2.5 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typical
Load regulation		±12 mV typical
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwidth	20 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response	(See Note 5)	70 µs recovery time Overshoot/undershoot 100 mV
Margin adjustment		±5.0% Vo

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated. Cin = 330 µF, Cout = 0 µF.

GENERAL SPECIFICATIONS

Efficiency	(See Efficiency Table)	93% max.
Insulation voltage		Non-isolated
Switching frequency	Fixed	300 kHz typ. ±25 kHz
Approvals and standards		EN60950, UL/cUL60950
Material flammability		UL94V-0
Dimensions	L x W x H	25.27 x 15.75 x 9.00 mm 0.995 x 0.620 x 0.354 in
Weight		3.7 g (0.13 oz)
MTBF	Telcordia SR-332	7,092,000 hours

EMC CHARACTERISTICS

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

ENVIRONMENTAL SPECIFICATIONS

Thermal performance (See Note 2)	Operating ambient temperature	-40 °C to +85 °C
	Non-operating temperature	-40 °C to +125 °C
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3
Protection		
Short-circuit	Auto reset	20 A typical

ORDERING INFORMATION

Model Number ⁽⁹⁾	Output Power (Max.)	Input Voltage	Output Voltage	Output Current (Min.)	Output Current (Max.)	Efficiency (Typical)	Regulation	
							Line	Load
PTH03060	25 W	2.95 - 3.65 V	0.8 - 2.5 V	0 A	10 A	93%	±10 mV	±12 mV

PART NUMBER SYSTEM WITH OPTIONS

Product Family	Input Voltage	Output Current	Mechanical Package	Output Voltage Code	Pin Option	Mounting Options	Pin Option
PTH	03	06	0	W	A	S	T
Point-of-Load Alliance compatible	03 = 3.3 V	06 = 10 A	Always 0	W = Wide		D = Horizontal through-hole (Matte Sn) Z = Surface-mount (96.5/3.0/0.5 Sn/Ag/Cu pin solder material)	No Suffix = Trays T = Tape and Reel ⁽⁹⁾

OUTPUT VOLTAGE ADJUSTMENT

The ultra-wide output voltage trim range offers major advantages to users who select the PTH03060. It is no longer necessary to purchase a variety of modules in order to cover different output voltages. The output voltage can be trimmed in a range of 0.8 Vdc to 2.5 Vdc. When the PTH03060 converter leaves the factory the output has been adjusted to the default voltage of 0.8 V.

Efficiency Table ($I_o = 7A$)

Output Voltage	Efficiency
$V_o = 1.0\text{ V}$	85%
$V_o = 1.2\text{ V}$	87%
$V_o = 1.5\text{ V}$	89%
$V_o = 1.8\text{ V}$	91%
$V_o = 2.0\text{ V}$	92%
$V_o = 2.5\text{ V}$	93%

Notes:

1. Remote ON/OFF, Positive Logic

ON: Pin 3 open; or $V > V_{in} - 0.5\text{ V}$

OFF: Pin 3 GND; or $V < 0.8\text{ V}$ (min - 0.2 V).

2. See Figures 1 for safe operating curves.

3. A 330 μF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 700 mA rms of ripple current.

4. An external output capacitor is not required for basic operation. Adding 330 μF of distributed capacitance at the load will improve the transient response.

5. 1 A/ μs load step, 50 to 100% I_{omax} , $C_{out} = 330\text{ }\mu\text{F}$.

6. If utilized V_{out} will track applied voltage by $\pm 0.3\text{ V}$ (up to V_o set point).

7. The pre-bias start-up feature is not compatible with Auto-Track™. This is because when the module is under Auto-Track™ control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track™ function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 154 for more details.

8. Tape and reel packaging only available on the surface-mount versions.

9. NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at <http://www.artesyn.com> to find a suitable alternative.

OUTPUT VOLTAGE ADJUSTMENT (CONTINUED)

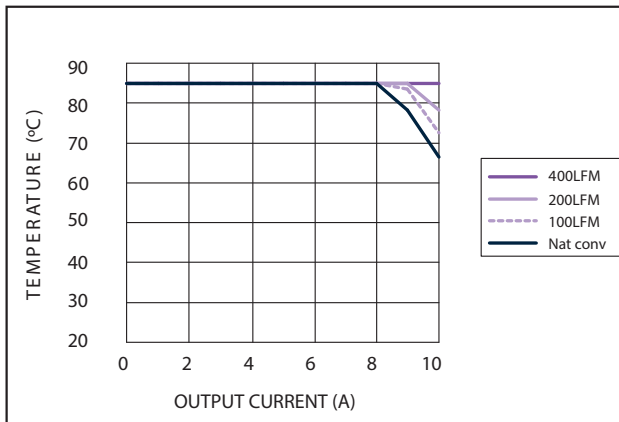


Figure 1 - Safe Operating Area
 $V_{in} = 3.3\text{ V}$, Output Voltage = 2.5 V (See Note A)

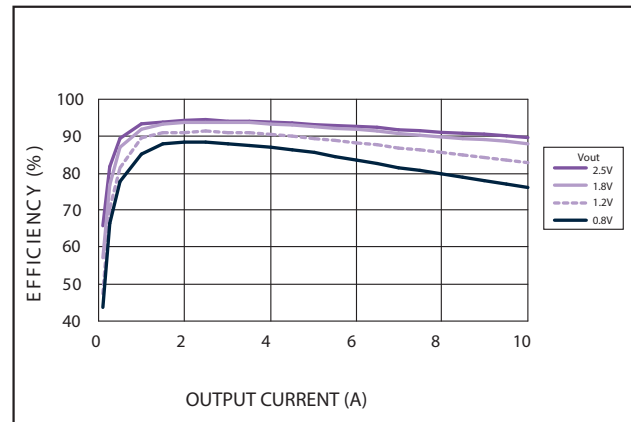


Figure 2 - Efficiency vs Load Current
 $V_{in} = 3.3\text{ V}$ (See Note B)

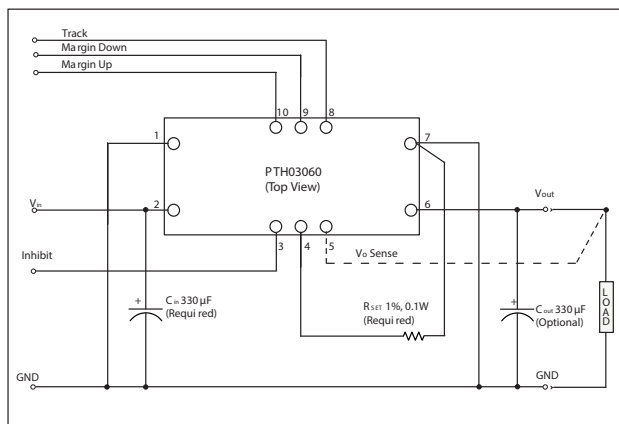


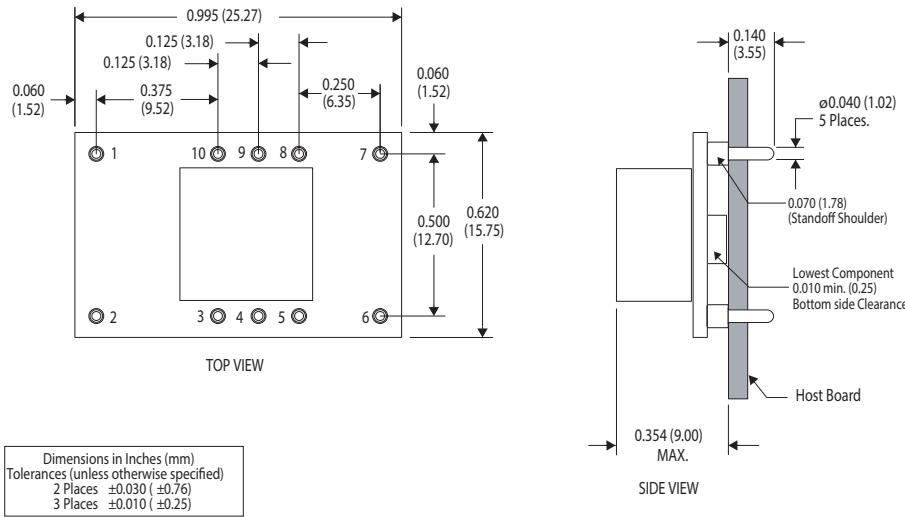
Figure 3 - Standard Application

Notes:

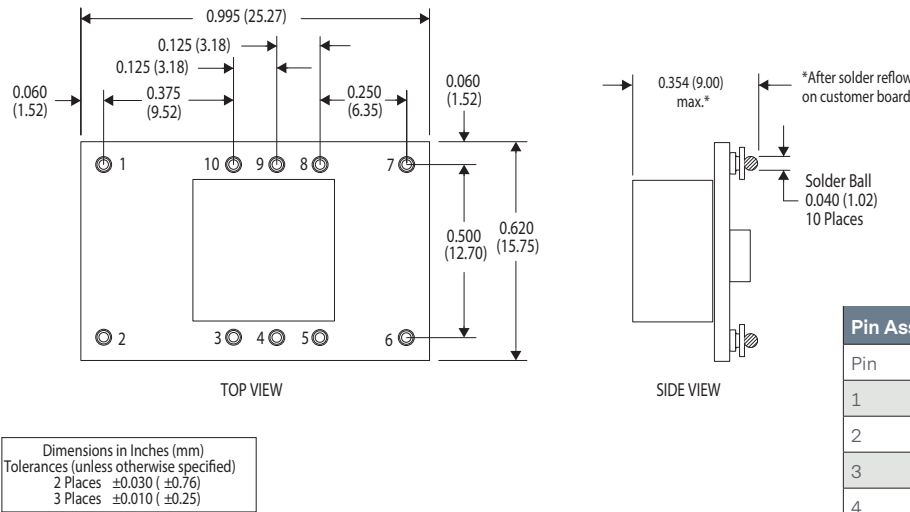
- A. SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
 B. Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.

MECHANICAL DRAWINGS

Plated through-hole



Surface-mount



Pin Assignments	
Pin	Function
1	Ground
2	Vin
3	Inhibit*
4	Vo adjust
5	Vo sense
6	Vout
7	Ground
8	Track
9	Margin down*
10	Margin up*
*Denotes negative logic: Open = Normal operation Ground = Function active	



For international contact information,
visit advancedenergy.com.

powersales@aei.com (Sales Support)
productsupport.ep@aei.com (Technical Support)
+1 888 412 7832

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