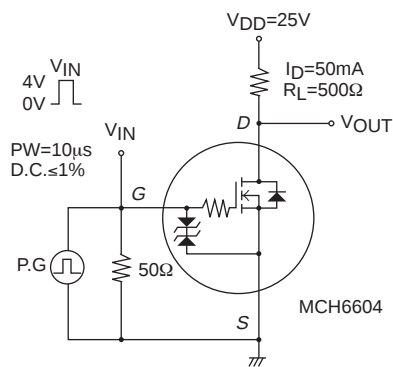


MCH6604

Electrical Characteristics at Ta=25°C

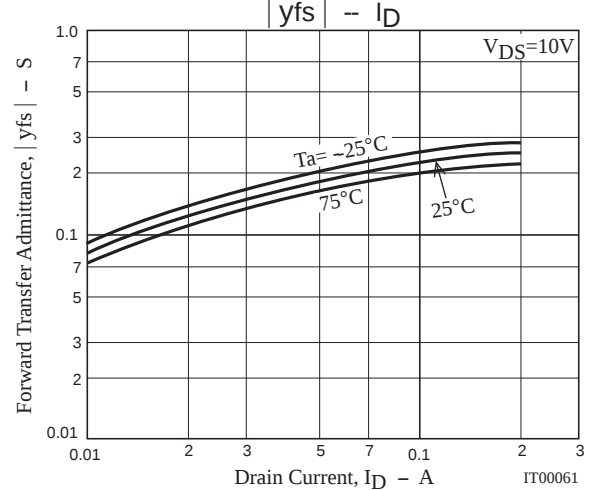
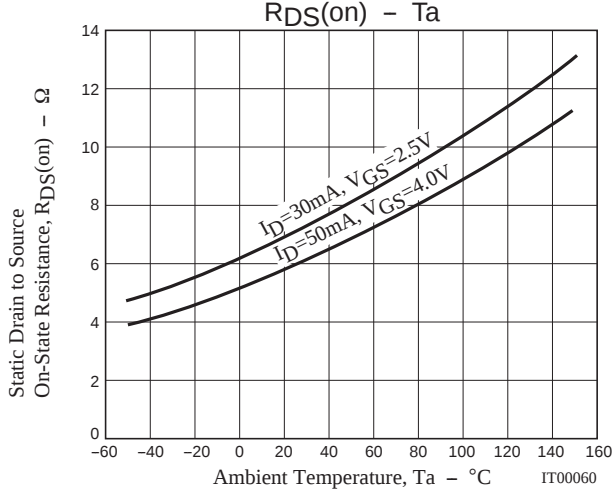
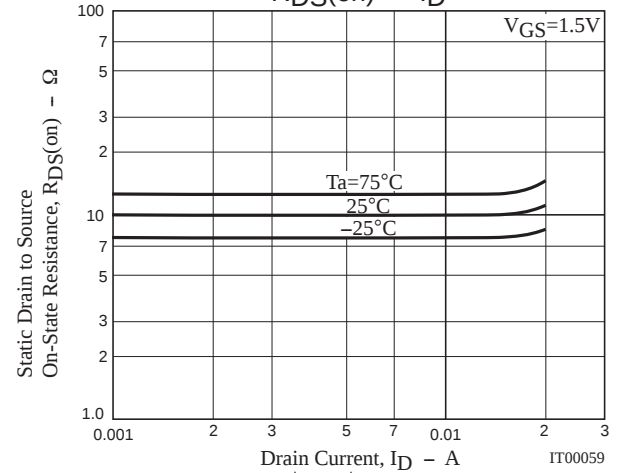
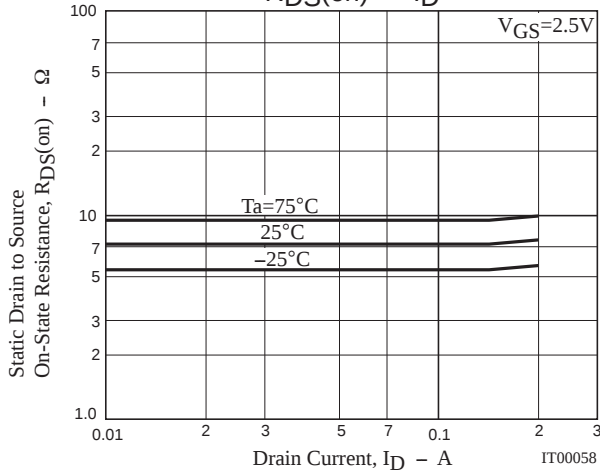
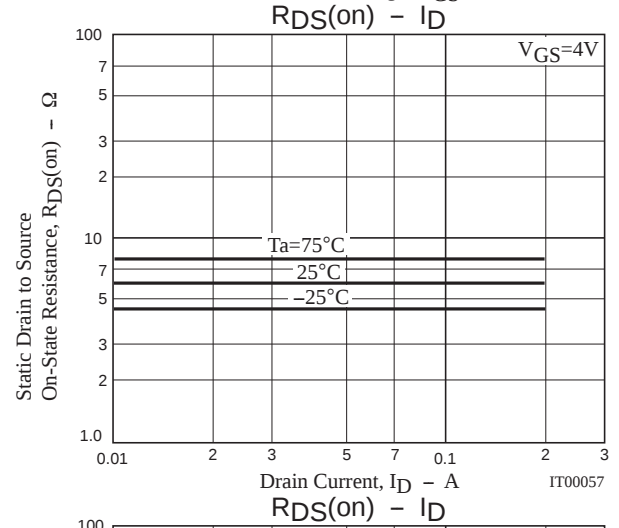
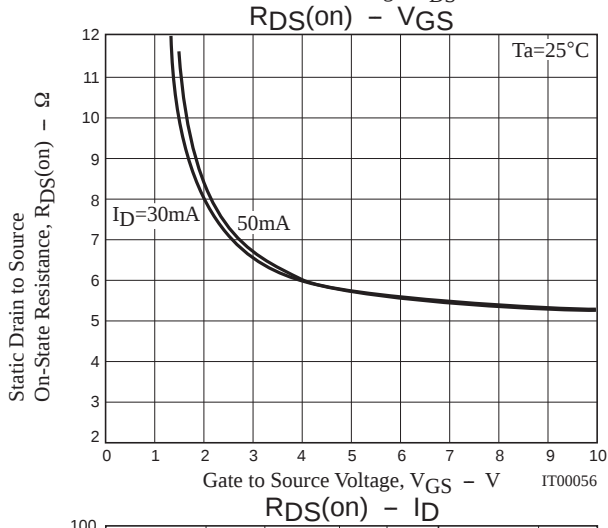
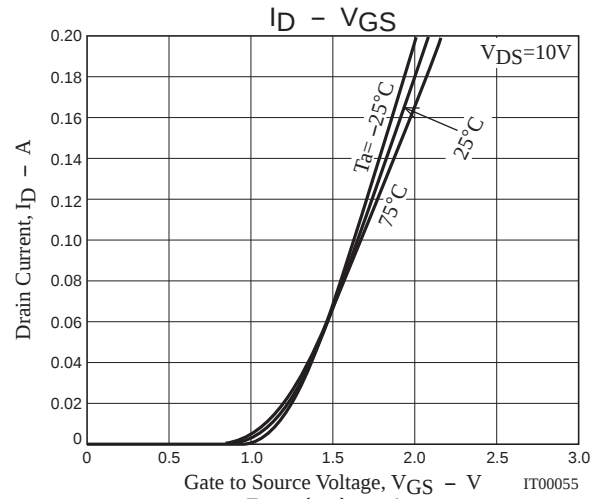
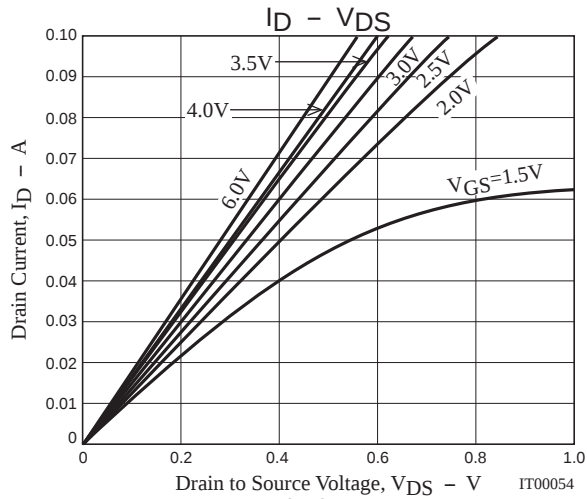
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0V$	50			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=50V, V_{GS}=0V$			1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=100\mu A$	0.4		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=50mA$	130	180		mS
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D=50mA, V_{GS}=4V$		6	7.8	Ω
	$R_{DS(on)2}$	$I_D=30mA, V_{GS}=2.5V$		7.1	9.9	Ω
	$R_{DS(on)3}$	$I_D=10mA, V_{GS}=1.5V$		10	20	Ω
Input Capacitance	C_{iss}	$V_{DS}=10V, f=1MHz$		6.6		pF
Output Capacitance	C_{oss}			4.7		pF
Reverse Transfer Capacitance	C_{rss}			1.7		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		18		ns
Rise Time	t_r			42		ns
Turn-OFF Delay Time	$t_{d(off)}$			190		ns
Fall Time	t_f			105		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=10V, I_D=100mA$		1.57		nC
Gate to Source Charge	Q_{gs}			0.20		nC
Gate to Drain "Miller" Charge	Q_{gd}			0.32		nC
Diode Forward Voltage	V_{SD}	$I_S=100mA, V_{GS}=0V$		0.85	1.2	V

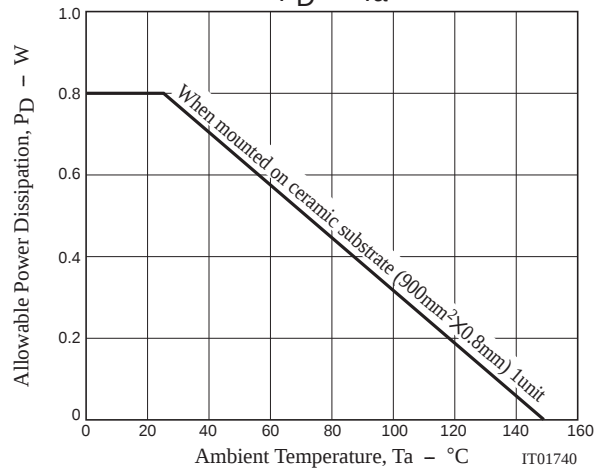
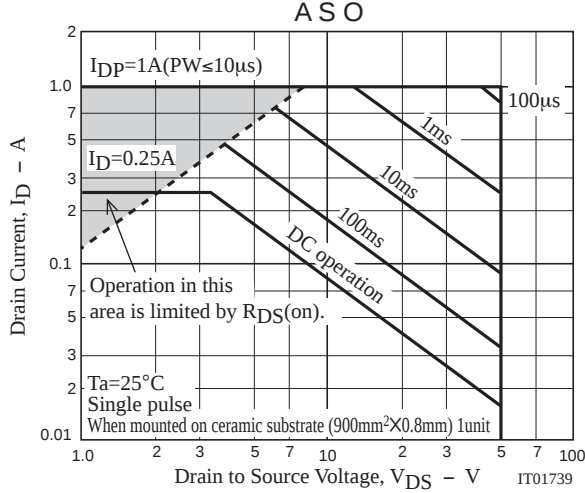
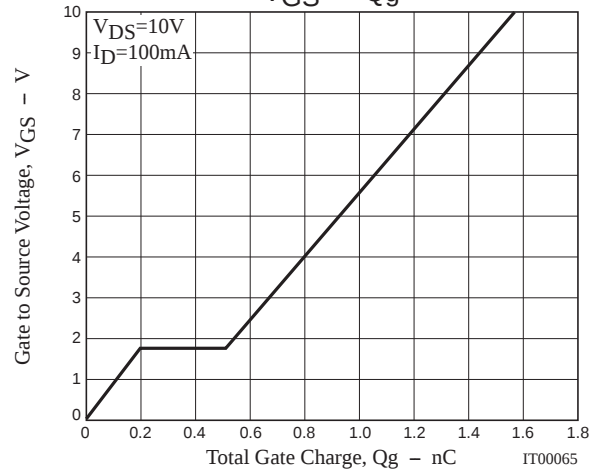
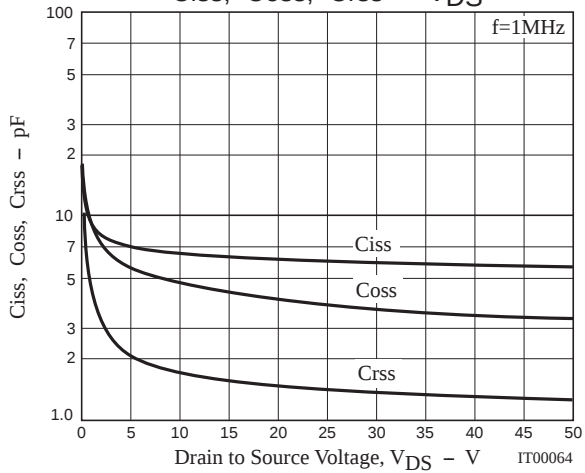
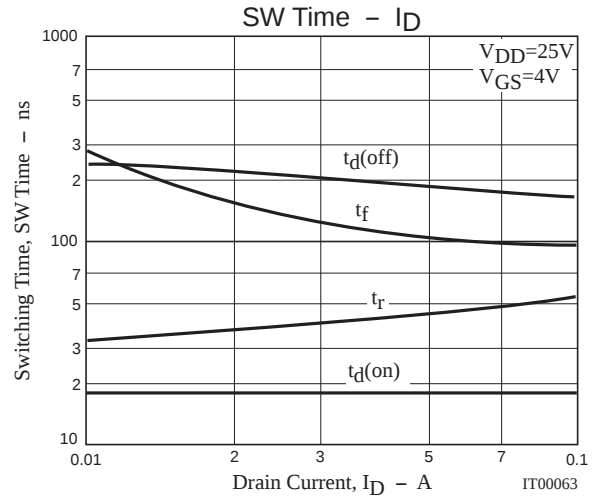
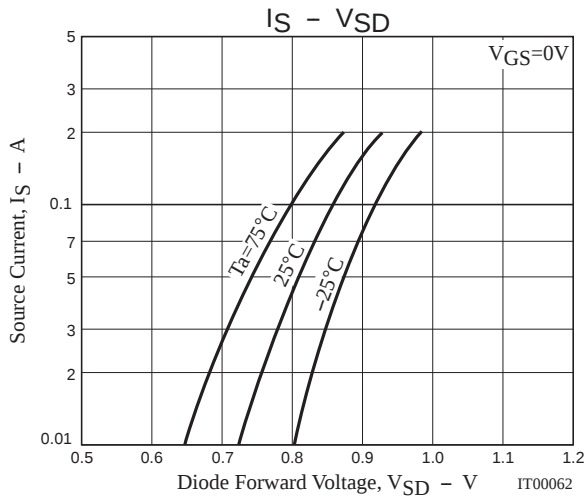
Switching Time Test Circuit



Ordering Information

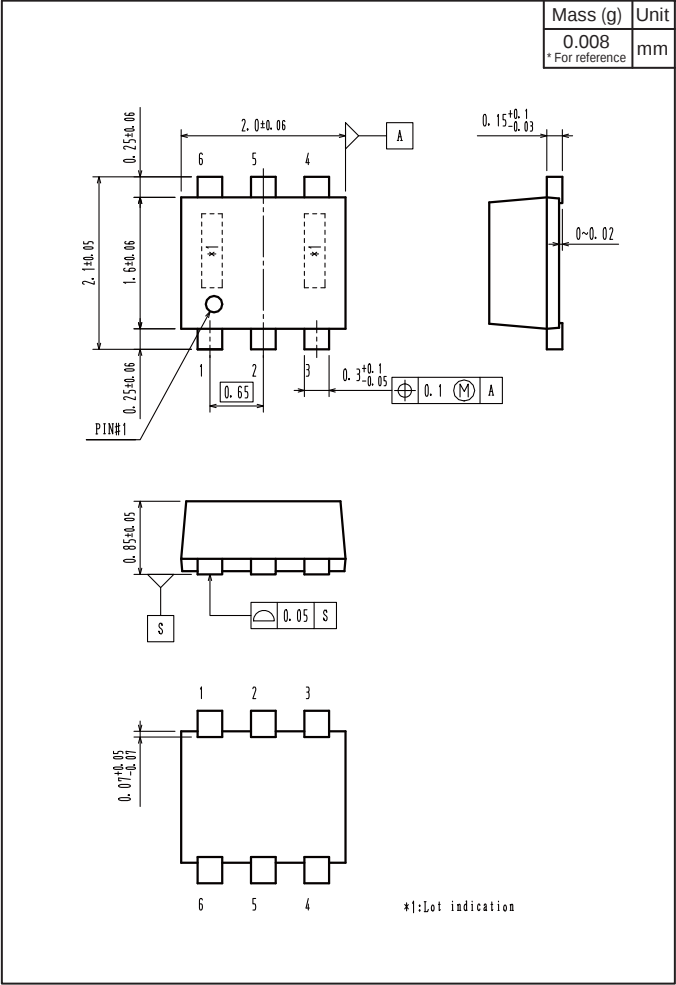
Device	Package	Shipping	memo
MCH6604-TL-E	MCPH6	3,000pcs./reel	Pb-Free



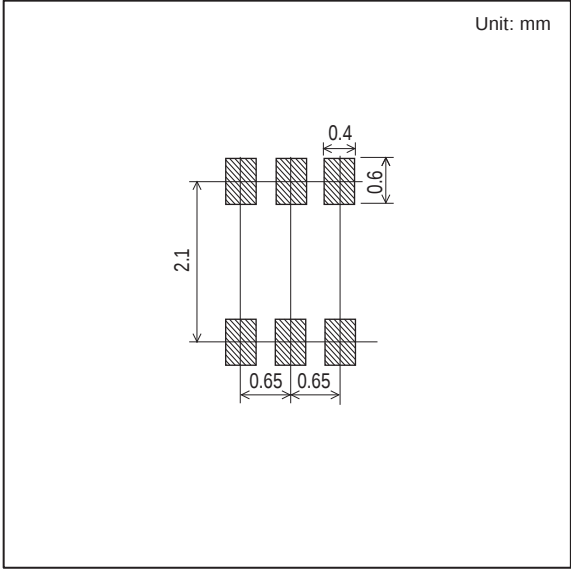


MCH6604

Outline Drawing
MCH6604-TL-E



Land Pattern Example



Note on usage : Since the MCH6604 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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