#### **SPECIFICATIONS**

### **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub> = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CC</sub> max	Supply Voltage		28	V
I <sub>OUT</sub> max	Output Current		0.8	Α
V <sub>OUT</sub> max	Output Withstand Voltage		28	V
V <sub>RD/FG</sub> max	Output Withstand Voltage of RD/FG Output Pin		28	V
I <sub>RD/FG</sub> max	RD/FG Output Current		5	mA
I <sub>B</sub> max	HB Output Current		10	mA
P <sub>d</sub> max	Allowable Dissipation	Mounted on a specified board (Note 1)	800	mW
T <sub>opr</sub>	Operating Temperature		-30 to +90	°C
T <sub>stg</sub>	Storage Temperature		-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Mounted on a specified board:  $114.3 \times 76.1 \times 1.6 \text{ mm}^3$ , glass epoxy board.

CAUTION: Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

CAUTION: Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact

us for the further details.

## **RECOMMENDED OPERATING CONDITIONS** ( $T_A = -30 \text{ to } +90^{\circ}\text{C}$ )

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	Supply Voltage		8	-	26.4	V
V <sub>I</sub> CM	Common-phase Input Voltage Range of Hall Input		0	-	V <sub>CC</sub> -1.5	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C, $V_{CC} = 24$ V, unless otherwise specified)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CC</sub> 1	Circuit Current	During drive (CT = L)	4.5	7	9.5	mA
I <sub>CC</sub> 2		During lock protection (CT = H)	2.5	4.5	6.5	mA
I <sub>CT</sub> 1	Lock Detection Capacitor Charge Current		2.0	2.7	3.5	μΑ
I <sub>CT</sub> 2	Capacitor Discharge Current		0.15	0.23	0.30	μΑ
R <sub>CT</sub>	Capacitor Charge and Discharge Current Ratio	$RCD = I_{CT}1/I_{CT}2$	10	12	14	
V <sub>CT</sub> 1	CT Charge Voltage		1.55	1.7	1.8	V
V <sub>CT</sub> 2	CT Discharge Voltage		0.65	0.75	0.85	V
V <sub>O</sub> L	OUT Output L Saturation Voltage	I <sub>O</sub> = 200 mA	_	0.2	0.3	V
V <sub>O</sub> H	OUT Output H Saturation Voltage	I <sub>O</sub> = 200 mA	-	0.9	1.2	V
V <sub>HN</sub>	Hall Input Sensitivity	Zero peak value (including offset and hysteresis)	-	7	-	mV
V <sub>RD/FG</sub>	RD/FG Output Pin L Voltage	I <sub>RD/FG</sub> = 5 mA	-	0.2	0.3	V
I <sub>RD/FG</sub>	RD/FG Output Pin Leak Current	V <sub>RD/FG</sub> = 15 V	_	1	3	μΑ
$V_{HB}$	HB Output Voltage	I <sub>HB</sub> = 5 mA	1.3	1.5	1.7	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## **TRUTH TABLE**

IN-	IN+	СТ	OUT1	OUT2	FG	RD	Mode
Н	L	L	Н	L	L	L	During rotation
L	Н		L	Н	Н		
-	-	Н	OFF	OFF	-	Н	During lock protection

NOTE: -: Don't care.

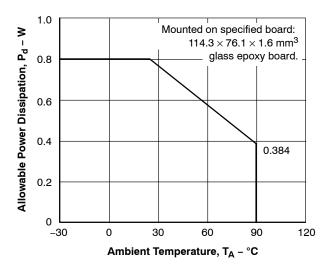


Figure 1. P<sub>d</sub> max – T<sub>A</sub>

## **BLOCK DIAGRAM**

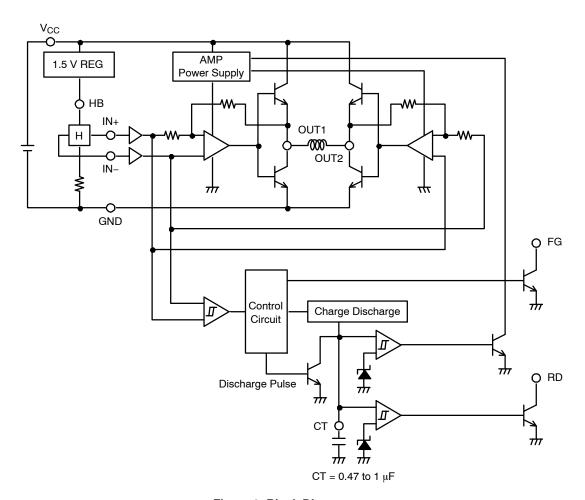


Figure 2. Block Diagram

#### **APPLICATION CIRCUIT EXAMPLE**

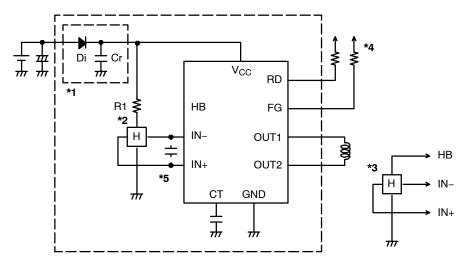


Figure 3. Application Circuit Example

- \*1: When Di to prevent breakdown in case of reverse connection is used, it is necessary to insert a capacitor Cr to secure the regenerative current route. Similarly, Cr is necessary to enhance the reliability when there is no capacitor near the fan power line.
- \*2: When taking Hall bias from V<sub>CC</sub>, carry out bias to V<sub>CC</sub> with resistor R1 as shown in the figure. Linear drive is achieved through voltage control of the coil by amplifying the Hall output. With large Hall element output, the start performance and efficiency are improved. Noise can be reduced further by adjusting the Hall element.
- \*3: When the Hall bias is taken from the HB pin, constant-voltage bias is made with about 1.5 V. Therefore, the Hall element can provide the output satisfactory in temperature characteristics.
- \*4: Keep this open when not using.
- \*5: When the wiring from the Hall output to IC Hall input is long, noise may be carried through the wiring.

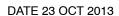
In this case, insert the capacitor as shown in the figure.

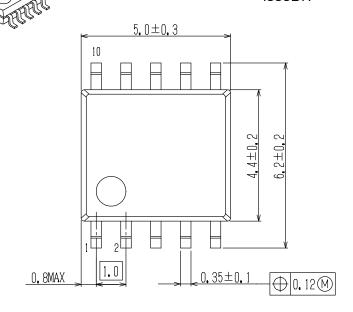
## **ORDERING INFORMATION**

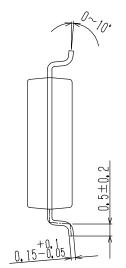
Device	Package	Wire Bond	Shipping <sup>†</sup> (Qty / Packing)
LA6588MC-AH	MFP10SK (225mil)	Au-wire	1,000 / Tape & Reel
LA6588MC-W-AH	MFP10SK (225mil)	Cu-wire	1,000 / Tape & Reel

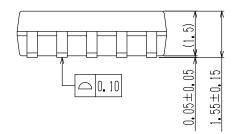
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <a href="https://example.com/BRD8011/D">BRD8011/D</a>.

#### SOIC10W / MFP10SK (225 mil) CASE 751DA ISSUE A

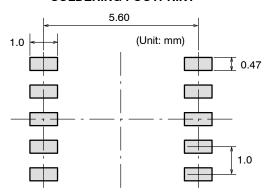








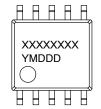
#### **SOLDERING FOOTPRINT\***



NOTE: The measurements are not to guarantee but for reference only.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

Y = Year M = Month

DDD = Additional Traceability Data

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

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