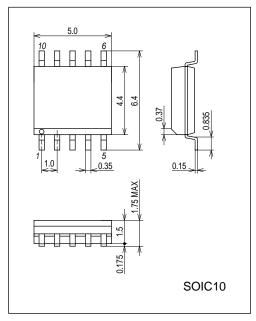
Operating Characteristics at Ta = 25 °C, $V_{CC} = 3.0V$, $f_{C} = 10.7MHz$

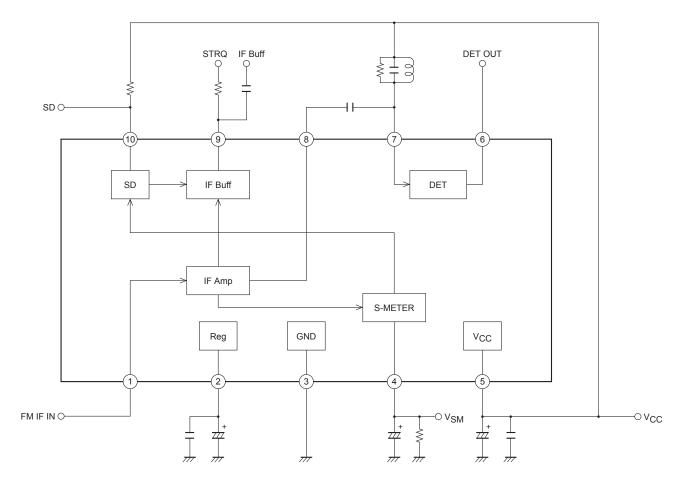
Downston	Symbol	Conditions	Ratings			11.2
Parameter			min	typ	max	Unit
Current drain	Icco	No input	3.0	4.0	5.0	mA
Demodulator output	VO	100dBμV, 100% mod., fm = 1kHz	70	150	220	mV
Total harmonic distortion	THD	100dBμV, 100% mod., fm = 1kHz		0.5	0.8	%
Signal-to-noise ratio	S/N	100dBμV, 100% mod., fm = 1kHz	65	73		dB
3dB sensitivity	-3dBL.S	100dBμV, 100% mod., fm = 1kHz output reference, when the input is -3dB	19	28	37	dBμV
SD sensitivity	SDON	0% mod.	35	50	65	dΒμV
IF counter buffer output	VIFBuff	100dBμV	90	130	170	mV

Package Dimensions

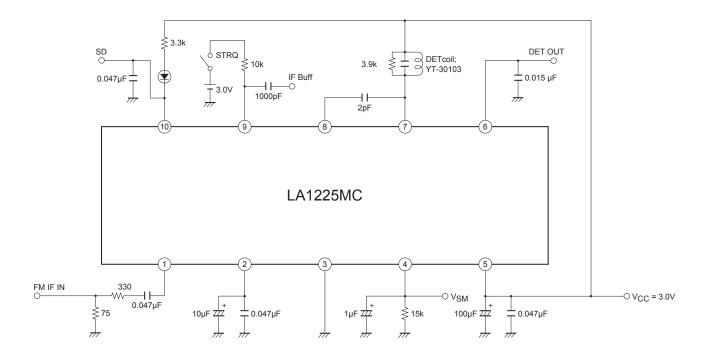
Unit: mm 3426



Block Diagram and Test Circuit



Sample Application Circuit



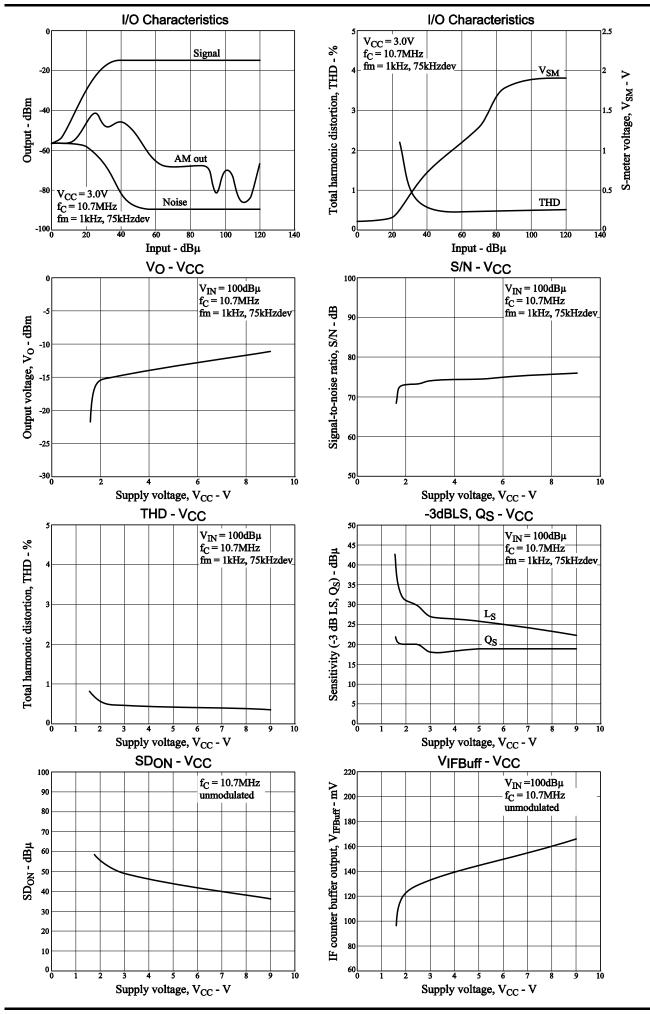
Pin Functions No-Signal Voltage at $V_{CC} = 3.0V$

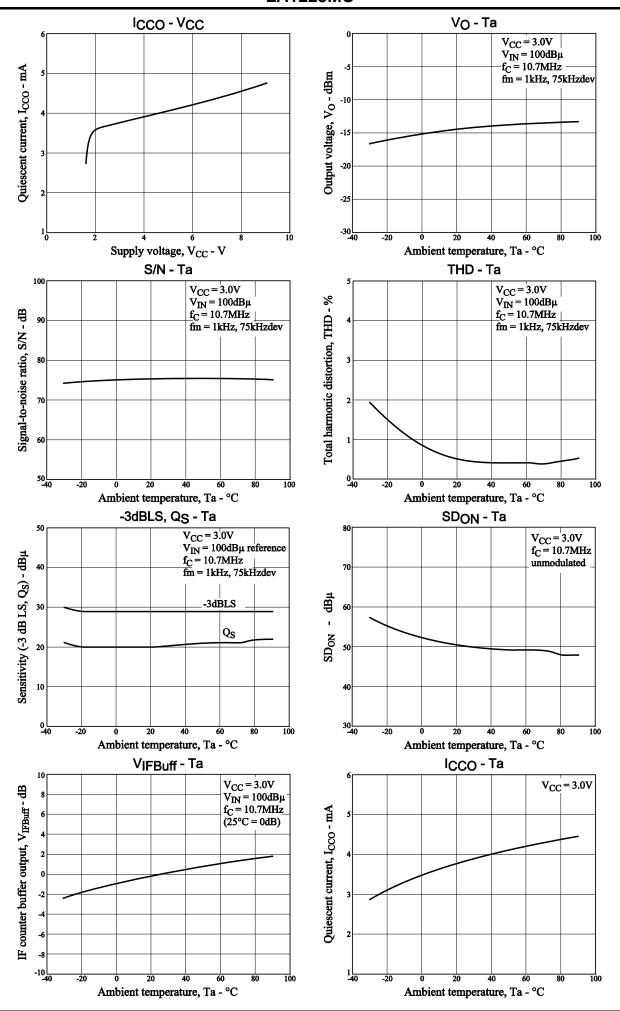
	unctions No-Signal	No-signal	1 10 - 3.0 1	
Pin No.	Function	voltage (V)	Equivalent circuit	Notes
1	IF input	1.2	1 RIN 2	Input impedance $R_{IN}=330\Omega$
2	Reg	1.2	2 A11507	Vreg = 1.2V
3	GND	0		
4	S-meter output	0.1	A11508	Open collector output. The SD sensitivity can be adjusted with an external resistor connected to this pin.
5	Vcc	3.0		
6	Demodulated output	1.5	ROUT 6	Output impedance $R_{OUT} = 3k\Omega$
7	DET	3.0	A11510	The detector coil is inserted between pin 7 and pin 5 (V _{CC}).

Continued on next page.

Continued from preceding page.

Continued	Continued from preceding page.								
Pin No.	Function	No-signal voltage (V)	Equivalent circuit	Notes					
8	Limiter amplifier output	2.8	A11511	Pin 8 and pin 7 (DET) are connected through a capacitor.					
9	IF buffer (Also used for control SW)	0	Control SW A11512	The IF buffer output is turned on when the voltage applied to the pin is the recommended 1.5V or higher.					
10	SD	1.6	10 A11513	This is an active-low output. This is an open-collector output and can directly drive an LED. (I _C max = 20mA)					





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