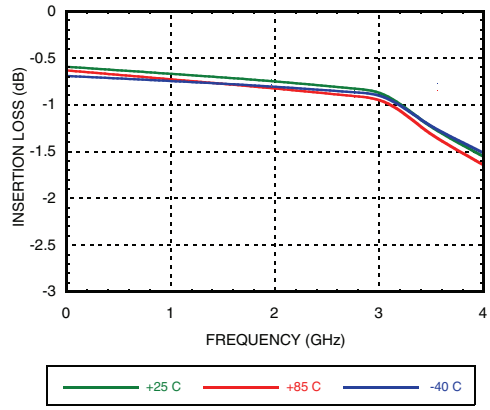
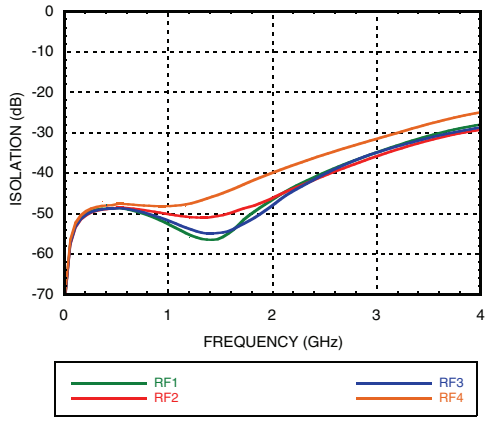
GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 3.5 GHz

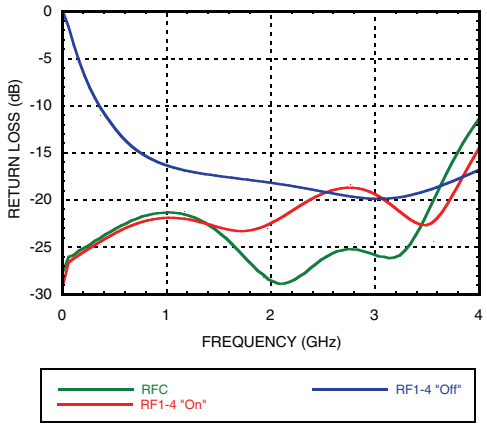
Insertion Loss



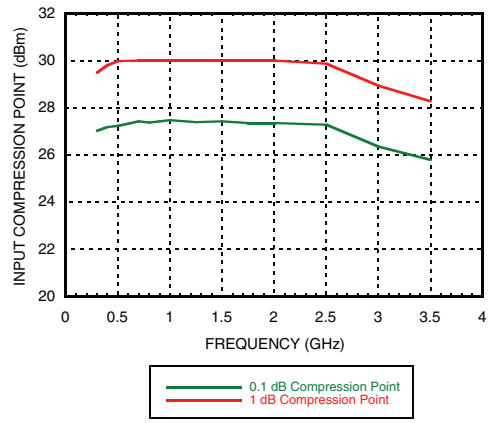
Isolation



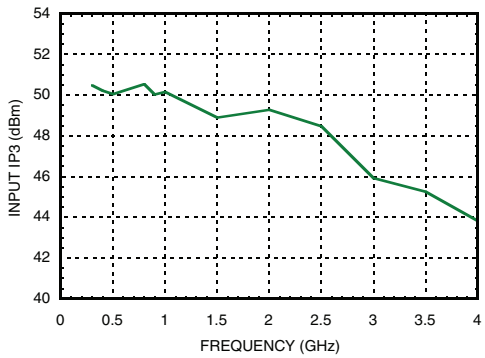
Return Loss



0.1 and 1 dB Input Compression Point



Input Third Order Intercept Point



NOTE:
DC Blocking capacitors are required at ports RFC and RF1, 2, 3, 4.

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Bias Voltage & Current

Vdd Range = +5 Vdc ± 10%		
Vdd (Vdc)	Idd (Typ.) (mA)	Idd (Max.) (mA)
+5	2.5	6.0

TTL/CMOS Control Voltages

State	Bias Condition
Low	0 to +0.8 Vdc @ 0.5µA Typ.
High	+2.0 to +5 Vdc @ 50 µA Typ.

Truth Table

Control Input		Signal Path State
A	B	RFCOM to:
LOW	LOW	RF1
HIGH	LOW	RF2
LOW	HIGH	RF3
HIGH	HIGH	RF4

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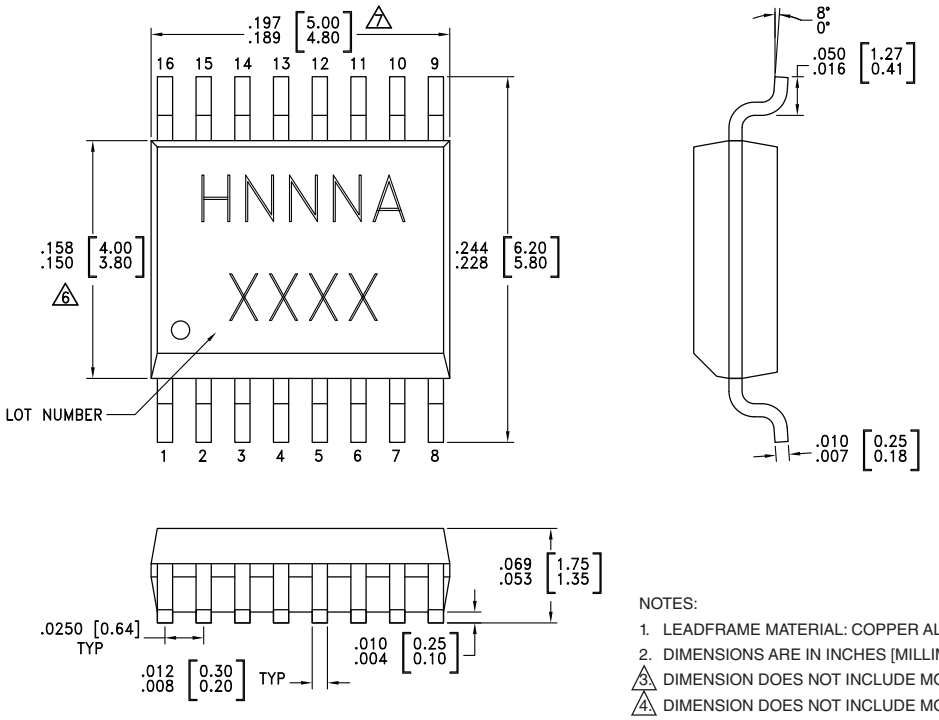
Absolute Maximum Ratings

Bias Voltage Range (Port Vdd)	+7.0 Vdc
Control Voltage Range (A & B)	-0.5V to Vdd +1 Vdc
Channel Temperature	150 °C
Thermal Resistance Insertion Loss Path Terminated Path	 150 °C/W 297 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
Maximum Input Power Vdd = +5 Vdc Insertion Loss Path Terminated Path	 +28.5 dBm +23.4 dBm
ESD Sensitivity (HBM)	Class 1A

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Outline Drawing



NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
3. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
4. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
5. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

Package Information

Part Number	Package Body Material	Leadframe Plating	MSL Rating	Package Marking [3]
HMC241AQS16	Low Stress Injection Molded Plastic Silica and Silicon Impregnated	Sn/Pb Solder	MSL1 [1]	HMC241A XXXX
HMC241AQS16E	RoHS-compliant Low Stress Injection Molded Plastic Silica and Silicon Impregnated	100% Matte Tin	MSL1 [2]	HMC241A XXXX

[1] Max peak reflow temperature of 235 °C
[2] Max peak reflow temperature of 260 °C
[3] 4-Digit lot number XXXX

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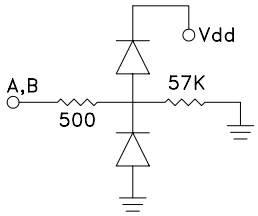
Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 4, 6, 12, 14	RF4, RF3, RF2, RF1, RFC	This pin is DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
2, 3, 5, 7, 11, 13, 15, 16	GND	This pin must be connected to PCB RF ground to maximize isolation.	
8	Vdd	Supply Voltage +5 Vdc ±10%	
9	B	See truth table and control voltage table.	
10	A	See truth table and control voltage table.	

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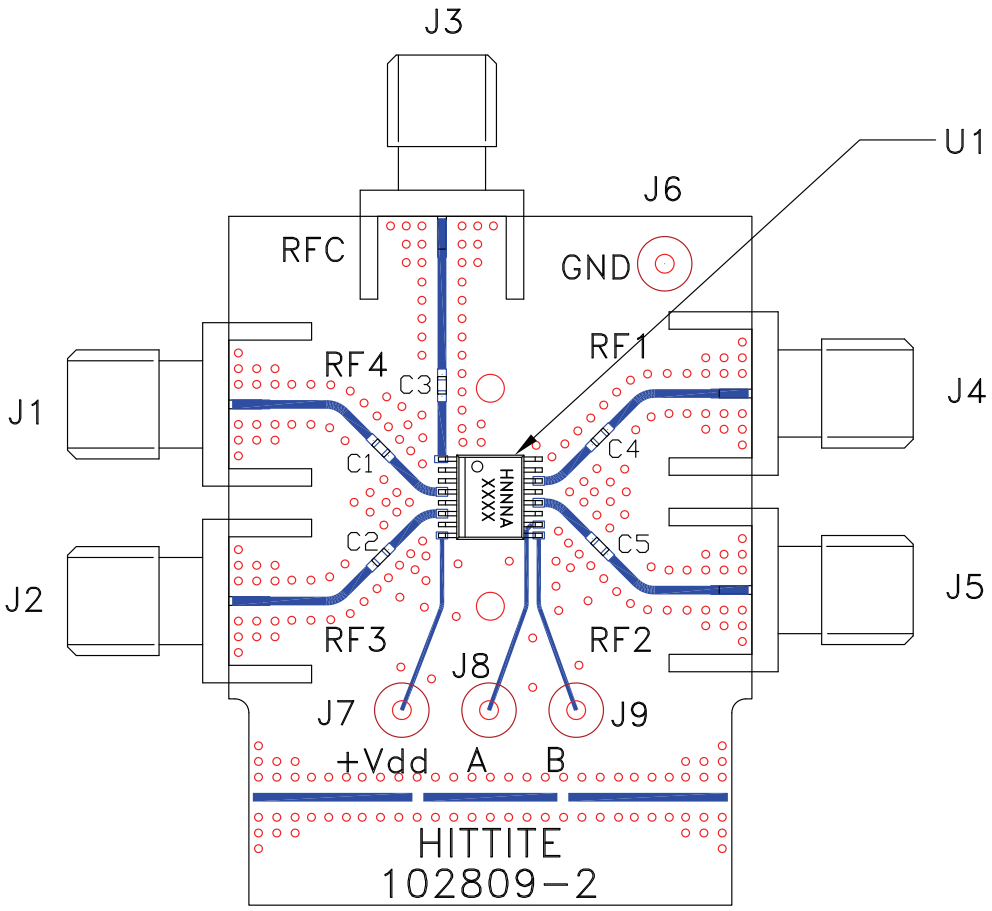
Evaluation PCB



List of Materials for Evaluation PCB EV1HMC241AQS16 [1]

Item	Description
J1 - J5	PCB Mount SMA RF Connector
J6 - J9	DC Pin
C1 - C5	330 pF capacitor, 0402 Pkg.
U1	HMC241AQS16 / 241AQS16E SP4T Switch
PCB [2]	102809 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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