

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	300	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	ID	0.21 0.16	А
Pulsed Drain Current (10µs pulse, duty cycle ≦1%)			I _{DM}	1	А
Maximum Body Diode Continuous Current (Note 6)			Is	2	А

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Dower Discinction	(Note 5)	D	0.9	W
Total Power Dissipation	(Note 6)	PD	2.2	
Thermal Desistance, Junction to Ambient	(Note 5)	P	132	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{ extsf{ heta}JA}$	55	
Thermal Resistance, Junction to Case	(Note 6)	R _θ JC	9.6	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Tun	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Symbol	IVIIII	Тур	Wax	Unit	Test condition	
Drain-Source Breakdown Voltage	BV _{DSS}	300			V	V _{GS} = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current				1	μA	$V_{\rm DS} = 240V, V_{\rm GS} = 0V$	
Gate-Body Leakage	I _{DSS}			±100	nA	$V_{\rm DS} = 240V, V_{\rm DS} = 0V$ $V_{\rm GS} = \pm 20V, V_{\rm DS} = 0V$	
ON CHARACTERISTICS (Note 7)	I _{GSS}			1100		VGS - ±200, VDS - 00	
Gate Threshold Voltage	V _{GS(th)}	1	_	3	V	V _{DS} = V _{GS} , I _D = 250µA	
° °		_	6	14	Ω	$V_{GS} = 10V, I_D = 0.3A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	6	20		V _{GS} = 4.5V, I _D = 0.2A	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 0.3A$	
DYNAMIC CHARACTERISTICS (Note 8)	·			•	•	·	
Input Capacitance	C _{iss}	_	96	_	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	C _{oss}	_	5.8	_			
Reverse Transfer Capacitance	C _{rss}	_	3.2	_			
Gate Resistance	R _G	_	12	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	4	_		V _{DS} = 192V, V _{GS} = 10V, I _D = 0.5A	
Gate-Source Charge	Q _{gs}	_	0.3	_	nC		
Gate-Drain Charge	Q _{gd}	_	1.9	_			
Turn-On Delay Time	t _{D(on)}		3.3	_			
Turn-On Rise Time	tr		8.6	_	-0	V _{DS} = 60V, R _L =200Ω V _{GS} = 10V, R _G = 25Ω	
Turn-Off Delay Time	t _{D(off)}	_	22	_	nS		
Turn-Off Fall Time	tf		12		1		
Reverse Recovery Time	trr	_	43		nS	1/1 = 100/1 = 1.00 = di/dt = 100.01/10	
Reverse Recovery Charge	Q _{rr}		47	—	nC	V _R = 100V, I _F =1.0A, di/dt=100A/µ	

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
Short duration pulse test used to minimize self-heating effect

8. Guaranteed by design. Not subject to production testing



DMN30H14DLY

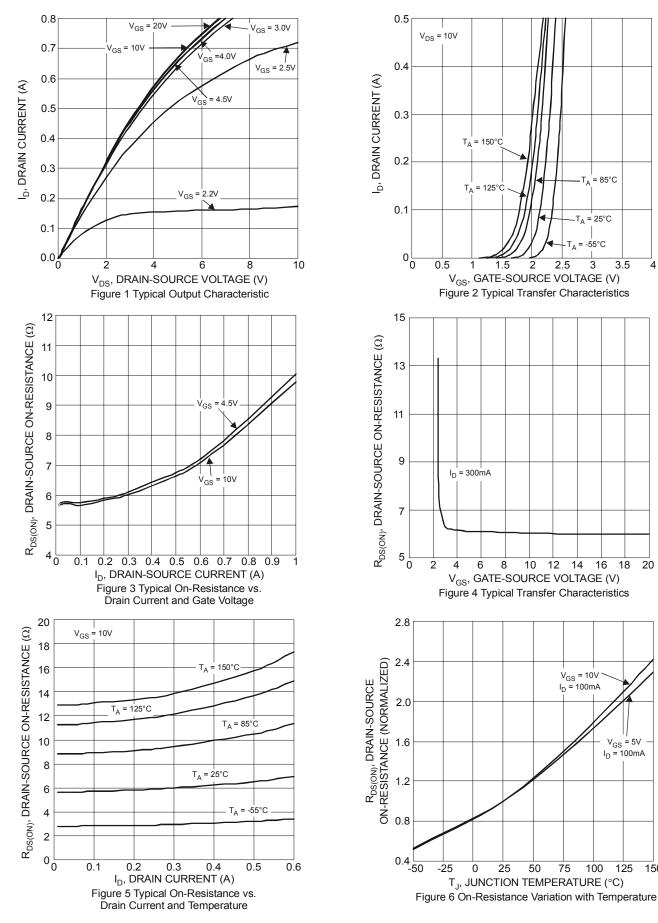
3.5

18 20

V_{GS} = 5V I_D = 100mA

125

4

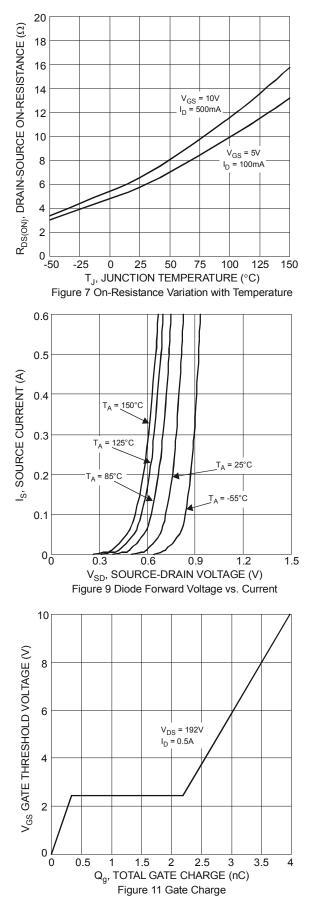


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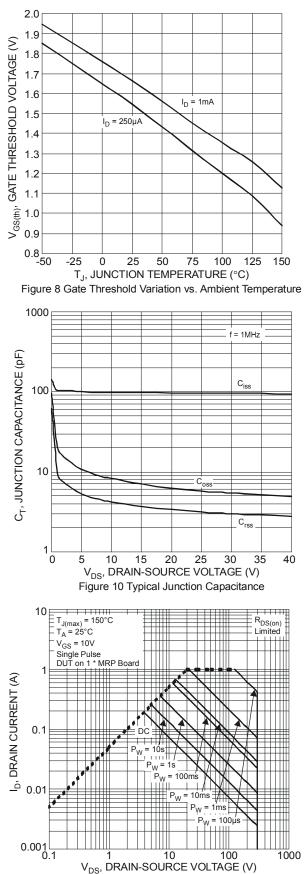
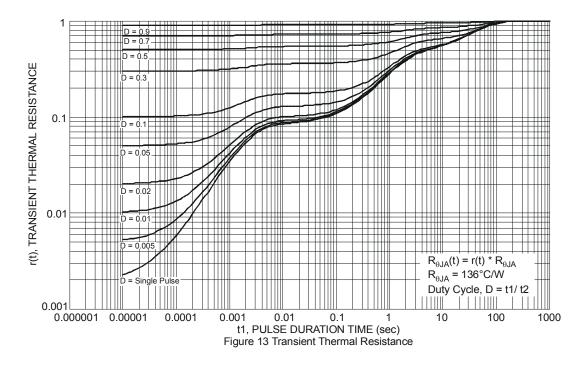


Figure 12 SOA, Safe Operation Area

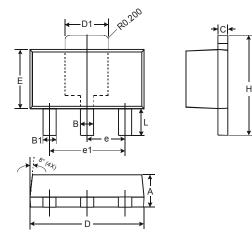
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Package Outline Dimensions

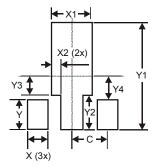
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT89				
Dim	Min	Max		
Α	1.40	1.60		
В	0.44	0.62		
B1	0.35	0.54		
С	0.35	0.43		
D	4.40	4.60		
D1	1.52	1.83		
Е	2.29	2.60		
е	1.50 Typ			
e1	3.00 Typ			
Н	3.94	4.25		
L	0.89	1.20		
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.900
X1	1.733
X2	0.416
Y	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
С	1.500



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