

MXT799T-AT/MXT799T-AB 1.0

Keys

- Up to 32 nodes can be allocated as mutual capacitance sensor keys (subject to other configurations)
- Adjacent Key Suppression (AKS) technology is supported for false key touch prevention

Enhanced Algorithms

- Lens bending algorithms to remove display noise
- Touch suppression algorithms to remove unintentional large touches, such as palm
- Palm Recovery Algorithm for quick restoration to normal state

Power Saving

- Programmable timeout for automatic transition from active to idle states
- Pipelined analog sensing detection and digital processing to optimize system power efficiency

Application Interfaces

- I²C-compatible slave with support for:
 - Standard mode (up to 100 kHz)
 - Fast mode (up to 400 kHz)
 - Fast-mode Plus (up to 1 MHz)
 - High-speed mode (up to 3.4 MHz)
- SPI slave interface (up to 8 MHz)
- Interrupt to indicate when a message is available
- SPI Debug Interface to read the real-time raw data for tuning and debugging purposes

Power Supply

- Digital (Vdd) 3.3 V nominal
- Digital I/O (VddIO) 3.3 V nominal
- Analog (AVdd) 3.3 V nominal
- High voltage external X line drive (XVdd) up to 9.0 V

Package

- 144-pin LQFP 20 × 20 × 1.4 mm, 0.5 mm pitch

Operating Temperature

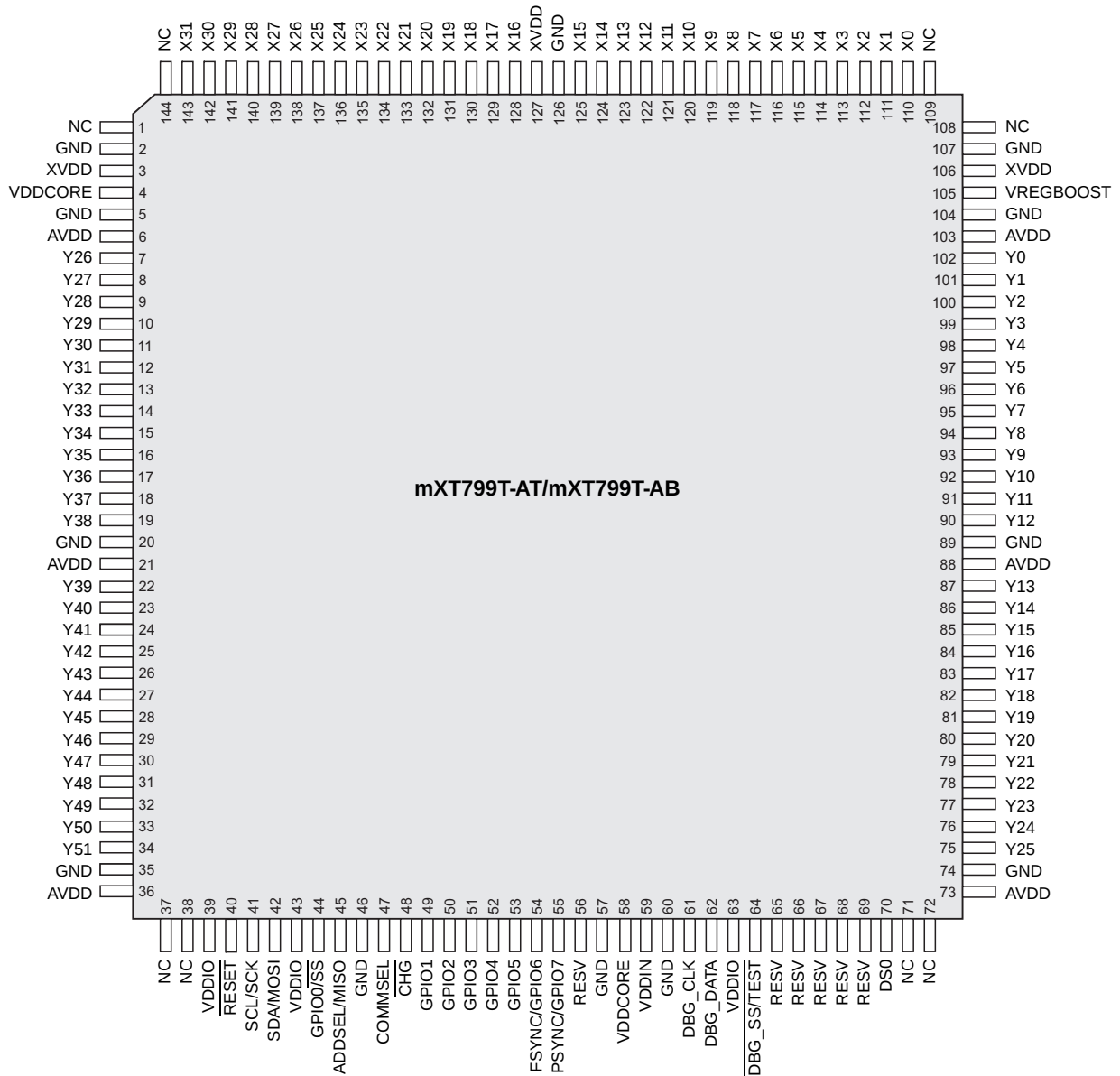
- mXT799T-AT: -40°C to +85°C (Grade 3)
- mXT799T-AB: -40°C to +105°C (Grade 2)

Design Services

- Review of device configuration, stack-up and sensor patterns
- Custom firmware versions can be considered, such as for specific gestures or proprietary OEM host communication protocols
- Contact your Microchip representative for more information

PIN CONFIGURATION

Pin Configuration – 144-pin LQFP



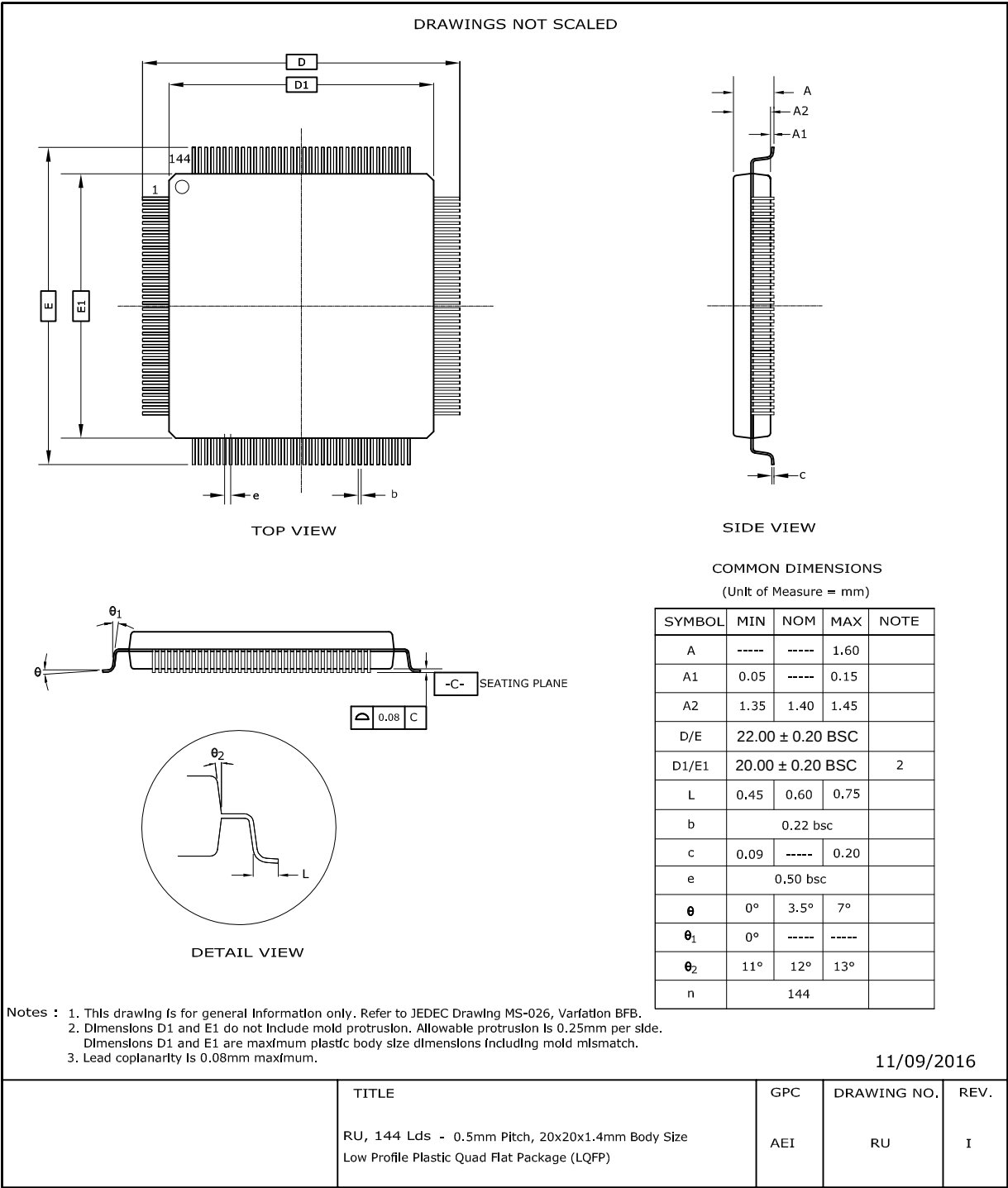
Top view

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1.0 PACKAGING INFORMATION

The following section gives the technical details of the package for the device.

1.1 144-pin LQFP 20 × 20 × 1.4 mm



APPENDIX A: REVISION HISTORY

Revision A (October 2017)

Initial edition for firmware revision 1.0.AB – Release

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PRODUCT IDENTIFICATION SYSTEM

The table below gives details on the product identification system for maXTouch devices. See [“Orderable Part Numbers”](#) below for example part numbers for the MXT799T-AT/mXT799T-AB.

To order or obtain information, for example on pricing or delivery, refer to the factory or the listed sales office.

PART NO.	-XXX	[X]	[XX]	[X]	[XXX]
Device	Package	Temperature Range	Sample Type	Tape and Reel Option	Pattern
Device:	Base device name				
Package:	A	=	QFP (Plastic Quad Flatpack)		
	CCU	=	UFBGA (Ultra Thin Fine-pitch Ball Grid Array)		
	C2U	=	UFBGA (Ultra Thin Fine-pitch Ball Grid Array)		
	NHU	=	UFBGA (Ultra Thin Fine-pitch Ball Grid Array)		
	C4U	=	X1FBGA (Extra Thin Fine-pitch Ball Grid Array)		
	MAU	=	XQFN (Super Thin Quad Flat No Lead Sawn)		
	MA5U	=	XQFN (Super Thin Quad Flat No Lead Sawn)		
	UU	=	WLCSP (Wafer Level Chip Scale Package)		
Temperature Range:	Blank	=	-40°C to +85°C (Grade 3)		
	T	=	-40°C to +85°C (Grade 3)		
	B	=	-40°C to +105°C (Grade 2)		
Sample Type:	Blank	=	Release Sample		
	ES	=	Pre-release (Engineering) Sample		
Tape and Reel Option:	Blank	=	Standard Packaging (Tube or Tray)		
	R	=	Tape and Reel ⁽¹⁾		
Pattern:	QTP, SQTP, Code or Special Requirements (Blank Otherwise)				

Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. See “[Orderable Part Numbers](#)” below or check with your Microchip Sales Office for package availability with the Tape and Reel option.

Orderable Part Numbers

Orderable Part Number	Firmware Revision	Description
ATMXT799T-AT (Supplied in trays)	1.0.AB	144-pin LQFP 20 × 20 × 1.4 mm, RoHS compliant Operating temperature range –40°C to +85°C (Grade 3) Automotive grade sample; suitable for automotive characterization
ATMXT799T-ATR (Supplied in tape and reel)		
ATMXT799T-AB (Supplied in trays)	1.0.AB	144-pin LQFP 20 × 20 × 1.4 mm, RoHS compliant Operating temperature range –40°C to +105°C (Grade 2) Automotive grade sample; suitable for automotive characterization
ATMXT799T-ABR (Supplied in tape and reel)		

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 2355 West Chandler Blvd.
 Chandler, AZ 85224-6199
 Tel: 480-792-7200
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ASIA/PACIFIC

Asia Pacific Office
 Suites 3707-14, 37th Floor
 Tower 6, The Gateway
 Harbour City, Kowloon

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 Fax: 852-2401-3431

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