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## Vishay Semiconductors

ELECTRICAL SPECIFICA	TIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS	
		19 A	T.I = 25 °C	-	0.41		
Forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 0	-	0.52	- V	
See fig. 1	VFM (*)	19 A	T 405.00	0.25	0.33		
		40 A	T <sub>J</sub> = 125 °C	0.37	0.50		
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 25 \ ^{\circ}C$	$V_{\rm B} = Rated V_{\rm B}$	-	10	mA	
See fig. 2	IRM (1)	T <sub>J</sub> = 100 °C	VR – naleu VR	-	600		
Threshold voltage	V <sub>F(TO)</sub>	T. T		0.182		82	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		7	.6	mΩ	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ , (test signal ran	ge 100 kHz to 1 MHz), 25 °C	-	2000	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 m	m from package body	8	-	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10	000	V/µs	

### Note

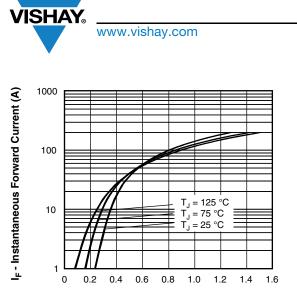
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Maximum junction temperature range	TJ		-55 to +125	°C	
Maximum storage temperature range	T <sub>Stg</sub>		-55 to +150	C	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	1.5		
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (For TO-220)	0.50	°C/W	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	40		
Approximate weight			2	g	
Approximate weight			0.07	oz.	
Mounting torque		Non-lubricated threads	6 (5)	kgf · cm	
Mounting torque maximum			12 (10)	(lbf · in)	
Marking device		Case style D <sup>2</sup> PAK	20L15TS		

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V<sub>FM</sub> - Forward Voltage Drop (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

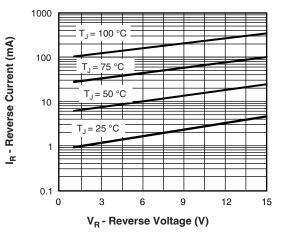


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

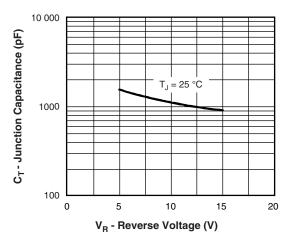


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

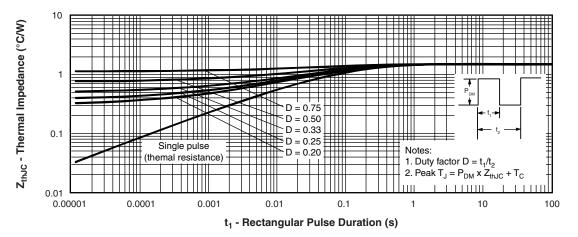
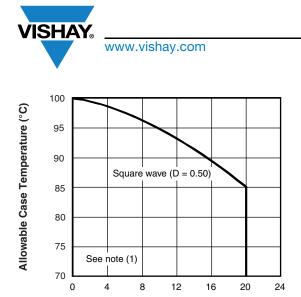


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

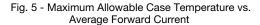
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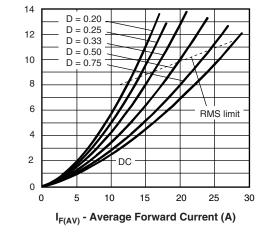


### **Vishay Semiconductors**

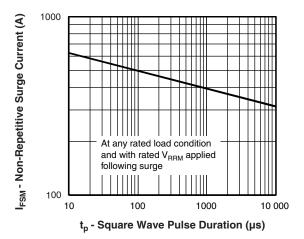


I<sub>F(AV)</sub> - Average Forward Current (A)









Average Power Loss (W)

Fig. 7 - Maximum Non-Repetitive Surge Current

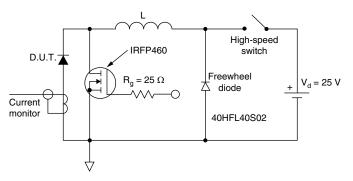


Fig. 8 - Unclamped Inductive Test Circuit

### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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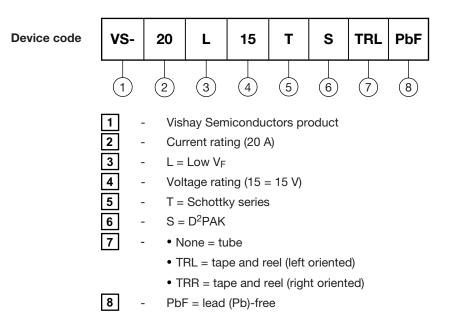
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### **ORDERING INFORMATION TABLE**

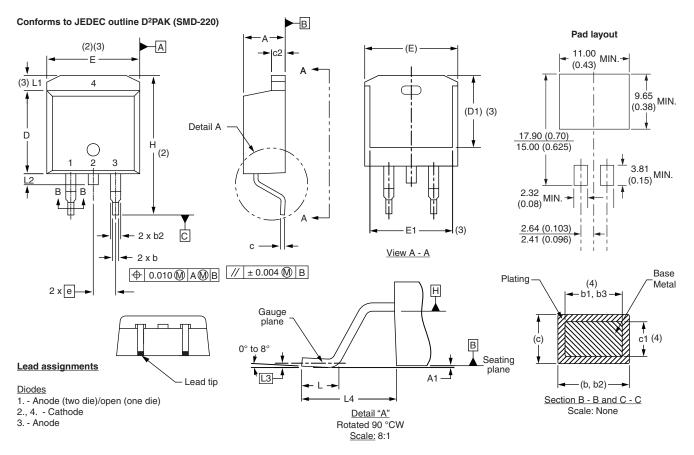


ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-20L15TSPbF	50	1000	Antistatic plastic tubes	
VS-20L15TSTRLPbF	800	800	13" diameter reel	
VS-20L15TSTRRPbF	800	800	13" diameter reel	
VS-20L15T-1PbF	50	1000	Antistatic plastic tubes	

LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032

Vishay Semiconductors

# D<sup>2</sup>PAK, TO-262



### DIMENSIONS - D<sup>2</sup>PAK in millimeters and inches

SHA

SYMBOL	MILLIN	MILLIMETERS		INCHES	
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
с	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

INCHES

MILLIMETERS

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing per ASME Y14.5 M-1994  $\,$ 

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch

Document Number: 95014 Revision: 31-Mar-09

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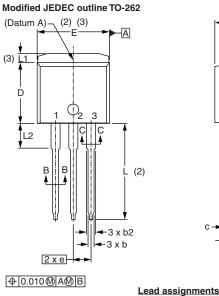
# **Outline Dimensions**

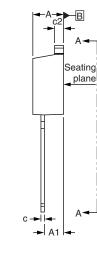
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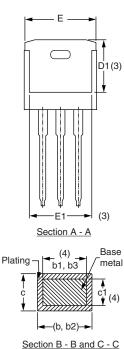
D<sup>2</sup>PAK, TO-262



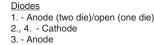
### DIMENSIONS - TO-262 in millimeters and inches

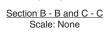






Lead tip





(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the

SYMBOL -	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

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actual package outline

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