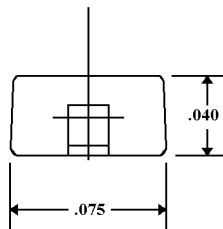
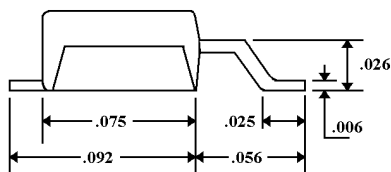
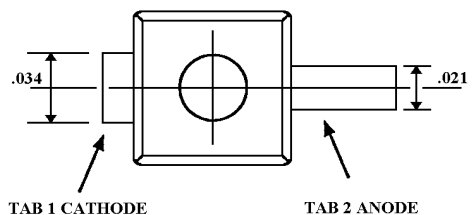


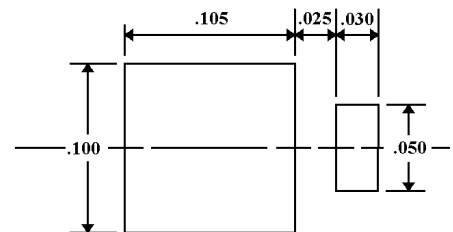
Parameter	Symbol	Conditions	T <sub>J</sub> = 25°C	T <sub>J</sub> = 85°C	Units
Maximum Forward Voltage (Note 1) See Figure 2	V <sub>F</sub>	I <sub>F</sub> = 0.1 A I <sub>F</sub> = 1.0 A I <sub>F</sub> = 3.0 A	0.34 0.45 0.65	0.25 0.415 0.67	V
Maximum Instantaneous Reverse Current (Note 1)	I <sub>R</sub>	V <sub>R</sub> = 20 V V <sub>R</sub> = 10 V	0.40 0.10	25 18	mA

Note: 1 Short duration test pulse used to minimize self – heating effect.

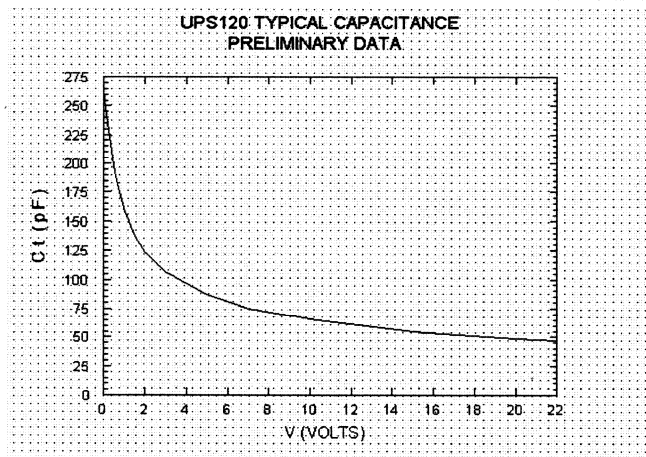
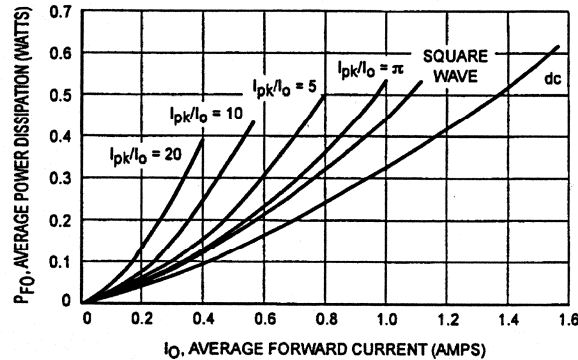
### PACKAGE & MOUNTING PAD DIMENSIONS



DO-216 Package (All dimensions +/- .005 inches)



MOUNTING PAD in inches

**CHARTS AND GRAPHS**

**FIGURE 1**

**FIGURE 2**

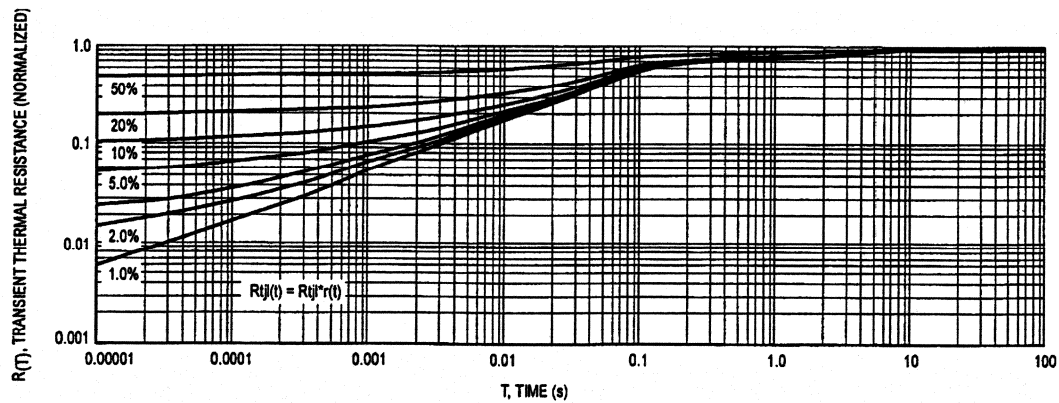
Forward Power Dissipation

\* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of  $T_J$  therefore must include forward and reverse power effects. The allowable operating  $T_J$  may be calculated from the equation:

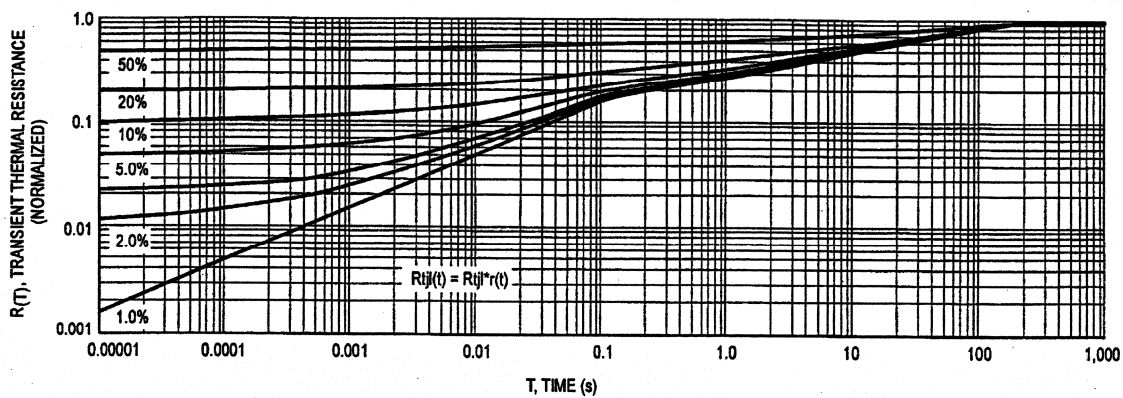
$$T_J = T_{J \max} = r(t)(P_f + P_r) \text{ where}$$

$r(t)$  = thermal impedance under given conditions.  
 $P_f$  = forward power dissipation, and  
 $P_r$  = reverse power dissipation

This graph displays the derated allowable  $T_J$  due to reverse bias under DC conditions only and is calculated as  $T_J = T_{J \max} - r(t) P_r$ , Where  $r(t) = R_{thja}$ . For other power applications further calculations must be performed.



**FIGURE 3 – Thermal Impedance Junction to Case (bottom)**



**FIGURE 4 – Thermal Impedance Junction to Ambient**