Vishay Semiconductors



Absolute Maximum Ratings

Absolute Maximum Ratings

 $T_{amb} = 25 \ ^{\circ}C$, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Supply Voltage	(Pin 2)	V _S	- 0.3 to + 6.0	V
Supply Current	(Pin 2)	۱ _S	5	mA
Output Voltage	(Pin 3)	V _O	- 0.3 to + 6.0	V
Output Current	(Pin 3)	Ι _Ο	5	mA
Junction Temperature		Тj	100	°C
Storage Temperature Range		T _{stg}	- 25 to + 85	°C
Operating Temperature Range		T _{amb}	- 25 to. + 85	°C
Power Consumption	$(T_{amb} \le 85 \ ^{\circ}C)$	P _{tot}	50	mW
Soldering Temperature	$t \leq$ 10 s, 1 mm from case	T _{sd}	260	°C

Electrical and Optical Characteristics

 T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Supply Current (Pin 2)	$V_{\rm S} = 5 \rm V, E_{\rm v} = 0$	I _{SD}	0.8	1.2	1.5	mA
	$V_{S} = 5 V$, $E_{v} = 40 klx$, sunlight	I _{SH}		1.5		mA
Supply Voltage (Pin 2)		V _S	4.5		5.5	V
Transmission Distance	$E_v = 0$, test signal see fig.1, IR diode TSAL6200, I _F = 400 mA	d		35		m
Output Voltage Low (Pin 3)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ f = f _o , test signal see fig. 1	V _{OSL}			250	mV
Minimum Irradiance (30 - 40 kHz)	Pulse width tolerance: $t_{pi} - 5/f_0 < t_{po} < t_{pi} + 6/f_0$, test signal see fig.1	E _{e min}		0.35	0.5	mW/m ²
Minimum Irradiance (56 kHz)	Pulse width tolerance: t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see fig.1	E _{e min}		0.4	0.6	mW/m ²
Maximum Irradiance	t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see fig. 1	E _{e max}	30			W/m ²
Directivity	Angle of half transmission distance	φ _{1/2}		± 45		deg



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Typical Characteristics (Tamb = 25 °C unless otherwise specified)

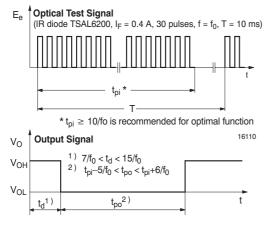


Figure 1. Output Function

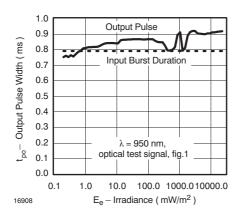
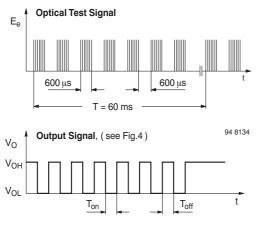


Figure 2. Pulse Length and Sensitivity in Dark Ambient





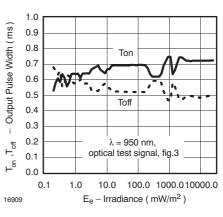


Figure 4. Output Pulse Diagram

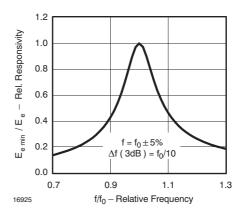


Figure 5. Frequency Dependence of Responsivity

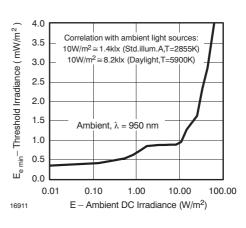


Figure 6. Sensitivity in Bright Ambient

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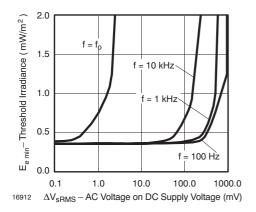


Figure 7. Sensitivity vs. Supply Voltage Disturbances

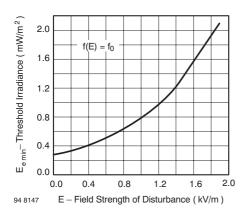


Figure 8. Sensitivity vs. Electric Field Disturbances

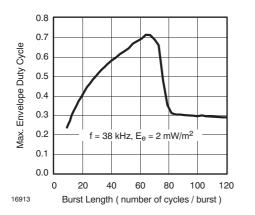


Figure 9. Max. Envelope Duty Cycle vs. Burstlength

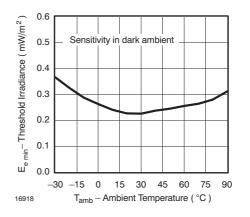


Figure 10. Sensitivity vs. Ambient Temperature

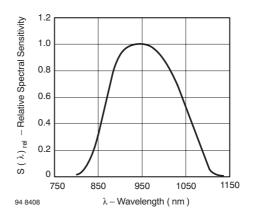


Figure 11. Relative Spectral Sensitivity vs. Wavelength

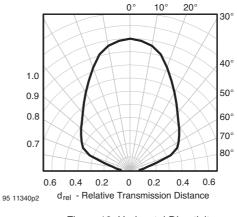


Figure 12. Horizontal Directivity $\phi_{\textbf{X}}$

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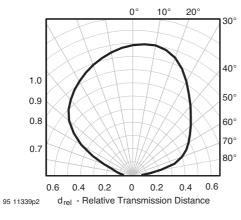


Figure 13. Vertical Directivity ϕ_y

Suitable Data Format

The circuit of the TSOP12..SB1 is designed in that way that unexpected output pulses due to noise or disturbance signals are avoided. A bandpass filter, an integrator stage and an automatic gain control are used to suppress such disturbances.

The distinguishing mark between data signal and disturbance signal are carrier frequency, burst length and duty cycle.

The data signal should fulfill the following conditions:

• Carrier frequency should be close to center frequency of the bandpass (e.g. 38 kHz).

• Burst length should be 10 cycles/burst or longer.

• After each burst which is between 10 cycles and 70 cycles a gap time of at least 14 cycles is necessary.

• For each burst which is longer than 1.8 ms a corresponding gap time is necessary at some time in the data stream. This gap time should be at least 4 times longer than the burst.

• Up to 800 short bursts per second can be received continuously.

Some examples for suitable data format are: NEC Code (repetitive pulse), NEC Code (repetitive data), Toshiba Micom Format, Sharp Code, RC5 Code, RC6 Code, R-2000 Code, Sony Code.

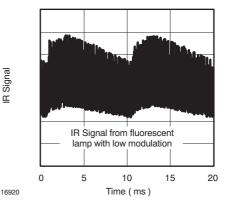
When a disturbance signal is applied to the TSOP12..SB1 it can still receive the data signal. However the sensitivity is reduced to that level that no unexpected pulses will occur.

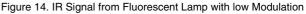
Some examples for such disturbance signals which are suppressed by the TSOP12..SB1 are:

DC light (e.g. from tungsten bulb or sunlight)

 Continuous signal at 38 kHz or at any other frequency

• Signals from fluorescent lamps with electronic ballast with high or low modulation (see Figure 14 or Figure 15).





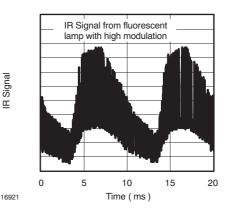
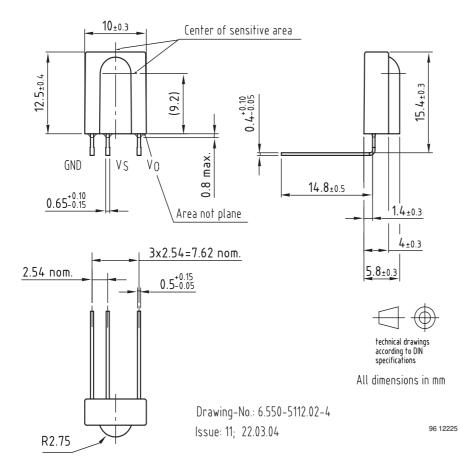


Figure 15. IR Signal from Fluorescent Lamp with high Modulation

TSOP12..SB1

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Package Dimensions in mm





TSOP12..SB1





Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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