TISP4290 SYMMETRICAL TRANSIENT VOLTAGE SUPPRESSORS

APRIL 1987 - REVISED SEPTEMBER 1997

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT	
Non-repetitive peak on-state pulse current (see Notes 1, 2 and 3)				
8/20 μs (ANSI C62.41, open-circuit voltage wave shape 1.2/50 μs)		150		
10/160 μs (FCC Part 68, open-circuit voltage wave shape 10/160 μs)		60		
5/200 μs (VDE 0433, open-circuit voltage wave shape 2 kV, 10/700 μs)		50		
0.2/310 µs (RLM 88, open-circuit voltage wave shape 1.5 kV, 0.5/700 µs)	I _{TSP}	38	Α	
5/310 μs (CCITT IX K17/K20, open-circuit voltage wave shape 2 kV, 10/700 μs)		50		
5/310 μs (FTZ R12, open-circuit voltage wave shape 2 kV, 10/700 μs)		50		
10/560 μs (FCC Part 68, open-circuit voltage wave shape 10/560 μs)		45		
10/1000 μs (REA PE-60, open-circuit voltage wave shape 10/1000 μs)		35		
Non-repetitive peak on-state current, 50 Hz, 2.5 s (see Notes 1 and 2)	I _{TSM}	10	A rms	
Initial rate of rise of on-state current, Linear current ramp, Maximum ramp value < 38 A	di _T /dt	250	A/µs	
Junction temperature	TJ	150	°C	
Operating free - air temperature range		0 to 70	°C	
Storage temperature range	T _{stg}	-40 to +150	°C	
Lead temperature 1.5 mm from case for 10 s	T _{lead}	260	°C	

- NOTES: 1. Above 70°C, derate linearly to zero at 150°C case temperature
 - 2. This value applies when the initial case temperature is at (or below) 70°C. The surge may be repeated after the device has returned to thermal equilibrium.
 - 3. Most PTT's quote an unloaded voltage waveform. In operation the TISP essentially shorts the generator output. The resulting loaded current waveform is specified.

electrical characteristics, T_J = 25°C

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _Z	Reference zener voltage	$I_Z = \pm 1 \text{mA}$	± 200			V
[∞] V _Z	Temperature coefficient of reference voltage			0.1		%/°C
V _(BO)	Breakover voltage	(see Notes 4 and 5)			± 290	V
I _(BO)	Breakover current	(see Note 4)	± 0.15		± 0.6	Α
V_{TM}	Peak on-state voltage	$I_T = \pm 5 \text{ A}$ (see Notes 4 and 5)		± 1.9	± 3	V
I _H	Holding current	(see Note 4)	± 150			mA
dv/dt	Critical rate of rise of off-state voltage	(see Note 6)			± 5	kV/μs
I _D	Off-state leakage current	$V_D = \pm 50 \text{ V}$			± 10	μА
C _{off}	Off-state capacitance	$V_D = 0$ $f = 1 \text{ kHz}$		110	200	pF

- NOTES: 4. These parameters must be measured using pulse techniques, t_w = 100 μ s, duty cycle \leq 2%.
 - 5. These parameters are measured with voltage sensing contacts seperate from the current carrying contacts located within 3.2 mm (0.125 inch) from the device body.
 - 6. Linear rate of rise, maximum voltage limited to 80 % $\rm V_{\rm Z}$ (minimum).

thermal characteristics

PARAMETER	MIN	TYP	MAX	UNIT
R _{0JA} Junction to free air thermal resistance			62.5	°C/W

PRODUCT INFORMATION

PARAMETER MEASUREMENT INFORMATION

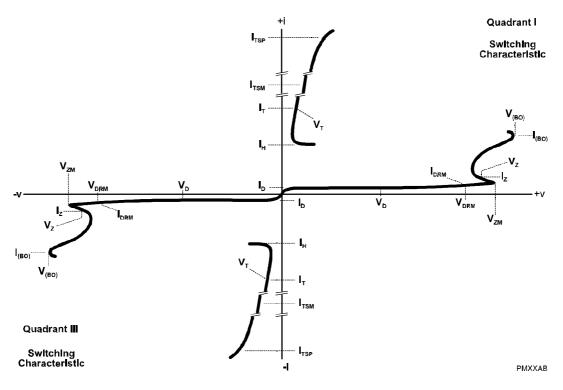


Figure 1. VOLTAGE-CURRENT CHARACTERISTIC FOR TERMINALS A AND B

TYPICAL CHARACTERISTICS A and B terminals

SURGE CURRENT

VS

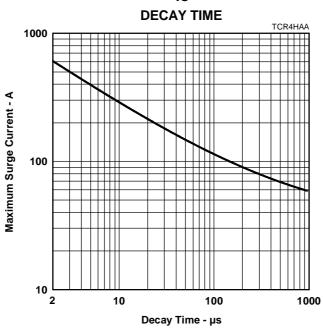


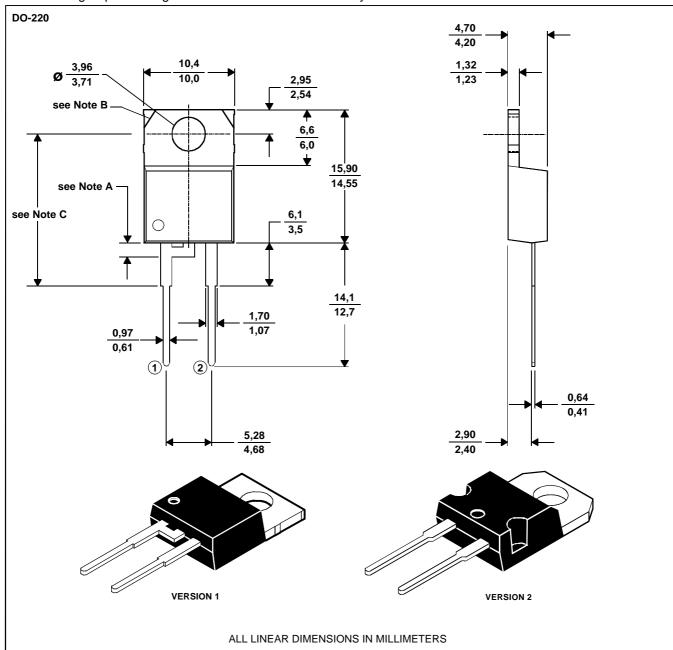
Figure 2.

MECHANICAL DATA

DO-220

2-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. Tie bar short stand off height according to package version.

Version 1, pin 1 is in electrical contact with the mounting tab via tie bar short - stand off height: 2,0 mm maximum.

Version 2, pin 1 is in electrical contact with the mounting tab (no external tie bar short).

- B. Mounting tab corner profile according to package version.
- C. Typical fixing hole centre stand off height according to package version. Version 1, 18,0 mm. Version 2, 17,6 mm.

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