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1 Electrical ratings

Table 2. Absolute maximum ratings

Cymhal	Parameter			Unit	
Symbol	Parameter	IPAK,DPAK	TO-220FP	TO-92	Unit
V _{DS}	Drain-source voltage		600		V
V _{GS}	Gate-source voltage		±30		V
I _D	Drain current (continuous) at T _C = 25 °C	2.0	2.0	0.5	Α
I _D	Drain current (continuous) at T _C =100 °C	1.26	1.26	0.32	Α
I _{DM} ⁽¹⁾	Drain current (pulsed)	8	8	2	Α
P _{TOT}	Total dissipation at T _C = 25 °C	45	20	3	W
	Derating factor	0.36	0.16	0.025	W/°C
V _{ESD(G-S)}	Gate source ESD (HBM-C=100 pF, R=1.5 kΩ)		2000		V
V _{ISO}	Insulation withstand voltage (DC)		2500		V
dv/dt ⁽²⁾	Peak diode recovery voltage slope	4.5			V/ns
T _J T _{stg}	Operating junction temperature Storage temperature	- 55 to 150		°C	
T _I	Maximum lead temperature for soldering purpose	30	0	260	°C

^{1.} Pulse width limited by safe operating area.

Table 3. Thermal data

Symbol	Symbol Parameter		TO-220FP	TO-92	Unit
R _{thj-case}	Thermal resistance junction-case max	2.77	6.25		°C/W
R _{thj-amb}	Thermal resistance junction-ambient max	100	62.5	120	°C/W
R _{thj-lead}	Thermal resistance junction-lead max			40	°C/W

Table 4. Avalanche data

Symbol	Parameter	Value	Unit
I _{AR}	Avalanche current, repetitive or not repetitive (pulse width limited by Tjmax)	2	А
E _{AS}	Single pulse avalanche energy (starting Tj = 25 °C, I _D =I _{AR} , V _{DD} = 50 mV)	120	mJ



^{2.} $I_{SD} \le 2$ A, di/dt ≤ 200 A/ μ s, $V_{DD} = 80\%V_{(BR)DSS}$.

2 Electrical characteristics

(T_{CASE} = 25 °C unless otherwise specified)

Table 5. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$I_D = 1 \text{ mA}, V_{GS} = 0$	600			V
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} =600 V, V _{DS} = 600 V,Tc=125 °C			1 50	μ Α μ Α
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ±20 V			±10	μΑ
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}$, $I_D = 50 \mu A$	3	3.75	4.5	V
R _{DS(on)}	Static drain-source on-resistance	V _{GS} = 10 V, I _D = 1 A		4.4	4.8	Ω

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
g _{fs} ⁽¹⁾	Forward transconductance	V _{DS} =15 V, I _D = 1 A	-	1.5		S
C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	V _{DS} = 25 V, f=1 MHz, V _{GS} =0	-	280 38 7		pF pF pF
C _{oss eq} ⁽²⁾	Equivalent output capacitance	V _{GS} =0, V _{DS} =0 to 480 V	1	30		pF
$egin{array}{c} Q_{ m g} \ Q_{ m gd} \end{array}$	Total gate charge Gate-source charge Gate-drain charge	V_{DD} =480 V, I_{D} = 2 A V_{GS} =10 V (see Figure 19)	-	11 2.25 6	15	nC nC nC

^{1.} Pulsed: pulse duration=300µs, duty cycle 1.5%

^{2.} $C_{oss\ eq}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Table 7. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time Rise time	V_{DD} =300 V, I_{D} =1 A, R_{G} =4.7 Ω , V_{GS} =10 V	-	10 30	-	ns ns
t _{d(off)}	Turn-off delay time Fall time	(see Figure 18)	-	23 50	-	ns ns

Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current Source-drain current (pulsed)		-		2 8	A A
V _{SD} ⁽²⁾	Forward on voltage	I _{SD} = 2 A, V _{GS} = 0	-		1.3	V
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I_{SD} =2 A, di/dt = 100 A/ μ s, V_{DD} =20 V	-	178 445 5		ns nC A
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I _{SD} =2 A, di/dt = 100 A/μs, V _{DD} =20 V, Tj=150 °C	-	200 500 5		ns nC A

^{1.} Pulse width limited by safe operating area

^{2.} Pulsed: pulse duration=300µs, duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area for TO-92

Figure 3. Thermal impedance for TO-92

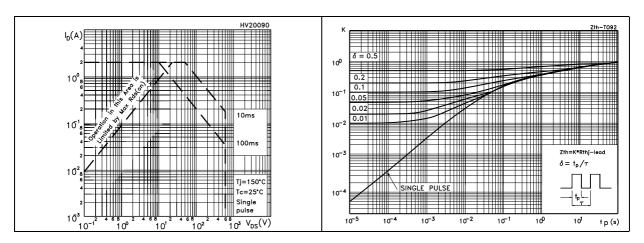


Figure 4. Safe operating area for TO-220FP

Figure 5. Thermal impedance for TO-220FP

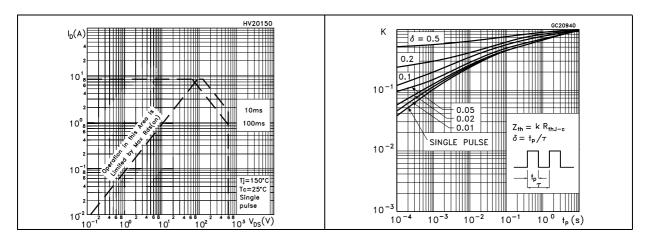
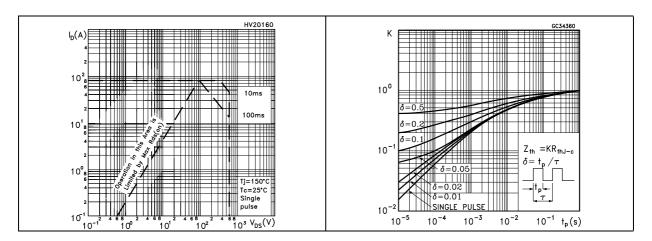


Figure 6. Safe operating area for IPAK/DPAK Figure 7. Thermal impedance for IPAK/DPAK



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Figure 8. Output characteristics

Figure 9. Transfer characteristics

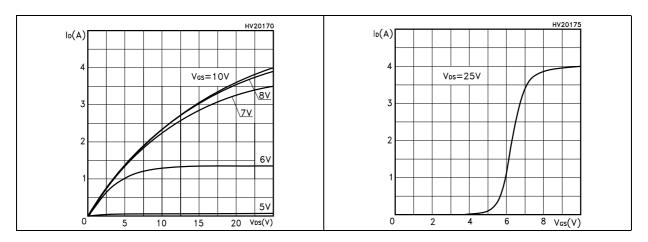


Figure 10. Normalized \mathbf{B}_{VDSS} vs temperature

Figure 11. Static drain-source on-resistance

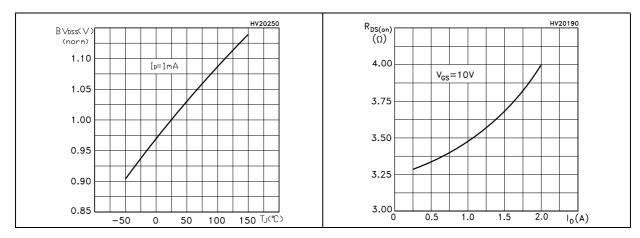
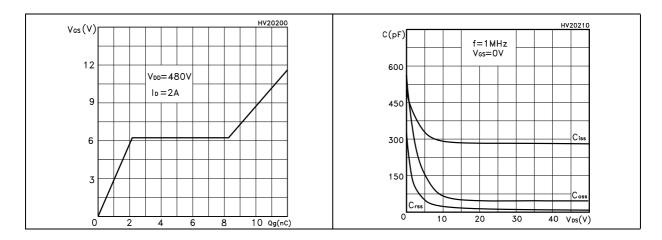


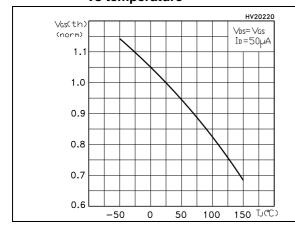
Figure 12. Gate charge vs gate-source voltage Figure 13. Capacitance variations



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Figure 14. Normalized gate threshold voltage Figure vs temperature

Figure 15. Normalized on-resistance vs temperature



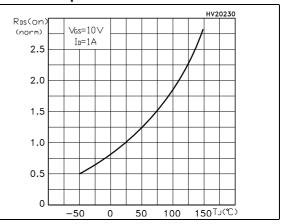
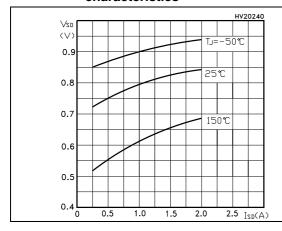
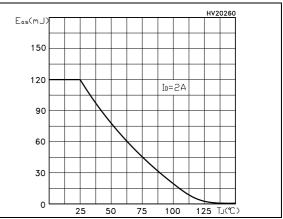


Figure 16. Source-drain diode forward characteristics

Figure 17. Maximum avalanche energy vs temperature





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3 Test circuits

Figure 18. Switching times test circuit for resistive load

Figure 19. Gate charge test circuit

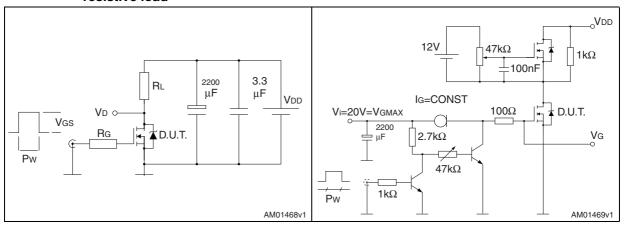


Figure 20. Test circuit for inductive load switching and diode recovery times

Figure 21. Unclamped inductive load test circuit

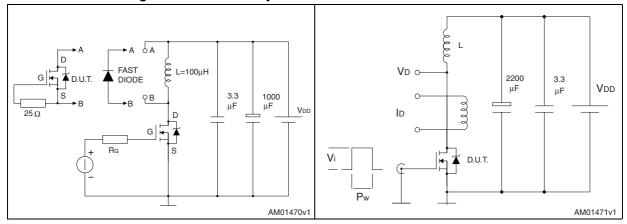
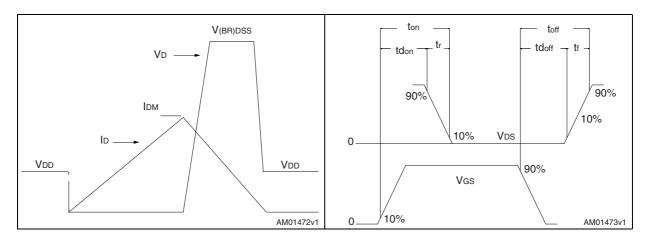


Figure 22. Unclamped inductive waveform

Figure 23. Switching time waveform



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4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 9. IPAK (TO-251) mechanical data

DIM		mm.	
DIM.	min.	typ	max.
А	2.20		2.40
A1	0.90		1.10
b	0.64		0.90
b2			0.95
b4	5.20		5.40
B5		0.3	
С	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
E	6.40		6.60
е		2.28	
e1	4.40		4.60
Н		16.10	
L	9.00		9.40
L1	0.80		1.20
L2		0.80	1.00
V1		10 °	

Figure 24. IPAK (TO-251) drawing

Table 10. DPAK (TO-252) mechanical data

Dim		mm	
Dim.	Min.	Тур.	Max.
А	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
С	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
Е	6.40		6.60
E1		4.70	
е		2.28	
e1	4.40		4.60
Н	9.35		10.10
L	1		1.50
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0°		8°

Figure 25. DPAK (TO-252) drawing

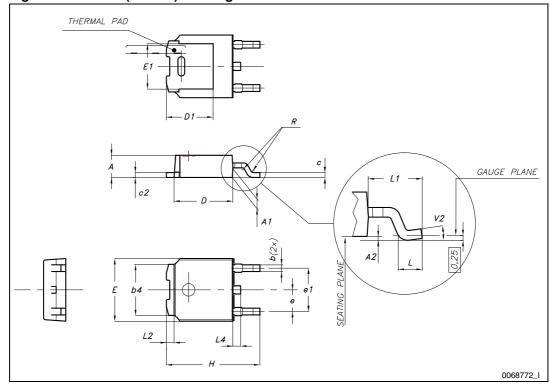
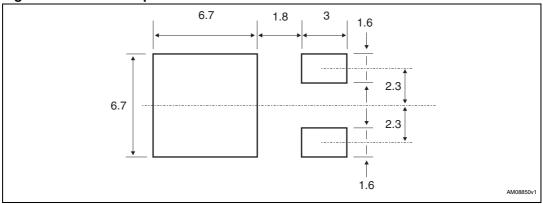


Figure 26. DPAK footprint^(a)

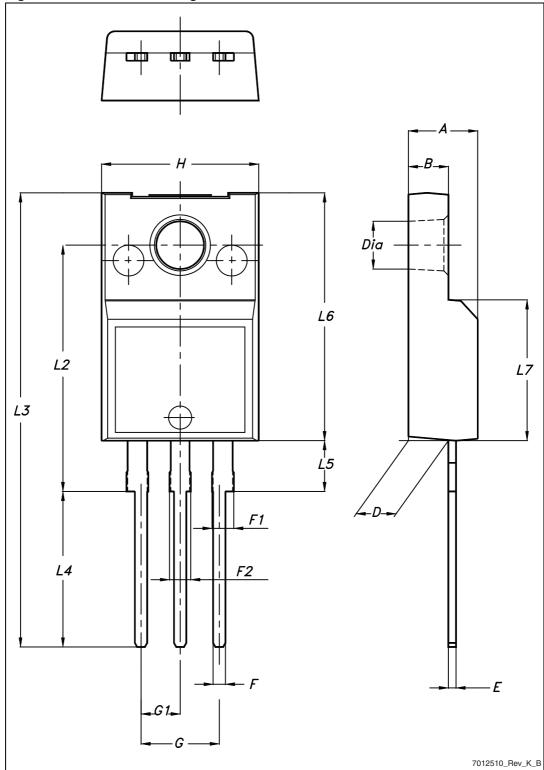


a. All dimensions are in millimeters

Table 11. TO-220FP mechanical data

Dim.		mm	
DIM. –	Min.	Тур.	Max.
А	4.4		4.6
В	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
Н	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

Figure 27. TO-220FP drawing



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Table 12. TO-92 ammopack mechanical data

Dim.		mm	
Dim.	Min.	Тур.	Max.
A1			4.80
Т			3.80
T1			1.60
T2			2.30
d			0.48
P0	12.50	12.70	12.90
P2	5.65	6.35	7.05
F1, F2	2.44	2.54	2.94
F3	4.98	5.08	5.48
delta H	-2.00		2.00
W	17.50	18.00	19.00
W0	5.70	6.00	6.30
W1	8.50	9.00	9.25
W2			0.50
Н	18.50		20.50
H3	0.5	1	1.5
H0	15.50	16.00	16.50
H1			25.00
D0	3.80	4.00	4.20
t			0.90
L			11.00
l1	3.00		
delta P	-1.00		1.00

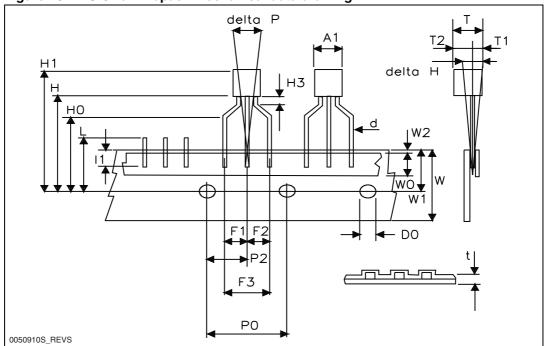


Figure 28. TO-92 ammopack mechanical data drawing

5 Packaging mechanical data

Table 13. DPAK (TO-252) tape and reel mechanical data

Таре				Reel		
Dim.	mm		Dim	mm		
	Min.	Max.	Dim.	Min.	Max.	
A0	6.8	7	Α		330	
В0	10.4	10.6	В	1.5		
B1		12.1	С	12.8	13.2	
D	1.5	1.6	D	20.2		
D1	1.5		G	16.4	18.4	
E	1.65	1.85	N	50		
F	7.4	7.6	Т		22.4	
K0	2.55	2.75			•	
P0	3.9	4.1		Base qty.	2500	
P1	7.9	8.1		Bulk qty.	2500	
P2	1.9	2.1			•	
R	40					
Т	0.25	0.35				
W	15.7	16.3				

Figure 29. Tape for DPAK (TO-252)

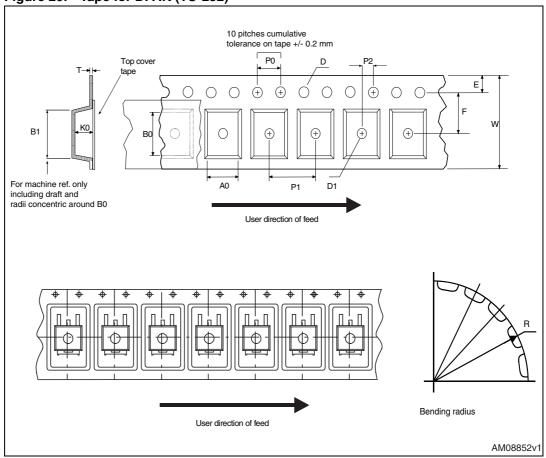
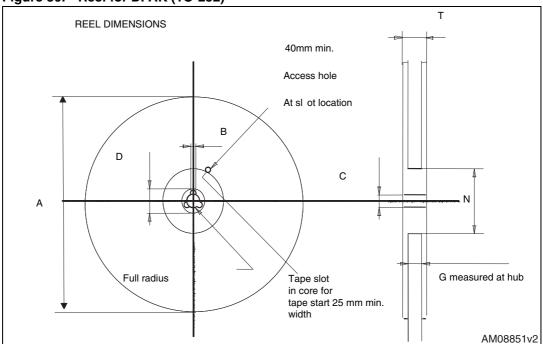


Figure 30. Reel for DPAK (TO-252)



6 Revision history

Table 14. Document revision history

Date	Revision	Changes
09-Mar-2004	1	First release
23-Mar-2004	2	Modified title
02-Apr-2005	3	Added new section: Electrical characteristics (curves)
06-Mar-2006	4	Inserted DPAK. The document has been reformatted
25-May-2012	5	Corrected unit in Table 5: On/off states

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