

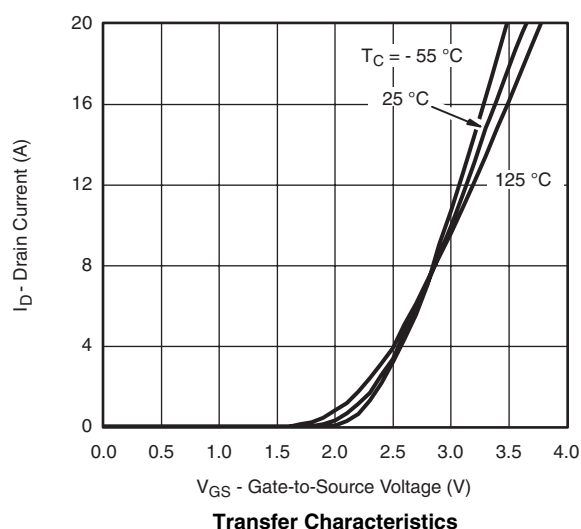
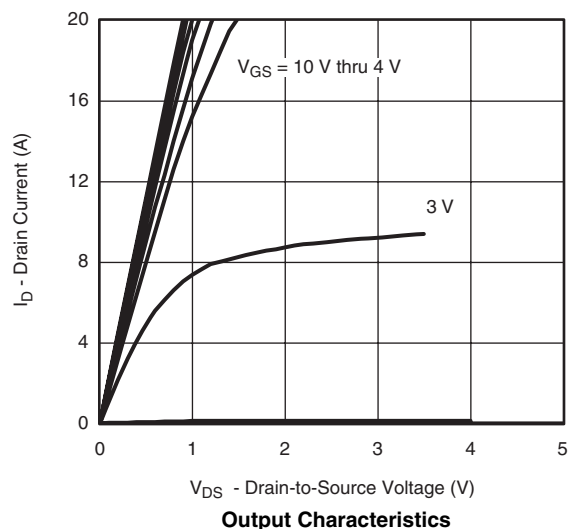
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250\text{ }\mu\text{A}$	-0.8		-2.2	V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		-40		$\text{mV}/^{\circ}\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			3.4		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 16\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -40\text{ V}$, $V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -40\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^{\circ}\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}$, $V_{GS} = -10\text{ V}$	-20			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}$, $I_D = -4.5\text{ A}$		0.045	0.054	Ω
		$V_{GS} = -15\text{ V}$, $I_D = -4.5\text{ A}$		0.059	0.072	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}$, $I_D = -4.5\text{ A}$		13		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7\text{ A}$, $V_{GS} = 0\text{ V}$		-0.79	-1.2	V
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -20\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$		805		pF
Output Capacitance	C_{oss}			120		
Reverse Transfer Capacitance	C_{rss}			85		
Total Gate Charge	Q_g	$V_{DS} = -20\text{ V}$, $V_{GS} = -4.5\text{ V}$, $I_D = -4.5\text{ A}$		9	14	nC
Gate-Source Charge	Q_{gs}			2		
Gate-Drain Charge	Q_{gd}			3.6		
Gate Resistance	R_g	$f = 1\text{ MHz}$		11.5	18	Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{ V}$, $R_L = 15\text{ }\Omega$ $I_D \cong -1\text{ A}$, $V_{GEN} = -10\text{ V}$, $R_g = 6\text{ }\Omega$		8	13	ns
Rise Time	t_r			12	18	
Turn-Off Delay Time	$t_{d(off)}$			74	110	
Fall Time	t_f			38	60	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 1.7\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$		27	45	nC
Body Diode Reverse Recovery Charge	Q_{rr}			17	26	

Notes:

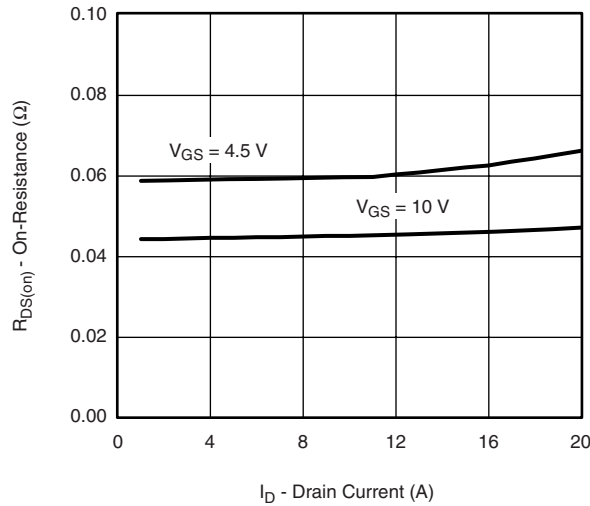
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

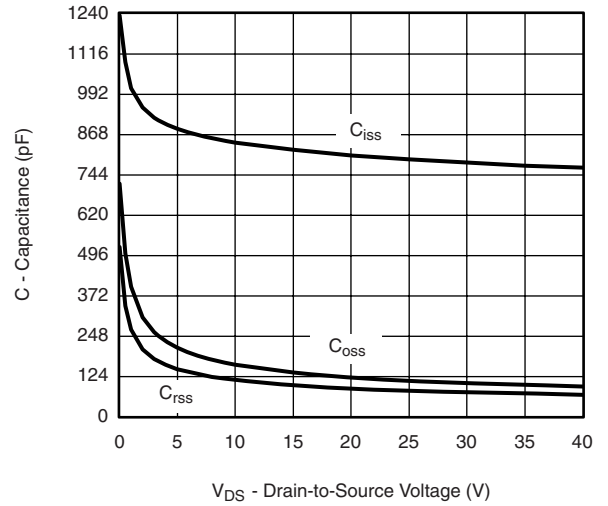
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS $25\text{ }^{\circ}\text{C}$, unless otherwise noted

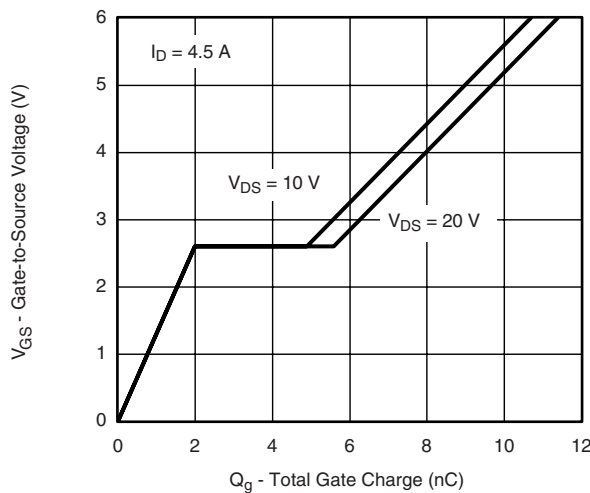
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



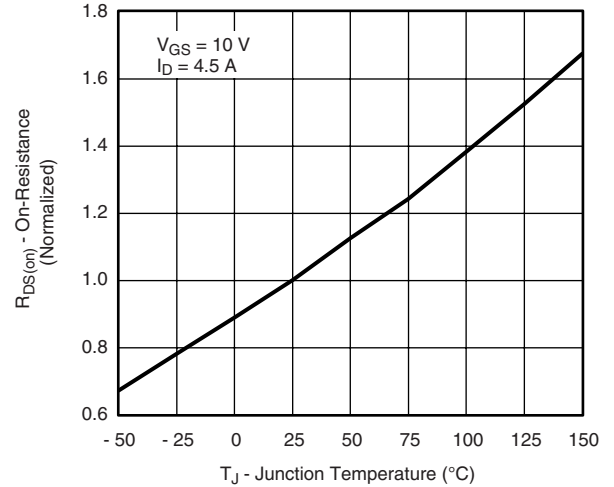
On-Resistance vs. Drain Current



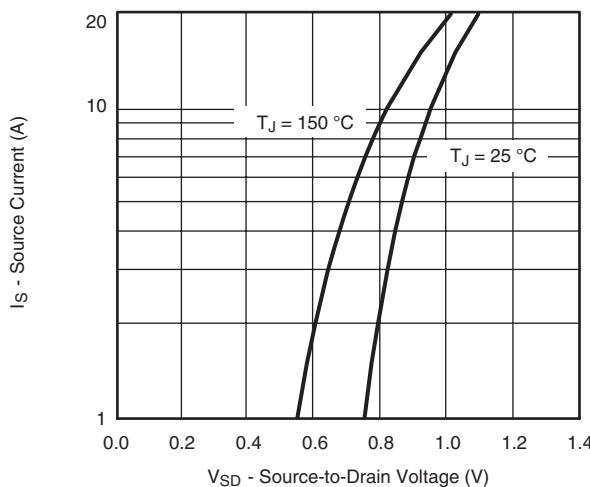
Capacitance



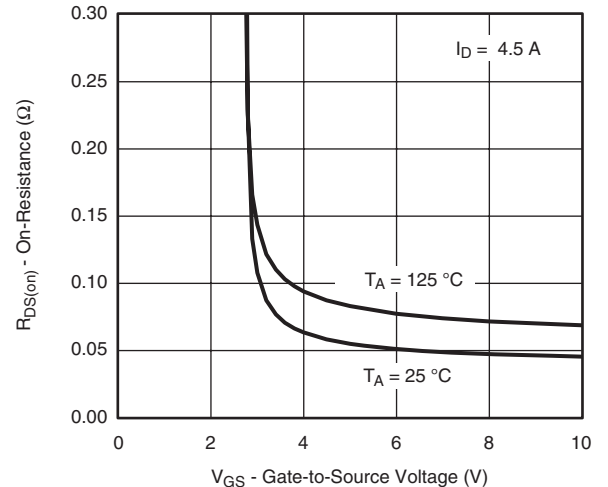
Gate Charge



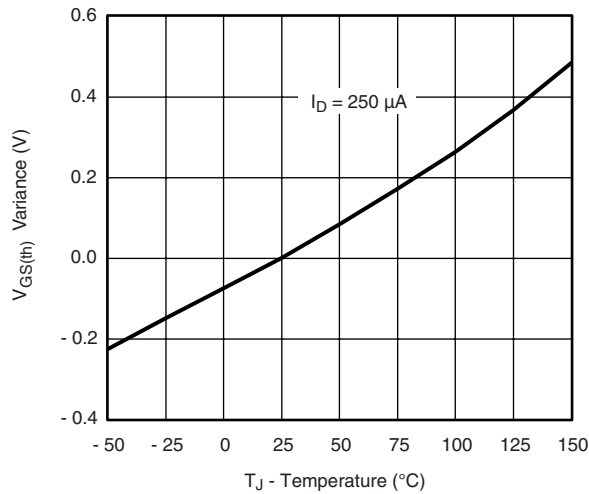
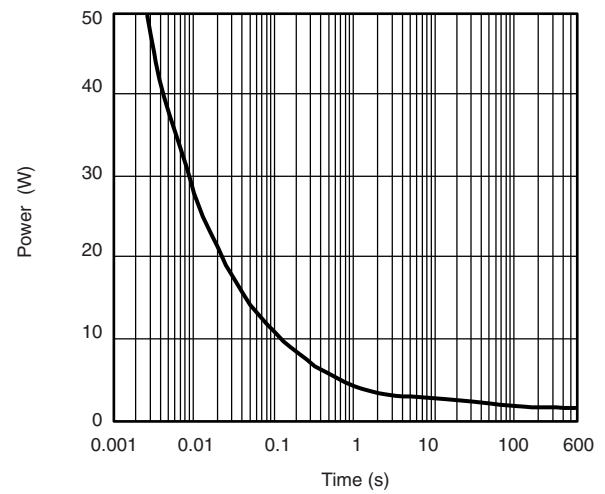
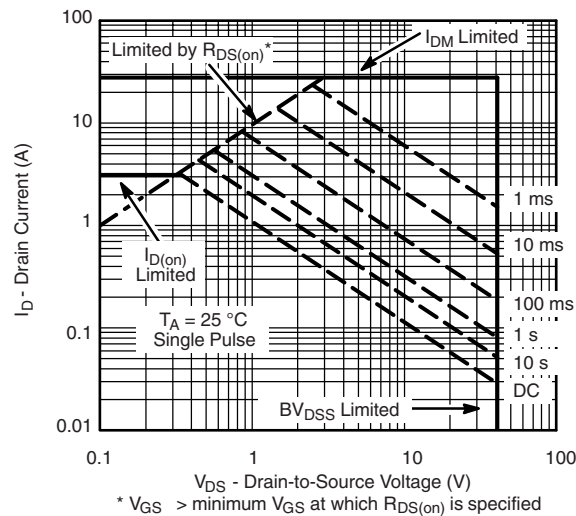
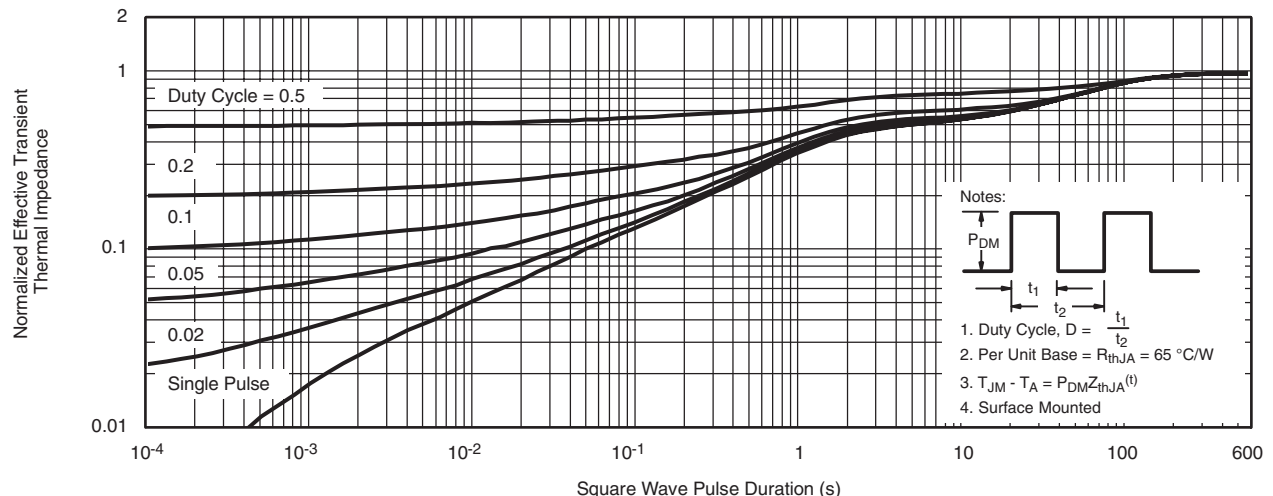
On-Resistance vs. Junction Temperature



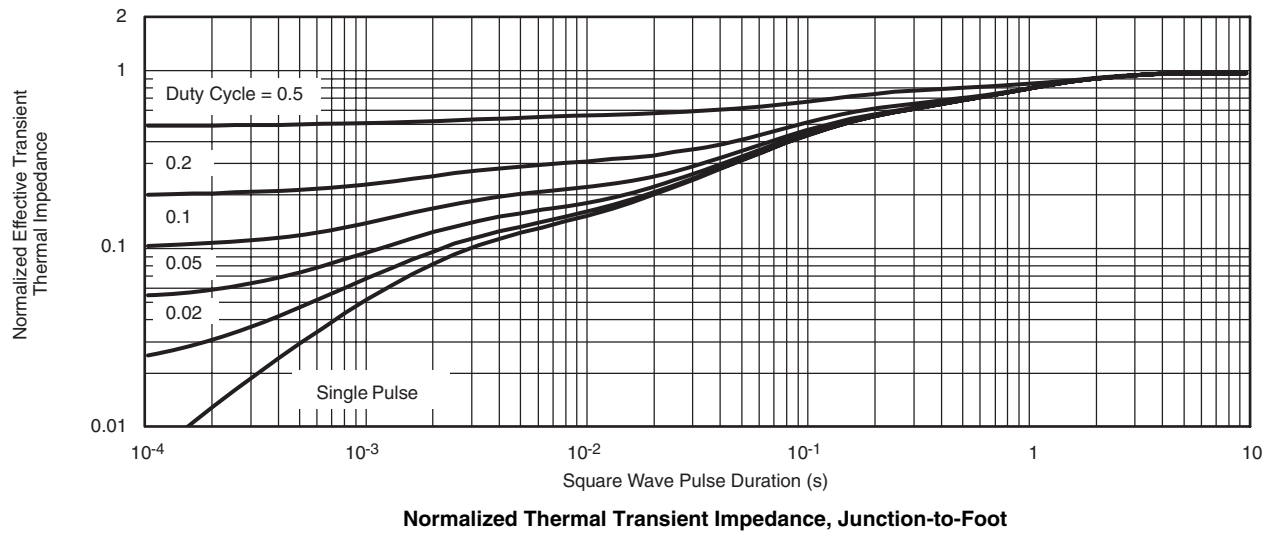
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Threshold Voltage****Single Pulse Power, Junction-to-Ambient****Safe Operating Area****Normalized Thermal Transient Impedance, Junction-to-Ambient**

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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