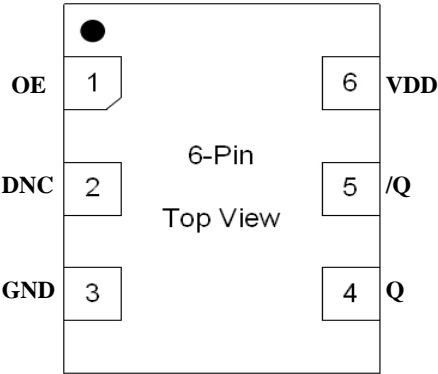


Ordering Information

Ordering Part Number	Marking Line 1	Marking Line 3	Shipping	Package
MX574BBF644M531	MX574BB	F644M531	Tube	6-Pin 7mm x 5mm LGA
MX574BBF644M531 TR	MX574BB	F644M531	Tape and Reel	6-Pin 7mm x 5mm LGA

Devices are Green and RoHS compliant. Sample material may have only a partial top mark.

Pin Configuration



Pin Description

Pin Number	Pin Name	Pin Type	Pin Level	Pin Function
1	OE	I, SE	LVC MOS	Output Enable, disables output to tri-state, 1 = Disabled, 0 = Enabled, 50k Ohms Pull-Down
2	DNC			Make no connection, leave floating.
3	GND	PWR		Power Supply Ground
4, 5	Q, /Q	O, Diff	LVPECL	Clock Output Frequency = 644.53125MHz
6	VDD	PWR		Power Supply

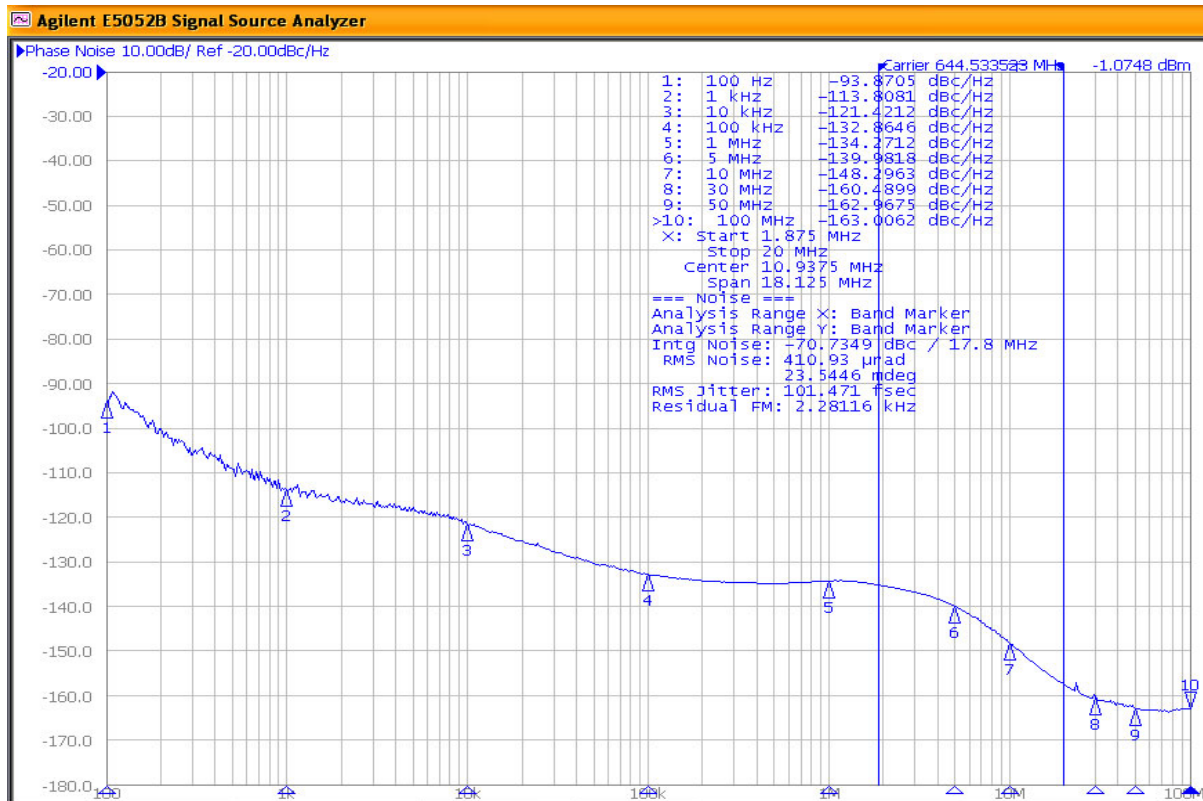


Figure 1. LVPECL Output 644.53125MHz 1.875MHz-20MHz 101fs

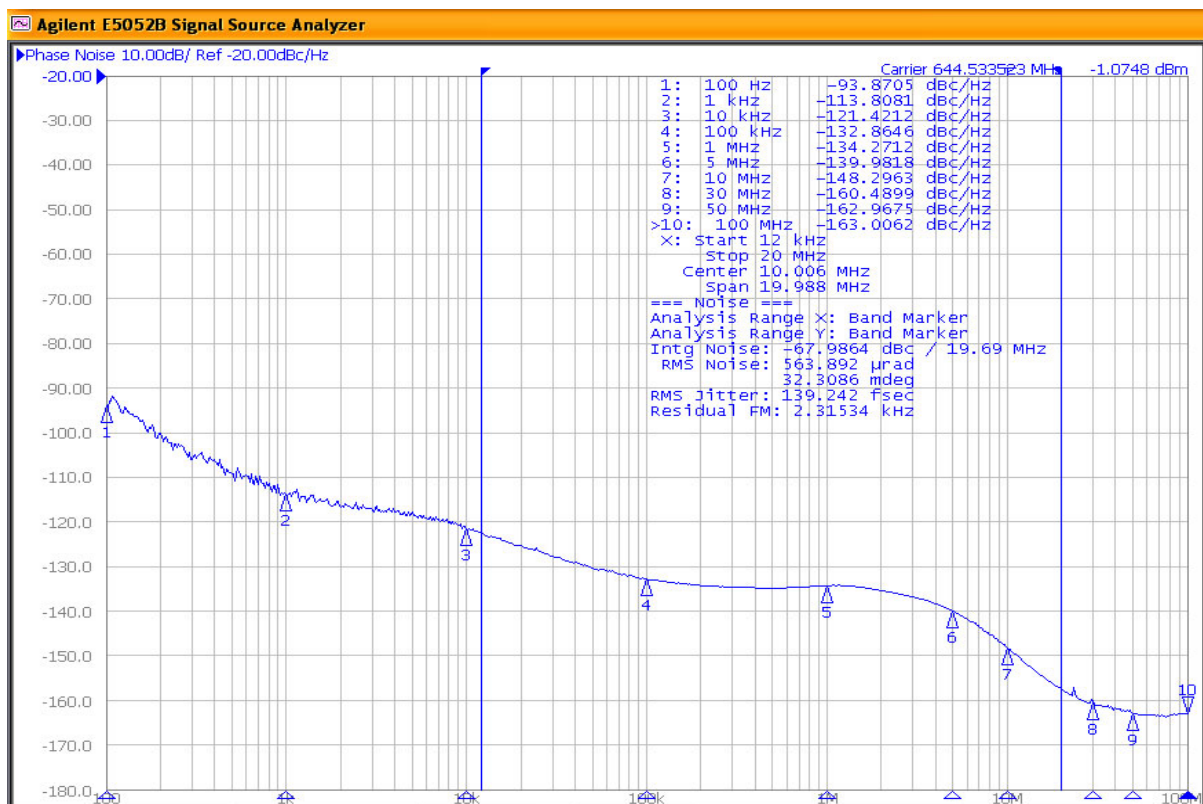
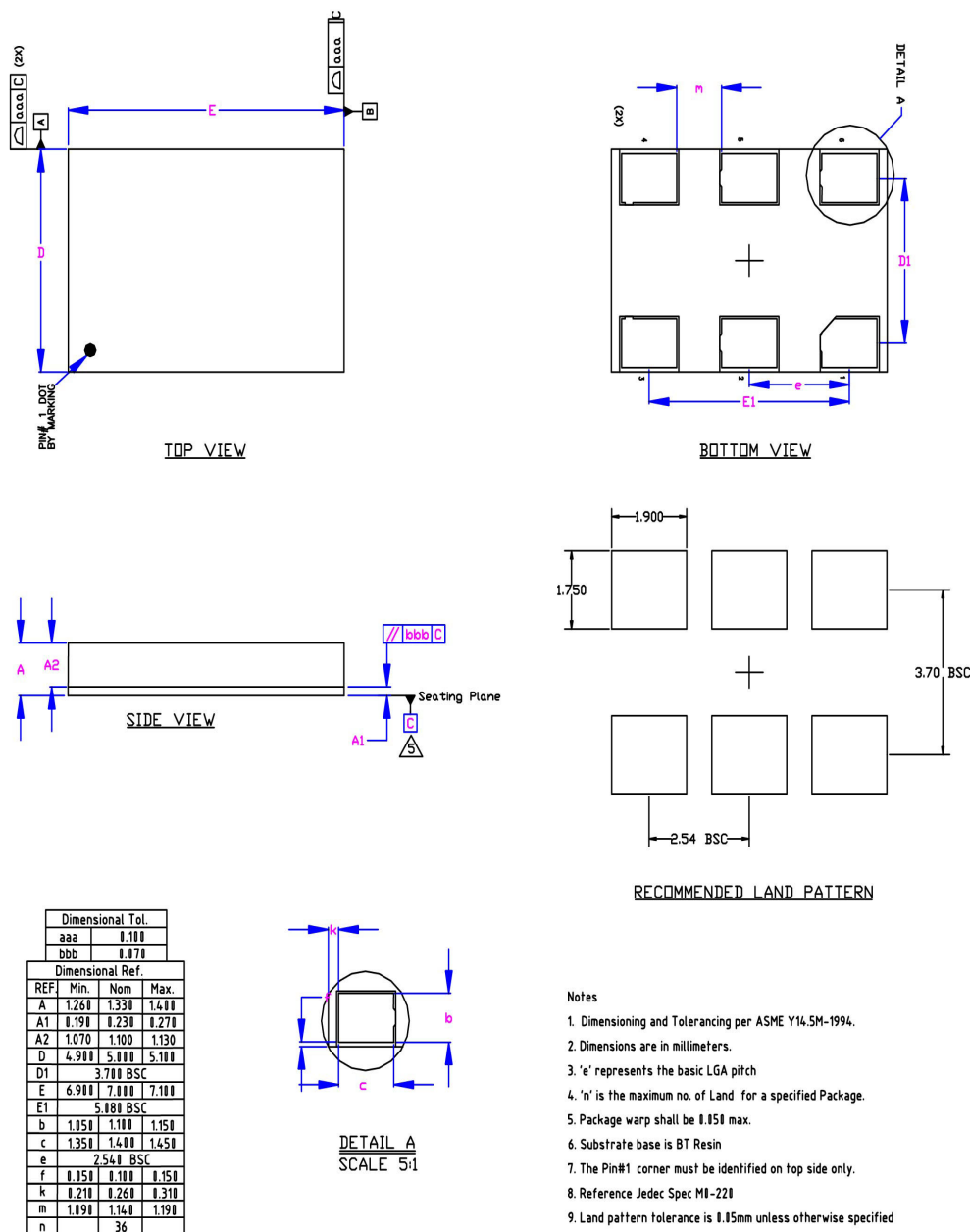


Figure 2. LVPECL Output 644.53125MHz 12kHz-20MHz 139fs

## Package Information and Recommended Land Pattern for 6-Pin LGA<sup>3</sup>



### 6-Pin LGA (7x5mm)

#### Note:

3. Package information is correct as of the publication date. For updates and most current information, go to [www.microchip.com](http://www.microchip.com).

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