### MBRA140TRPbF

## Vishay High Power Products Schottky Rectifier, 1.0 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum forward voltage drop See fig. 1		1 A	T <sub>J</sub> = 25 °C	0.55	V		
		2 A		0.71			
	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 100 °C	0.5			
	V FM \''	2 A		0.65			
		1 A	- T <sub>J</sub> = 125 °C	0.49			
		2 A		0.63			
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.5	mA		
		T <sub>J</sub> = 100 °C		10			
		T <sub>J</sub> = 125 °C		26			
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.36	V		
Forward slope resistance	r <sub>t</sub>			104	mΩ		
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V <sub>DC</sub> , T <sub>J</sub> = 25 °C, test signal = 1 MHz		38	pF		
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/μs		

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 150	°C		
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation See fig. 4	35	°C/W		
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>		80	C/VV		
Approximate weight			0.07	g		
			0.002	oz.		
Device marking		Case style SMA (similar D-64)	V1	V14		

#### Notes

For technical questions, contact: diodestech@vishay.com

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<sup>(1)</sup>  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

 $<sup>^{(2)}</sup>$  Mounted 1" square PCB, thermal probe connected to lead 2 mm from package



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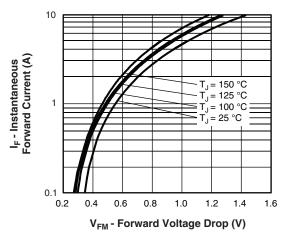


Fig. 1 - Maximum Forward Voltage Drop Characteristics

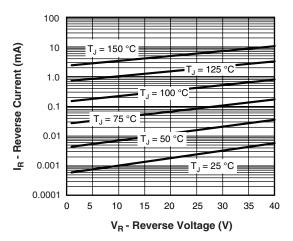


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

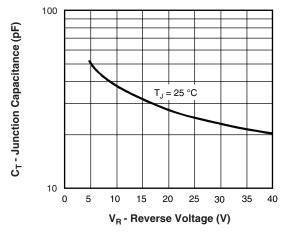


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

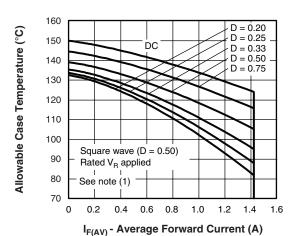


Fig. 4 - Maximum Average Forward Current vs.
Allowable Lead Temperature

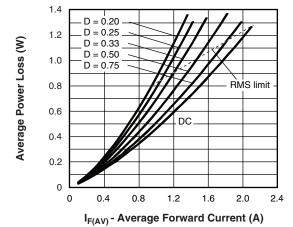
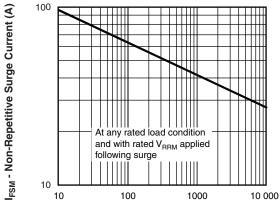


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t<sub>n</sub> - Square Wave Pulse Duration (μs)

Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

(1) Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

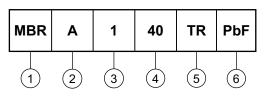
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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Schottky MBR series

2 - A = SMA

3 - Current rating (1 = 1 A)

4 - Voltage rating (40 = 40 V)

TR = Tape and reel (7500 pieces)

6 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95018			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			

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