

12V, Ultra-Low IQ, Low-Dropout Linear Regulators

ABSOLUTE MAXIMUM RATINGS

IN to GND	-14V to +14V	OUT Short Circuit.....	Indefinite
SHDN to GND.....	-0.3V to (V _{IN} + 0.3V)	Continuous Power Dissipation (T _A = +70°C)	
	(-0.3V to +0.3V when V _{IN} < 0V)	5-Pin SOT23 (derate 7.1mW/°C above +70°C).....	571mW
OUT, FB (MAX1725 only) to GND.....	-0.3V to +6V when	Junction Temperature	+150°C
	V _{IN} > 5.7V; -0.3V to (V _{IN} + 0.3V) when 0V <	Operating Temperature Range	-40°C to +85°C
	V _{IN} < 5.7V; -0.3V to +0.3V when V _{IN} < 0V	Storage Temperature Range	-65°C to +150°C
OUT Continuous Current.....	200mA	Lead Temperature (soldering, 10s)	+300°C
		Soldering Temperature (reflow)	+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

PACKAGE THERMAL CHARACTERISTICS (Note 1)

Junction-to-Ambient Thermal Resistance (θ_{JA}).....140°C/W

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maximintegrated.com/thermal-tutorial.

ELECTRICAL CHARACTERISTICS

(Circuit of Figure 1, $V_{IN} = V_{OUT} + 1V$, $SHDN = IN$, $I_{OUT} = 1mA$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Input Voltage Range	V _{IN}	(Note 3)		2.5		12	V
Supply Current	I _{IN}	V _{IN} = 12V			2	4.5	μA
Shutdown Supply Current	I _{IN} ($\overline{\text{SHDN}}$)	V $\overline{\text{SHDN}}$ = 0V, V _{IN} = 12V, V _{OUT} = 0V			0.7	2	μA
OUT Voltage Range	V _{OUT}	MAX1725 only		1.5		5	V
FB Voltage	V _{FB}	MAX1725 only (Note 4)			1.245		V
OUT Voltage Accuracy (Note 4)		T _A = +25°C		-1.5		1.5	%
		T _A = 0°C to +85°C		-2		+2	
		T _A = -40°C to +85°C		-3		+3	
Line Regulation	ΔV _{LNR}	V _{IN} = 2.5V or (V _{OUT} + 0.5V) to 12V			0.01	0.3	%/V
Load Regulation	ΔV _{LDR}	I _{OUT} = 1mA to 20mA, V _{IN} = (V _{OUT} + 1V) or 3V (min)			0.015	0.15	%/mA
Maximum OUT Current	I _{OUT}	V _{IN} = (V _{OUT} + 1V) or 3V (Note 5)		20	80		mA
Dropout Voltage (Note 5)	ΔV _{DO}	I _{OUT} = 20mA for V _{OUT} ≥ 2.5V, I _{OUT} = 10mA for V _{OUT} < 2.5V, not applicable for V _{OUT} < 1.9V			300	600	mV
Foldback Current Limit	I _{SC}	V _{IN} = 12V, V _{OUT} = 0V			40		mA
$\overline{\text{SHDN}}$ Input Threshold	V _{IH}	V _{IN} = 2.5V to 12V		2			V
	V _{IL}					0.5	
$\overline{\text{SHDN}}$ Input Bias Current	I $\overline{\text{SHDN}}$	V $\overline{\text{SHDN}}$ = 0V to 12V	T _A = +25°C	-50		+50	nA
			T _A = +85°C			0.1	
FB Input Bias Current (MAX1725 only)	I _{FB}	V _{FB} = 1.25V	T _A = +25°C	-20	2	+20	nA
			T _A = +85°C			1.5	
IN Reverse Leakage Current		V _{IN} = -12V, V $\overline{\text{SHDN}}$ = 0V			0.01	10	μA
Thermal-Shutdown Threshold		Temperature rising			150		°C
Thermal-Shutdown Hysteresis					15		°C

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ELECTRICAL CHARACTERISTICS (continued)

(Circuit of Figure 1, $V_{IN} = V_{OUT} + 1V$, $SHDN = IN$, $I_{OUT} = 1mA$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
OUT Line-Transient Overshoot/Undershoot		$\Delta V_{IN} = \pm 0.25V$, $t_R = t_F = 5\mu s$		15		mV
OUT Load-Transient Overshoot/Undershoot		I_{OUT} from 1mA to 10mA, $t_R = t_F = 1\mu s$		200		mV
OUT Noise	e_n	$f = 10Hz$ to $100kHz$		350		μV_{RMS}

Note 2: Limits are 100% production tested at $+25^{\circ}C$. All temperature limits are guaranteed by design.

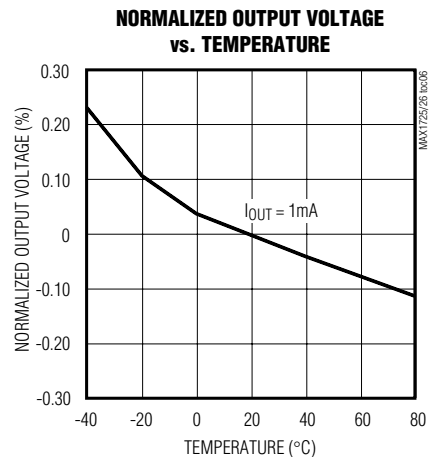
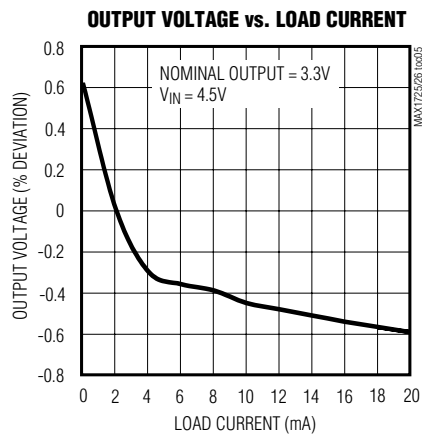
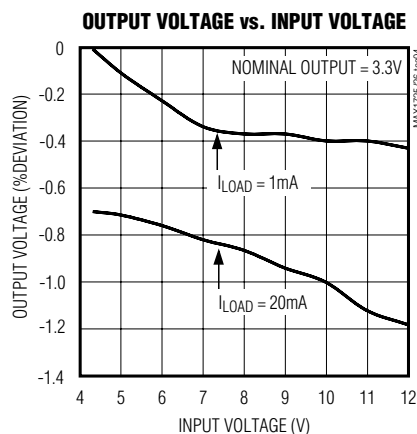
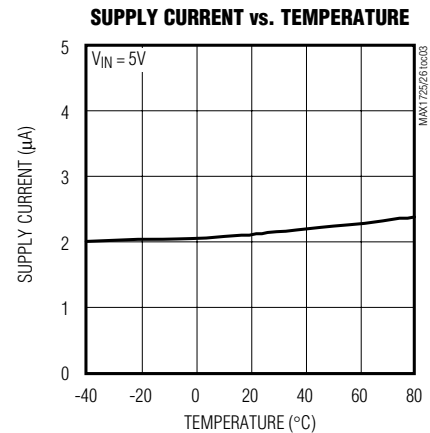
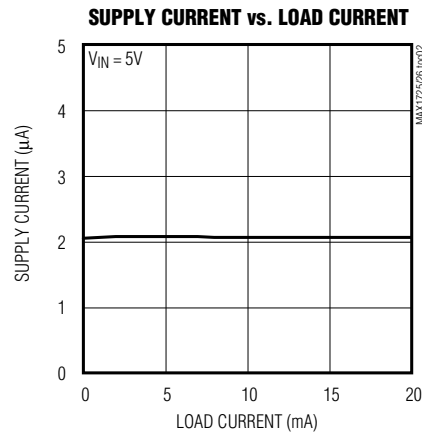
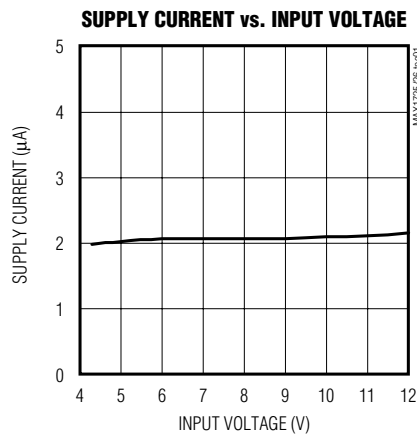
Note 3: Guaranteed by OUT line-regulation testing.

Note 4: OUT accuracy from nominal voltage. The MAX1725 is tested at $V_{OUT} = 1.5V$, $2.5V$, and $5V$.

Note 5: When V_{OUT} falls to 4% below its value at $V_{IN} = V_{OUT} + 1V$.

Typical Operating Characteristics

($V_{OUT} = +3.3V$, $I_{OUT} = 1mA$, $T_A = +25^{\circ}C$, unless otherwise noted.)

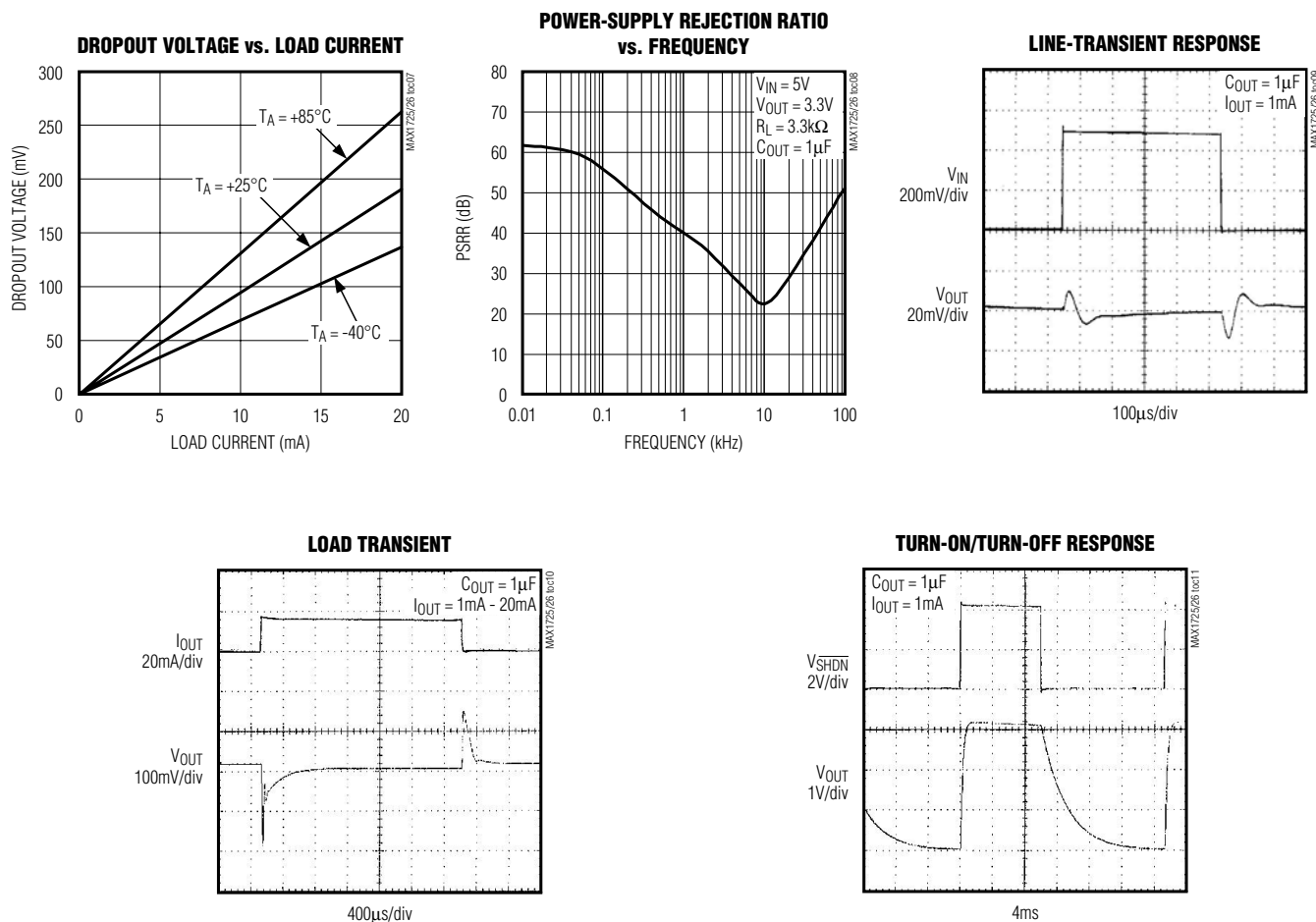


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Typical Operating Characteristics (continued)

($V_{OUT} = +3.3V$, $I_{OUT} = 1mA$, $T_A = +25^{\circ}C$, unless otherwise noted.)



Pin Description

PIN		NAME	FUNCTION
MAX1725	MAX1726		
1	1	IN	Supply Voltage Input
2	2	GND	Ground
3	3	OUT	Voltage Output
4	—	FB	Feedback Voltage Input
—	4	GND	Ground
5	5	SHDN	Active-Low Shutdown Input

Detailed Description

The MAX1725/MAX1726 are low-dropout, low-quiescent-current linear regulators designed primarily for battery-powered applications (Figure 1). The MAX1725 provides an adjustable output voltage from 1.5V to 5V using an external resistor-divider. The MAX1726 supplies preset output voltages of 1.8V, 2.5V, 3.3V, or 5V. These devices consist of a +1.245V error amplifier, MOSFET driver, and p-channel pass transistor (Figure 2).

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The error amplifier compares 1.245V to the selected feedback voltage and amplifies the difference. If the feedback voltage is lower than 1.245V, the pass-transistor gate is pulled lower, allowing more current to pass, and thus increasing the output voltage. If the feedback voltage is higher than 1.245V, the pass-transistor

gate is driven higher, allowing less current to pass to the output. The output voltage is fed back through either an internal resistor voltage-divider connected to OUT (MAX1726) or an external resistor network connected to FB (MAX1725). Additional features include an output current limiter, reverse-battery protection, a thermal sensor, and shutdown logic.

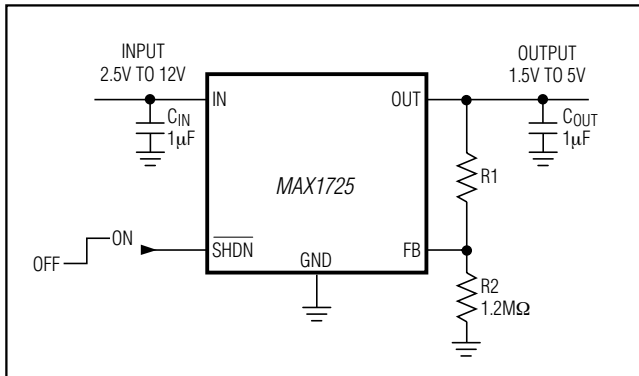


Figure 1. Typical Application Circuit

Internal P-Channel Pass Transistor

The MAX1725/MAX1726 feature a p-channel MOSFET pass transistor. This provides advantages over similar designs using PNP pass transistors, including longer battery life. The p-channel MOSFET requires no base drive, which reduces quiescent current considerably. PNP-based regulators waste considerable current in dropout when the pass transistor saturates; they also use high base-drive currents under large loads. The MAX1725/MAX1726 do not suffer from these problems, and consume only 2µA of quiescent current throughout their load range (see the *Typical Operating Characteristics*).

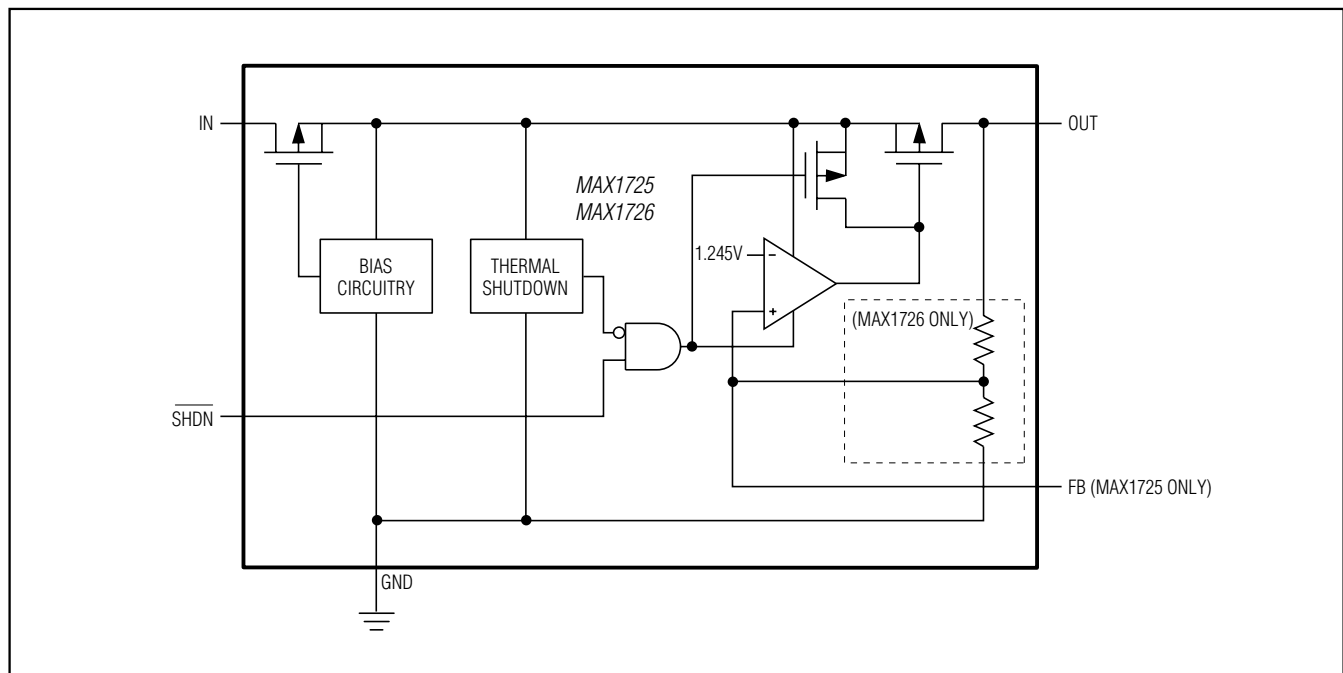


Figure 2. Functional Diagram

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Shutdown

To enter shutdown, drive the $\overline{\text{SHDN}}$ pin below 0.5V. When the MAX1725/MAX1726 are shut down, the output pass transistor shuts off, the output falls to ground, and supply current drops from 2 μA to 0.7 μA . Connect $\overline{\text{SHDN}}$ to IN for normal operation. If reverse-battery protection is needed, drive $\overline{\text{SHDN}}$ through a 100k Ω resistor.

Thermal-Overload Protection

Thermal-overload protection limits total power dissipation in the MAX1725/MAX1726. When the junction temperature exceeds $T_J = +150^\circ\text{C}$, the thermal sensor signals the shutdown logic, turning off the pass transistor and allowing the IC to cool. The thermal sensor turns the pass transistor on again after the IC's junction temperature cools by 15°C , resulting in a pulsed output during continuous thermal-overload conditions.

Thermal-overload protection is designed to protect the devices in the event of fault conditions. For continuous operation, do not exceed the absolute maximum junction temperature rating of $T_J = +150^\circ\text{C}$.

Foldback Current Limiting

The MAX1725/MAX1726 also include a foldback current limiter. When the output is shorted to ground, the output PMOS drive is limited so that the output current does not exceed 40mA (typ). The output can be shorted to ground indefinitely without damaging the part.

Reverse-Battery Protection

The MAX1725/MAX1726 have a unique protection scheme that limits the reverse supply current to less than 10 μA when V_{IN} is forced below ground. The circuitry monitors the polarity of the input, disconnecting the internal circuitry and parasitic diodes when the battery is reversed. This feature prevents the device, battery, and load from overheating and electrical stress. For reverse-battery protection, drive $\overline{\text{SHDN}}$ through a 100k Ω resistor.

Applications Information

Capacitor Selection and Regulator Stability

For general purposes, use a 1 μF capacitor on the MAX1725/MAX1726 input and output. Larger input capacitor values and lower ESR provide better supply-

noise rejection and transient response. A higher-value input capacitor (10 μF) may be necessary if large, fast transients are anticipated and the device is located several inches from the power source. For stable operation

$$V_{\text{OUT}} = V_{\text{FB}} \left(1 + \frac{R1}{R2} \right)$$

over the full temperature range, use a minimum of 1 μF on the output.

$$R1 = R2 \left(\frac{V_{\text{OUT}}}{V_{\text{FB}}} - 1 \right)$$

Output-Voltage Selection

For fixed 1.8V, 2.5V, 3.3V, or 5V output voltages, use the MAX1726.

The MAX1725 features an adjustable output voltage from 1.5V to 5V, using two external resistors connected as a voltage-divider to FB (Figure 1). The MAX1725 is optimized for operation with $R2 = 1.2\text{M}\Omega$. The output voltage is set by the following equation:

where typically $V_{\text{FB}} = 1.245\text{V}$. To simplify resistor selection:

Choose $R2 = 1.2\text{M}\Omega$ for best accuracy.

Power-Supply Rejection and Operation from Sources Other than Batteries

The MAX1725/MAX1726 are designed to deliver low dropout voltages and low quiescent currents in battery-powered systems. Power-supply rejection is 60dB at low frequencies and rolls off above 100Hz. At high frequencies, the output capacitor is the major contributor to the rejection of power-supply noise (see the Power-Supply Rejection Ratio vs. Frequency graph in the *Typical Operating Characteristics*).

When operating from sources other than batteries, improve supply-noise rejection and transient response by increasing the value of the input and output capacitors, and by using passive filtering techniques (see the Supply and Load Transient Response graph in the *Typical Operating Characteristics*).

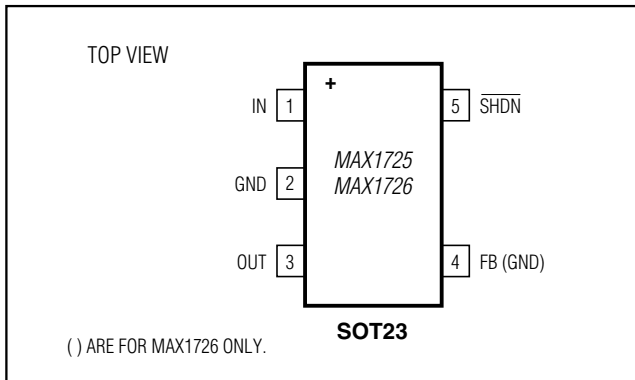
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Chip Information

TRANSISTOR COUNT: 112

Pin Configuration



Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
5 SOT23	U5+1	21-0057	90-0174

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
2	11/12	Added MAX1726EUK26/V+ and "+" lead(Pb)-free designations to <i>Ordering Information</i>	1



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