Ordering Information

Part N	umber‡		Temperature	
Standard	RoHS Compliant**	Range	Package	
LM2576BT*†	LM2576WT*†	–40°C to +85°C	TO-220-5	
LM2576-3.3BT [†]	LM2576-3.3WT [†]	–40°C to +85°C	TO-220-5	
LM2576-5.0BT ⁺	LM2576-5.0WT [†]	–40°C to +85°C	TO-220-5	
LM2576-12BT [†]	LM2576-12WT [†]	–40°C to +85°C	TO-220-5	
LM2576BU*	LM2576WU*	–40°C to +85°C	TO-263-5	
LM2576-3.3BU	LM2576-3.3WU	–40°C to +85°C	TO-263-5	
LM2576-5.0BU	LM2576-5.0WU	–40°C to +85°C	TO-263-5	
LM2576-12BU	LM2576-12WU	–40°C to +85°C	TO-263-5	

* Adjustable output regulators.

**RoHS compliant with "hot-melting solder" exemption.

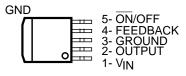
[†] Contact factory for bent or staggered leads option.

Pin Configurations



5-LEAD TO-263 (U)





Absolute Maximum Ratings (Note 1)

Operating	Ratings
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Temperature Range Supply Voltage $-40^{\circ}C \leq T_{_J} \leq +125^{\circ}C \\ 40V$

Maximum Supply Voltage	45V −0.3V ≤ V ≤ +40V
ON/OFF Pin Input Voltage	-0.3V ≤ V ≤ +40V -1V
Output Voltage to Ground (Steady State)	
Power Dissipation	Internally Limited
Storage Temperature Range	–65°C to +150°C
Minimum ESD Rating	
$C = 100 pF, R = 1.5 k\Omega$	2 kV
FB Pin	1 kV
Lead Temperature (soldering, 10 sec.)	260°C
Maximum Junction Temperature	150°C

Electrical Characteristics Specifications with standard typeface are for $T_J = 25^{\circ}C$, and those with **boldface type** apply over **full Operating Temperature Range**. Unless otherwise specified, $V_{IN} = 12V$, and $I_{LOAD} = 500$ mA.

Symbol	Parameter			LM2576	
		Conditions	Тур	Limit (Note 2)	Units (Limits)
SYSTEM	PARAMETERS, ADJUS	STABLE REGULATORS (Note 3) Test Circu	uit <i>Figure 1</i>		
V _{OUT}	Feedback Voltage	$V_{IN} = 12V$, $I_{LOAD} = 0.5A$ $V_{OUT} = 5V$	1.230	1.217 1.243	V V(min) V(max)
V _{OUT}	Feedback Voltage LM2576	$0.5A \le I_{LOAD} \le 3A, 8V \le V_{IN} \le 40V$ $V_{OUT} = 5V$	1.230	1.193/ 1.180 1.267/ 1.280	V V(min) V(max)
η	Efficiency	$V_{IN} = 12V, I_{LOAD} = 3A, V_{OUT} = 5V$	82		%
SYSTEM	PARAMETERS, 3.3V R	EGULATORS (Note 3) Test Circuit Figure 1	1		
V _{OUT}	Output Voltage	$V_{IN} = 12V$, $I_{LOAD} = 0.5A$ $V_{OUT} = 3.3V$	3.3	3.234 3.366	V V(min) V(max)
V _{OUT}	Output Voltage LM2576-3.3	$0.5A \le I_{LOAD} \le 3A, \ 6V \le V_{IN} \le 40V$ $V_{OUT} = 3.3V$	3.3	3.168/ 3.135 3.432/ 3.465	V V(min) V(max)
η	Efficiency	$V_{IN} = 12V, I_{LOAD} = 3A$	75		%
SYSTEM	PARAMETERS, 5V REG	GULATORS (Note 3) Test Circuit Figure 1			•
V _{OUT}	Output Voltage	$V_{IN} = 12V$, $I_{LOAD} = 0.5A$ $V_{OUT} = 5V$	5.0	4.900 5.100	V V(min) V(max)
V _{OUT}	Output Voltage LM2576-5.0	$0.5A \le I_{LOAD} \le 3A, 8V \le V_{IN} \le 40V$ $V_{OUT} = 5V$	5.0	4.800/ 4.750 5.200/ 5.250	V V(min) V(max)
η	Efficiency	$V_{IN} = 12V, I_{LOAD} = 3A$	82		%
SYSTEM	PARAMETERS, 12V RE	EGULATORS (Note 3) Test Circuit Figure 1			
V _{OUT}	Output Voltage	$V_{IN} = 25V$, $I_{LOAD} = 0.5A$ $V_{OUT} = 12V$	12	11.760 12.240	V V(min) V(max)
V _{OUT}	Output Voltage LMLM2576-12	$0.5A \le I_{LOAD} \le 3A, 15V \le V_{IN} \le 40V$ $V_{OUT} = 12V$	12	11.520/ 11.400 12.480/ 12.600	V V(min) V(max)
η	Efficiency	V _{IN} = 25V, I _{LOAD} = 3A	88		%

Electrical Characteristics (continued)

Symbol	Parameter	Conditions	Тур	LM2576	Units (Limits)
				Limit (Note 2)	
DEVICE I	PARAMETERS, ADJUSTA	BLE REGULATOR			
I _B	Feedback Bias Current	V _{OUT} = 5V	50	100/ 500	nA
DEVICE I	PARAMETERS, FIXED and	ADJUSTABLE REGULATORS			
f _O	Oscillator Frequency		52	47/ 42 58/ 63	kHz kHz (min) kHz (max)
V _{SAT}	Saturation Voltage	I _{OUT} = 3A (Note 4)	1.4	1.8/ 2.0	V V(max)
DC	Max Duty Cycle (ON)	(Note 5)	98	93	% %(min)
I _{CL}	Current Limit	Peak Current, $t_{ON} \le 3\mu s$ (Note 4)	5.8	4.2/ 3.5 6.9/ 7.5	A A(min) A(max)
IL	Output Leakage Current	$V_{IN} = 40V$, (Note 6), Output = $0V$ Output = $-1V$ (Note 6) Output = $-1V$	7.5	2 30	mA(max) mA mA(max)
Ι _Q	Quiescent Current	(Note 6)	5	10	mA mA(max)
I _{STBY}	Standby Quiescent Current	ON/OFF Pin = 5V (OFF)	50	200	μΑ μA(max)
$\theta_{JA} \\ \theta_{JA} \\ \theta_{JC}$	Thermal Resistance	T,U Package, Junction to Ambient (Note 7) T,U Package, Junction to Ambient (Note 8) T,U Package, Junction to Case	65 45 2		°C/W

Electrical Characteristics (continued)

Symbol	Parameter	Conditions		LM2576	Units (Limits)
			Тур	Limit (Note 2)	
ON/OFF	CONTROL, FIXED and AL	DJUSTABLE REGULATORS Test Circuit File	igure 1		<u> </u>
V _{IH} V _{IL}	ON/OFF Pin Logic Input Level	V _{OUT} = 0V V _{OUT} = 5V	1.4 1.2	2.2/ 2.4 1.0/ 0.8	V(min) V(max)
I _{IH}	ON /OFF Pin Logic Current	ON /OFF Pin = 5V (OFF)	4	30	μΑ μA(max)
I _{IL}		ON/OFF Pin = 0V (ON)	0.01	10	μΑ μA(max)
Note 1:		ate limits beyond which damage to the device may do not guarantee specific performance limits. For			
Note 2:	All limits guaranteed at room temperature (standard type face) and at temperature extremes (bold type face) . All room temperature limits are 100% production tested. All limits at temperature extreme are guaranteed via testing.				
Note 3:	External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance. When the LM2576/LM1576 is used as shown in <i>Figure 1</i> test circuit, system performance will be shown in system parameters section of Electrical Characteristics.				

Note 4: Output (pin 2) sourcing current. No diode, inductor or capacitor connected to output.

Note 5: Feedback (pin 4) removed from output and connected to 0V.

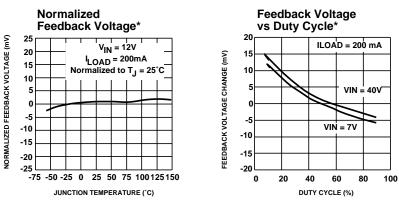
Note 6: Feedback (pin 4) removed from output and connected to 12V to force the output transistor OFF.

Note 7: Junction to ambient thermal resistance (no external heat sink) for the 5-lead TO-220 package mounted vertically, with 1/2" leads in a socket, or on PC board with minimum copper area.

Note 8: Junction to ambient thermal resistance (no external heat sink) for the 5-lead TO-220 package mounted vertically, with 1/4" leads soldered to PC board containing approximately 4 square inches of copper area surrounding the leads.

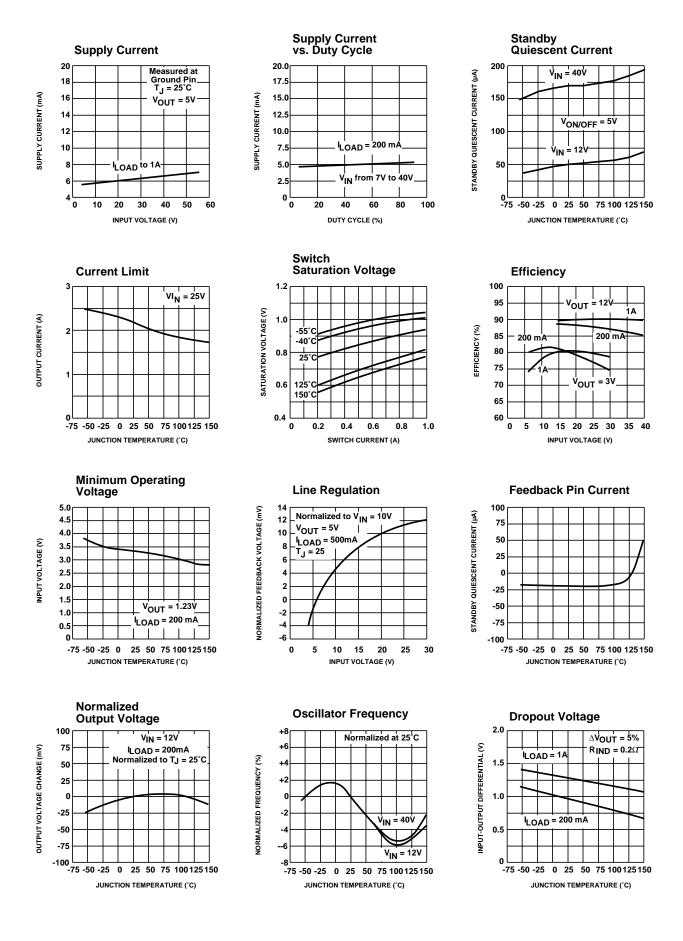
Note 9: Junction to ambient thermal resistance with approximately 1 square inch of pc board copper surrounding the leads. Additional copper will lower thermal resistance further.

Typical Performance Characteristics

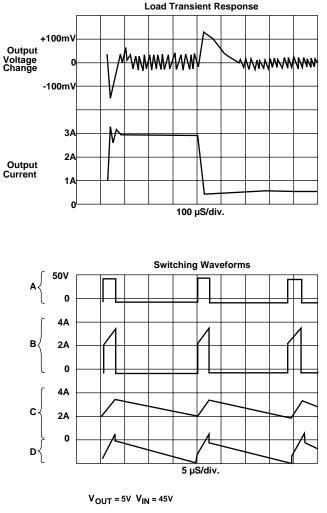


* Adjustable version only

Typical Performance Characteristics (continued) (Circuit of Figure 1)



Typical Performance Characteristics (Circuit of Figure 1)



A: Output pin voltage 50V/div B: Output pin current 2A/div C: Inductor current 2A/div D: Output ripple voltage 50 mV/div., AC coupled

Horizontal Time Base: 5µS/div

Test Circuits and Layout Guidelines

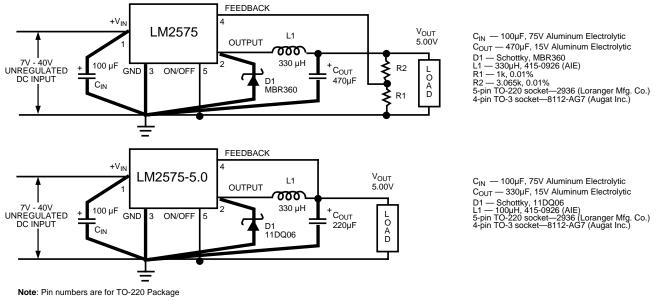
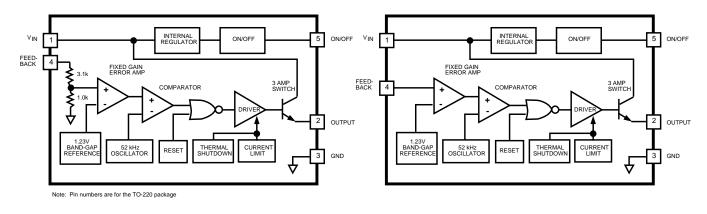


Figure 1.

As in any switching regulator, layout is very important. Rapidly switching currents associated with wiring inductance generate voltage transients which can cause problems. For minimal stray inductance and ground loops, the length of the leads indicated by heavy lines should be kept as short as possible. Single-point grounding (as indicated) or ground plane construction should be used for best results.

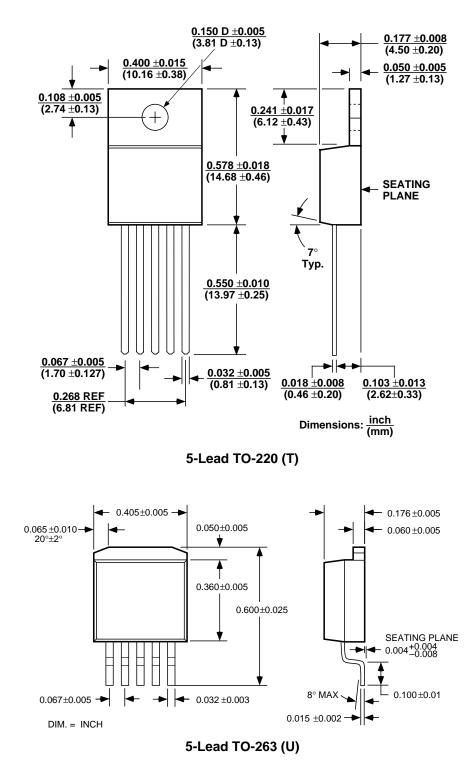
Block Diagrams







Package Information



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