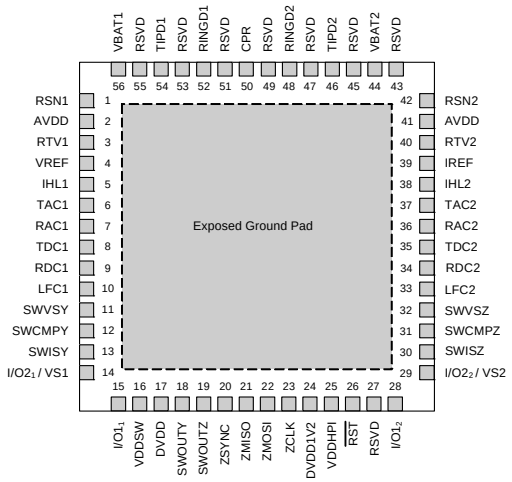
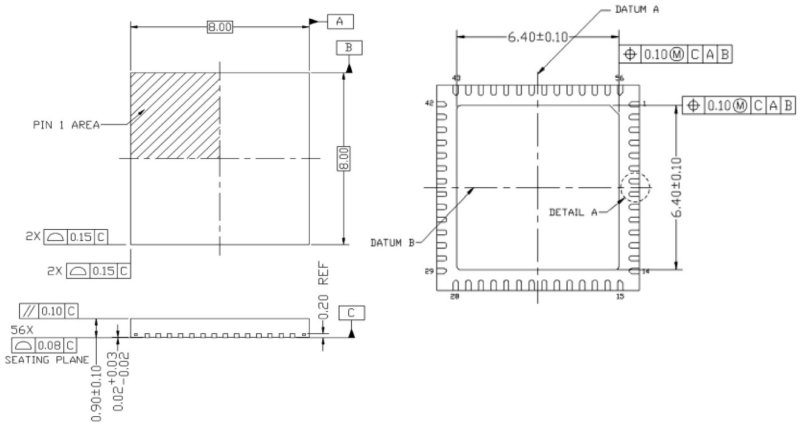
Selected Electrical Specifications

Description	Symbol	Test Conditions	Min	Typ	Max	Unit
Ambient Temperature, under Bias	T_A		-40		+85	°C
Digital and Analog Supply Voltages	DVDD, AVDD		3.135	3.3	3.465	V_{DC}
Host Port Interface Supply Voltage	VDDHPI		1.71	3.3	DVDD	V_{DC}
Battery Voltage		Active states	-150		-12	V_{DC}
Line Current	ILA		18	25	49	mA
Ringing Voltage	V_{RING}				140	V_P
Two-Wire Return Loss	R_L	200 to 3400 Hz		30		dB
Longitudinal Balance		1 kHz		58		dB
Device Power Consumption (Per Channel)	P_D	Flyback switcher				mW
Shutdown		Switchers off		6		
Disconnect				23		
Low Power Idle Mode		On-Hook		43		
Idle		On-Hook		85		
Active		OHT		194		
		Off-Hook, 300 Ω, ILA=25 mA		435		
Ringing		65 V_{RMS}, 3REN		480		
Device Power Dissipation, Continuous	$P_{D(max)}$	T_A = 85°C		2		W
Junction to Ambient Thermal Resistance	θ_{JA}			27		°C/W

Device Pinout



Package Drawings



Related Collateral

- *Le9662 Shared Battery Dual miSLIC™ Line Circuit Preliminary Data Sheet*, Document ID# 146852
- *Le9672 Tracking Battery Dual miSLIC™ Line Circuit Preliminary Data Sheet*, Document ID# 146853

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