# LB1862M,LB1962M

#### Allowable Operating Conditions at $Ta = 25^{\circ}C$

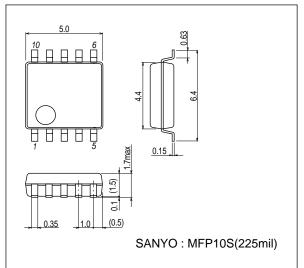
Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	V <sub>CC</sub>		3.8 to 16.8	V
ST input High level voltage	STH		3 to 14	V
ST input Low level voltage	STL		-0.3 to +0.4	V
Hall input common mode voltage	VICM		0.2 to V <sub>CC</sub> -1.5	V

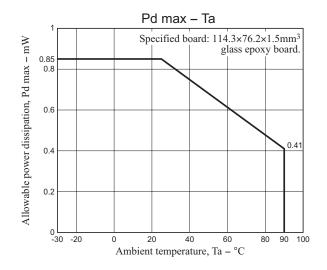
### **Electrical Characteristics** at $Ta = 25^{\circ}C$ , $V_{CC} = 5V$

Parameter	Querra ha a l	Conditions		1.1		
Parameter	Symbol	Conditions	min	typ	max	Unit
Current drain	ICC	In drive mode (CT = "L", ST = "L")		6.5	9.1	mA
		In lockup protection mode (CT = "H", ST = "L")		2.2	3.1	mA
		In standby mode (ST = "H")		110	150	μA
Lockup detection capacitor charge current	ICT1		1.9	2.8	3.7	μΑ
Capacitor discharge current	ICT2		0.32	0.46	0.60	μA
Capacitor charge/discharge current ratio	R <sub>CT</sub>	$R_{CT} = I_{CT} 1 / I_{CT} 2$	5.0	6.0	7.0	
CT charge voltage	V <sub>CT</sub> 1		2.55	2.75	2.95	V
CT discharge voltage	V <sub>CT</sub> 2		1.6	1.8	2.0	V
Output Low level voltage	V <sub>OL</sub>	I <sub>O</sub> = 200mA		0.2	0.3	V
Output High level voltage	VOH	I <sub>O</sub> = 200mA	3.9	4.1		V
Hall input sensitivity	V <sub>HN</sub>	Zero peak value (Including offset and hysteresis)		7	15	mV
RD/FG output pin Low voltage	V <sub>RD(FG)</sub>	I <sub>RD(FG)</sub> = 5mA		0.1	0.3	V
RD/FG output pin leakage current	IRD(FG)	V <sub>RD(FG)</sub> = 15V			30	μA
HB output Low voltage	V <sub>HBL</sub>	I <sub>HB</sub> = 5mA		1.0	1.3	V
ST pin input current	I <sub>ST</sub>	V <sub>ST</sub> = 5V		75	100	μΑ

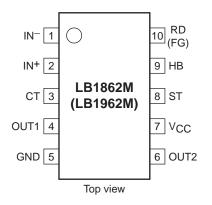
## **Package Dimensions**

unit : mm (typ) 3086B

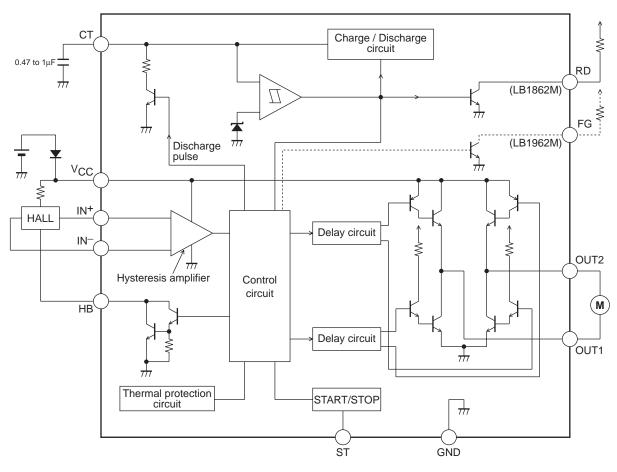




# Pin Assignment





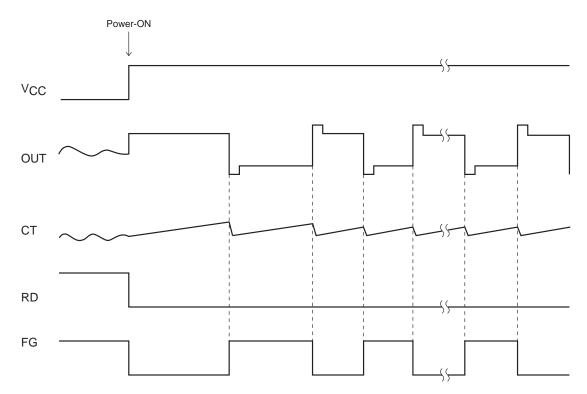


### **Truth Table**

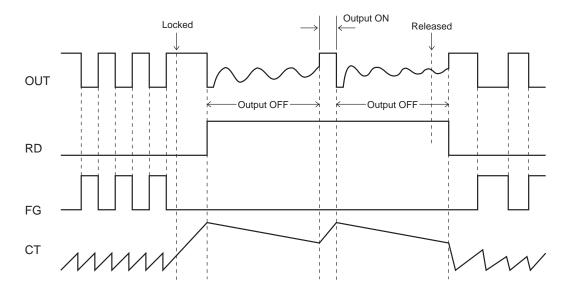
ST	IN-	IN+	СТ	OUT1	OUT2	RD	FG	HB	Mode	
н	-	-	-	OFF	OFF	OFF	OFF	OFF	Standby	
	Н	L		н	L		L		Dutation	
L	L	Н	L	L	Н	L	Н	L	Rotating	
	-	-	Н	OFF	OFF	OFF	-	L	Lockup protection activated	

Latch-type RD output is Low during rotation and High during stop.

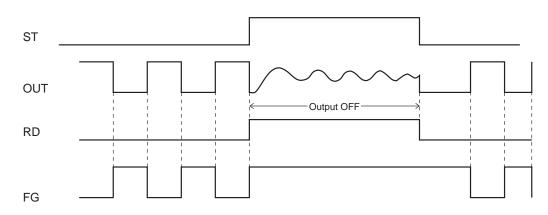
## Startup



## Lockup protection/automatic recovery



Start/stop



### **Design Reference**

(1) V <sub>CC</sub> pin Power supply pin for control block and motor drive						
Power supply pin for control block and motor drive. Accepts a wide operation voltage range from 3.8 to 16.8V, for 5V/12V dual power supply support.						
······································	,					
(2) OUT1, OUT2 pins						
Single-phase coil output pins.						
Bipolar drive output with upper side inverted and lower side single output. Built-in regenerative circuit regenerates						
kickback current between lower side NPN outputs when a diode is used for protection against reverse connection.						
(2) $\mathbf{N} = \mathbf{N} + \mathbf{z}$						
(3) IN <sup>-</sup> , IN <sup>+</sup> pins						
Hall input signal pins						
The Hall signal is amplified into a square wave by the Hall amplifier with hysteresis characteristics of $\pm 3.5$ mV (typ.).						
The Hall input signal amplitude should be 70mV or more						
(4) CT pin						
This pin serves for connecting a capacitor between CT an						
The capacitor determines the characteristics of the built-in lockup protection circuit for preventing coil burnout in the						
case of motor restraint. Once normal motor load is restored, the automatic recovery circuit resets itself.						
Changing the capacitance alters the lockup detection time	) <b>.</b>					
When a 0.47 mF capacitor is connected between CT and	GND					
Lockup detection time	: approx. 0.5s					
Lockup protection time/automatic recovery time	: approx. 0.16s (output ON)					
	approx. 1s (output OFF)					
When not using lockup protection function, this pin shoul	d be connected to ground.					
(5) RD pin (LB1862M)						
Open-collector output pin that is Low during rotation and OFF when lockup is detected.						
Open-conector output pin that is now during rotation and	OTT when lockup is deletted.					

The output is a latch type which stays OFF also when the automatic recovery circuit has restored drive mode unless the rotation actually resumes.

(6) FG pin (LB1962M)

Open-collector output pin for rotation frequency detection. When  $IN^- = High$  and  $IN^+ = Low$ , then FG = LowWhen  $IN^- = Low$  and  $IN^+ = High$ , then FG = High

#### (7) ST pin and HB pin

ST pin : When input to this pin is High, motor drive is stopped (OUT is high impedance).

At this time, RD output indicates lockup protection mode OFF.

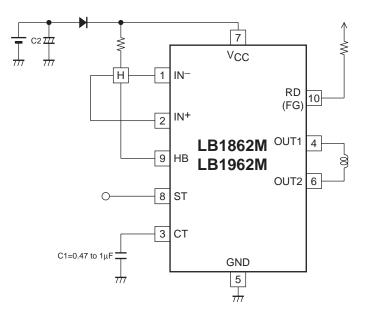
HB pin : Hall bias switching pin. At ST pin High input, Hall bias is switched to reduce current drain in fan standby mode.

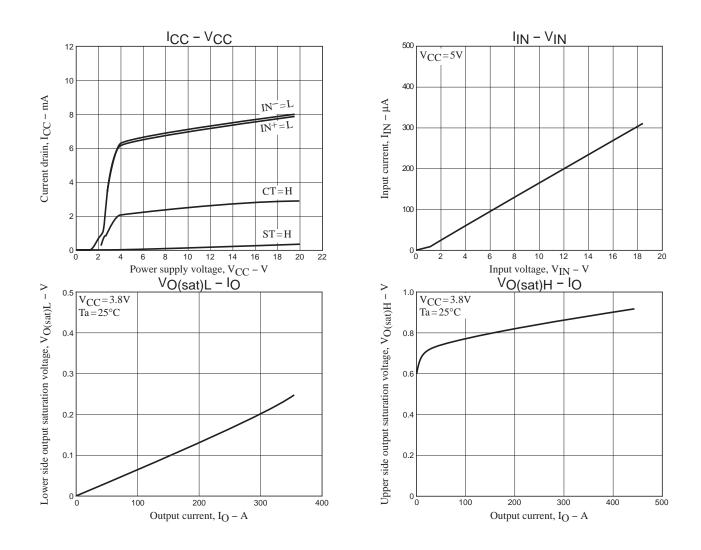
If not used, both pins should be open.

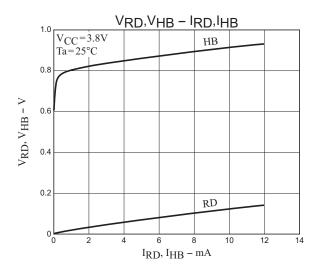
#### (8) Thermal protection circuit

When internal temperature Tj of IC reaches 180°C, output current limiter is activated to protect against damage.

### **Application Circuit Example**







- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of August, 2009. Specifications and information herein are subject to change without notice.