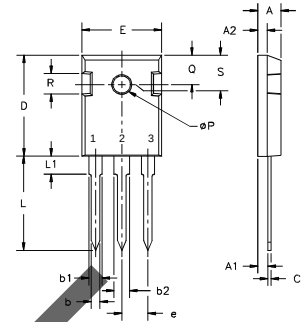


Symbol	Test Conditions	Characteristic Values (T _j = 25°C, unless otherwise specified)		
		min.	typ.	max.
g_{fs}	V _{DS} = 10 V; I _D = 0.5 • I _{D25} , pulse test	11	21	S
C_{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		4200	pF
C_{oss}			450	pF
C_{rss}			135	pF
t_{d(on)}	V _{GS} = 10 V, V _{DS} = 0.5 • V _{DSS} , I _D = 0.5 I _{D25} R _G = 2 Ω, (External)		24	30 ns
t_r			33	45 ns
t_{d(off)}			65	80 ns
t_f			30	40 ns
Q_{g(on)}	V _{GS} = 10 V, V _{DS} = 0.5 • V _{DSS} , I _D = 0.5 I _{D25}		160	190 nC
Q_{gs}			28	40 nC
Q_{gd}			75	85 nC
R_{thJC}			0.25	0.42 K/W
R_{thCK}				K/W

Source-Drain Diode

Symbol	Test Conditions	Characteristic Values (T _j = 25°C, unless otherwise specified)		
		min.	typ.	max.
I_S	V _{GS} = 0	21N50 24N50		21 24 A
I_{SM}	Repetitive; pulse width limited by T _{JM}	21N50 24N50		84 96 A
V_{SD}	I _F = I _S , V _{GS} = 0 V, Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %			1.5 V
t_{rr}	I _F = I _S , -di/dt = 100 A/μs, V _R = 100 V		600	ns

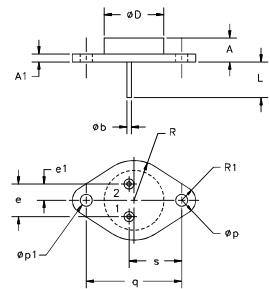
TO-247 AD (IXTH) Outline



Terminals: 1 - Gate 2 - Drain
3 - Source Tab - Drain

Dim.	Millimeter Min. Max.	Inches Min. Max.
A	4.7 5.3	.185 .209
A ₁	2.2 2.54	.087 .102
A ₂	2.2 2.6	.059 .098
b	1.0 1.4	.040 .055
b ₁	1.65 2.13	.065 .084
b ₂	2.87 3.12	.113 .123
C	.4 .8	.016 .031
D	20.80 21.46	.819 .845
E	15.75 16.26	.610 .640
e	5.20 5.72	0.205 0.225
L	19.81 20.32	.780 .800
L1	4.50	.177
ØP	3.55 3.65	.140 .144
Q	5.89 6.40	0.232 0.252
R	4.32 5.49	.170 .216
S	6.15 BSC	.242 BSC

TO-204 AE(IXTM) Outline



Pins 1 - Gate 2 - Source
Case - Drain

Dim.	Millimeter Min. Max.	Inches Min. Max.
A	6.4 11.4	.250 .450
A ₁	1.53 3.42	.060 .135
Øb	1.45 1.60	.057 .063
ØD	22.22	.875
e	10.67 11.17	.420 .440
e1	5.21 5.71	.205 .225
L	11.18 12.19	.440 .480
Øp	3.84 4.19	.151 .165
Øp1	3.84 4.19	.151 .165
q	30.15 BSC	1.187 BSC
R	12.58 13.33	.495 .525
R1	3.33 4.77	.131 .188
s	16.64 17.14	.655 .675

Fig. 1 Output Characteristics

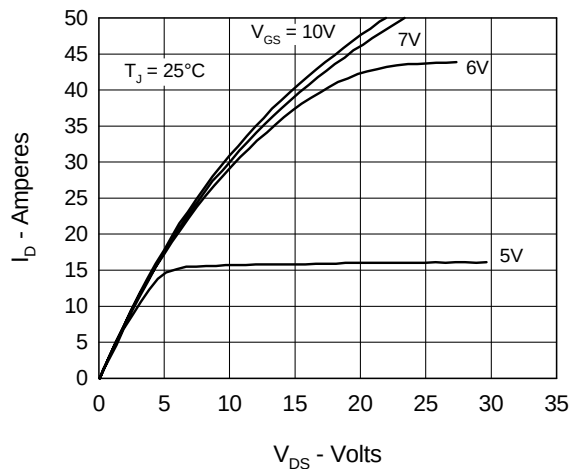


Fig. 2 Input Admittance

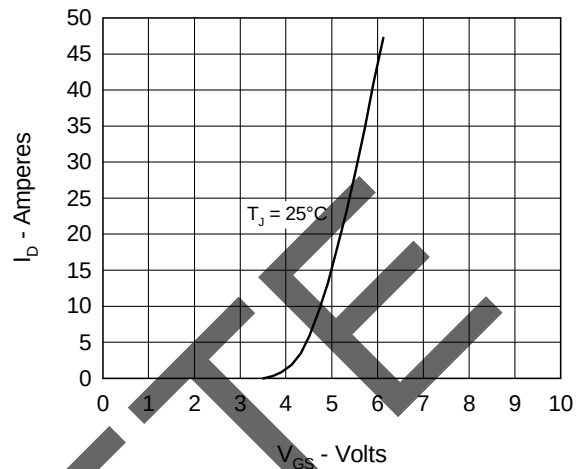


Fig. 3 $R_{DS(on)}$ vs. Drain Current

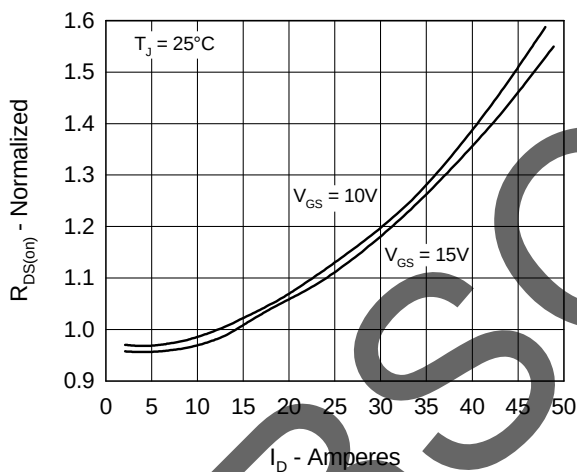


Fig. 4 Temperature Dependence of Drain to Source Resistance

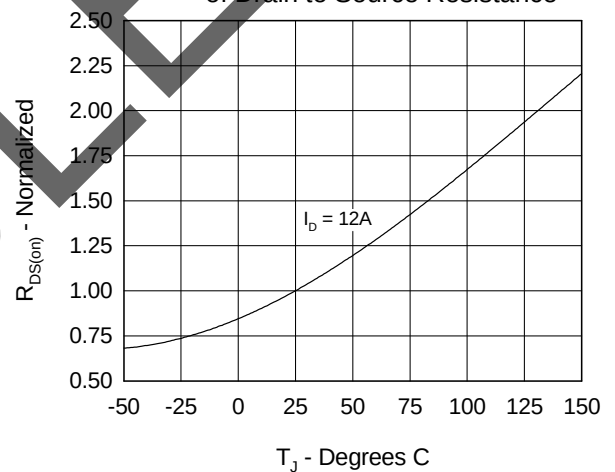


Fig. 5 Drain Current vs. Case Temperature

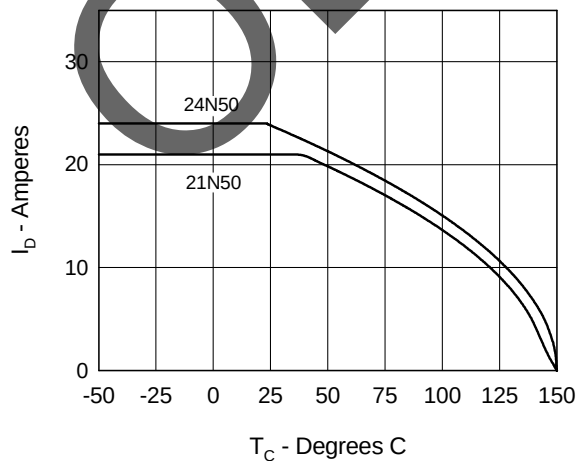


Fig. 6 Temperature Dependence of Breakdown and Threshold Voltage

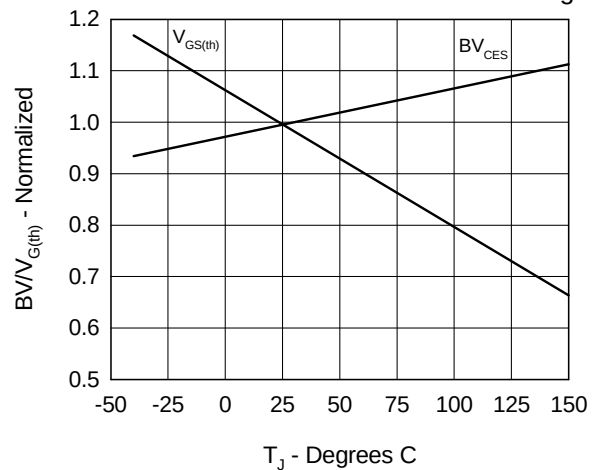


Fig.7 Gate Charge Characteristic Curve

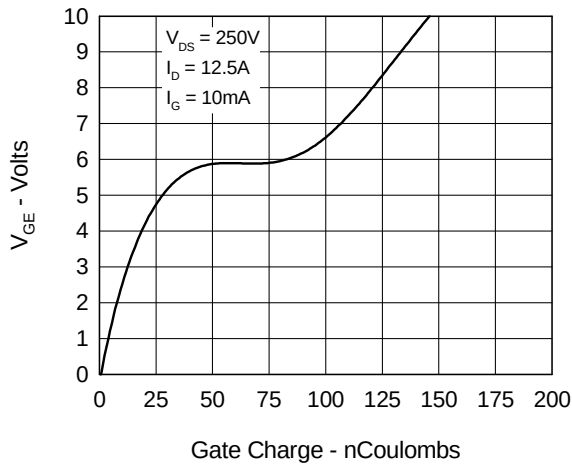


Fig.8 Forward Bias Safe Operating Area

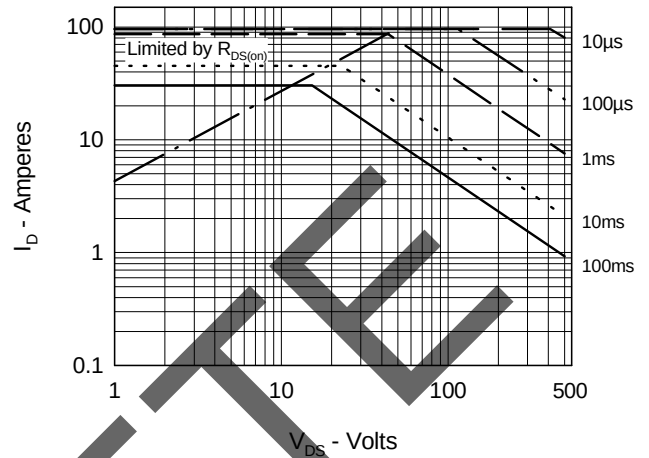


Fig.9 Capacitance Curves

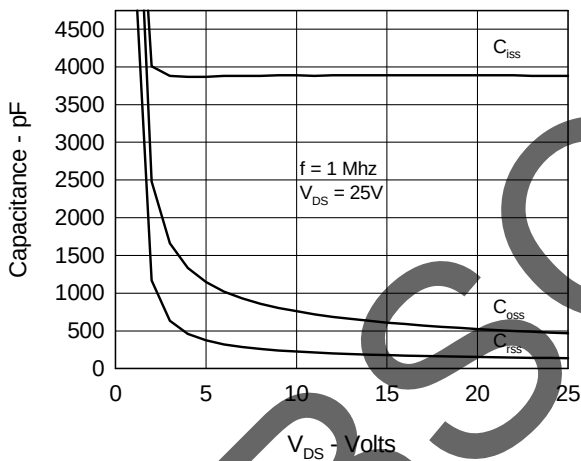


Fig.10 Source Current vs. Source to Drain Voltage

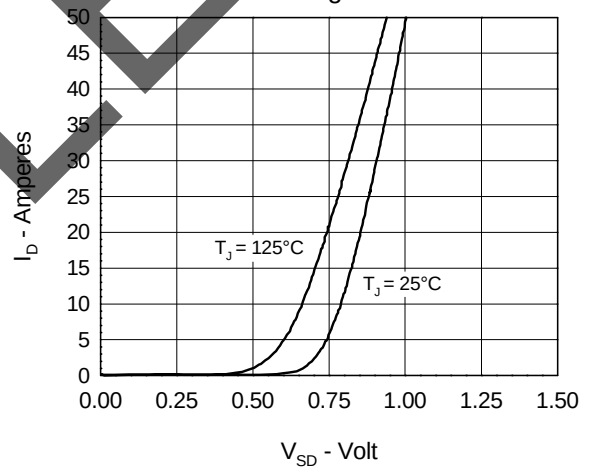
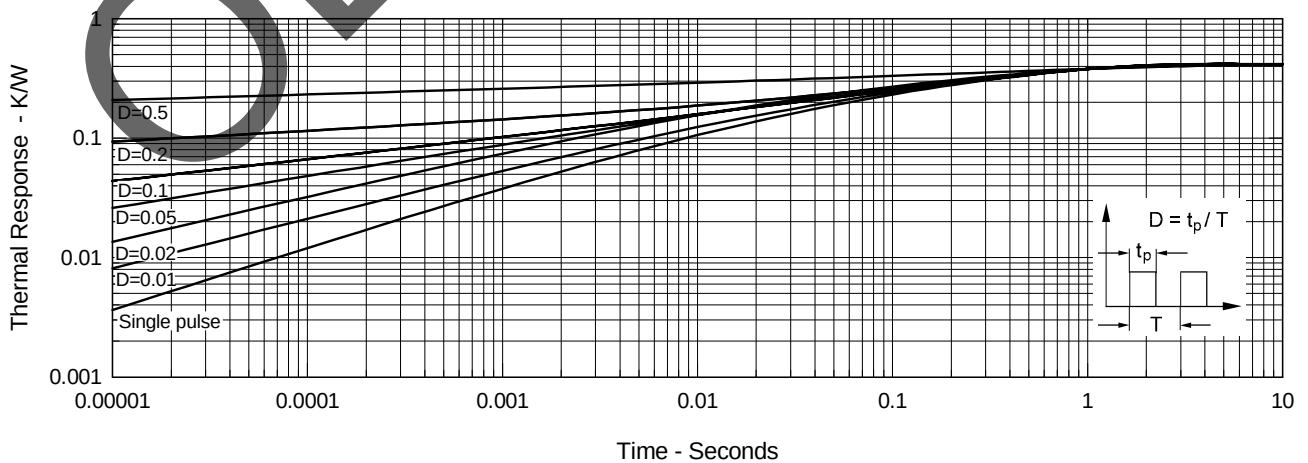


Fig.11 Transient Thermal Impedance





Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at www.littelfuse.com/disclaimer-electronics.