

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		2.0	K/W
Diode thermal resistance, junction - case	R_{thJCD}		3.2	
Thermal resistance, junction – ambient	R_{thJA}	P-TO-220-3-1 P-TO-247-3-21	62	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_C=300\mu A$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C=3A$	-	2.2	2.8	
		$T_j=25^{\circ}C$	-	2.5	-	
		$T_j=150^{\circ}C$	-	2.4	-	
Diode forward voltage	V_F	$V_{GE} = 0, I_F=2A$	-	2.0	2.5	
		$T_j=25^{\circ}C$	-	1.75	-	
		$T_j=150^{\circ}C$	-	-	-	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=90\mu A, V_{CE}=V_{GE}$	2.1	3	3.9	
Zero gate voltage collector current	I_{CES}	$V_{CE}=1200V, V_{GE}=0V$	-	-	20	μA
		$T_j=25^{\circ}C$	-	-	80	
		$T_j=150^{\circ}C$	-	-	-	
Gate-emitter leakage current	I_{GES}	$V_{CE}=0V, V_{GE}=20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20V, I_C=3A$	-	2	-	S
Dynamic Characteristic						
Input capacitance	C_{iss}	$V_{CE}=25V,$	-	205	-	pF
Output capacitance	C_{oss}	$V_{GE}=0V,$	-	24	-	
Reverse transfer capacitance	C_{rss}	$f=1MHz$	-	7	-	
Gate charge	Q_{Gate}	$V_{CC}=960V, I_C=3A$ $V_{GE}=15V$	-	22	-	nC
Internal emitter inductance	L_E	PG-TO-220-3-1	-	7	-	nH
measured 5mm (0.197 in.) from case		PG-TO-247-3-21	-	13	-	

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25^{\circ}\text{C}$, $V_{CC}=800\text{V}$, $I_C=3\text{A}$, $V_{GE}=15\text{V}/0\text{V}$, $R_G=82\Omega$, $L_{\sigma}^{(2)}=180\text{nH}$, $C_{\sigma}^{(2)}=40\text{pF}$ Energy losses include “tail” and diode ³⁾ reverse recovery.	-	9.2	-	ns
Rise time	t_r		-	5.2	-	
Turn-off delay time	$t_{d(off)}$		-	281	-	
Fall time	t_f		-	29	-	
Turn-on energy	E_{on}			-	0.14	-
Turn-off energy	E_{off}	-		0.15	-	
Total switching energy	E_{ts}	-		0.29	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_j=25^{\circ}\text{C}$, $V_R=800\text{V}$, $I_F=3\text{A}$, $R_G=82\Omega$	-	42	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.23	-	μC
Diode peak reverse recovery current	I_{rrm}		-	10.3	-	A
Diode current slope	di_F/dt		-	993	-	A/ μs
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	1180	-	

Switching Characteristic, Inductive Load, at $T_j=150^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=150^{\circ}\text{C}$ $V_{CC}=800\text{V}$, $I_C=3\text{A}$, $V_{GE}=15\text{V}/0\text{V}$, $R_G=82\Omega$, $L_{\sigma}^{(2)}=180\text{nH}$, $C_{\sigma}^{(2)}=40\text{pF}$	-	9.4	-	ns
Rise time	t_r		-	6.7	-	
Turn-off delay time	$t_{d(off)}$		-	340	-	
Fall time	t_f		-	63	-	
Turn-on energy	E_{on}	$R_G=82\Omega$, $L_{\sigma}^{(2)}=180\text{nH}$, $C_{\sigma}^{(2)}=40\text{pF}$	-	0.22	-	mJ
Turn-off energy	E_{off}		-	0.26	-	
Total switching energy	E_{ts}		-	0.48	-	
Energy losses include “tail” and diode ³⁾ reverse recovery.						
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_j=150^{\circ}\text{C}$ $V_R=800\text{V}$, $I_F=3\text{A}$, $R_G=82\Omega$	-	125	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.51	-	μC
Diode peak reverse recovery current	I_{rrm}		-	12	-	A
Diode current slope	di_F/dt		-	829	-	A/ μs
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	540	-	

²⁾ Leakage inductance L_{σ} and stray capacity C_{σ} due to dynamic test circuit in figure E

³⁾ Commutation diode from device IKP03N120H2

Switching Energy ZVT, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-off energy	E_{off}	$V_{CC}=800V,$				mJ
		$I_C=3A,$				
		$V_{GE}=15V/0V,$				
		$R_G=82\Omega,$				
		$C_r^{2)}=4nF$				
		$T_j=25^{\circ}C$	-	0.05	-	
		$T_j=150^{\circ}C$	-	0.09	-	

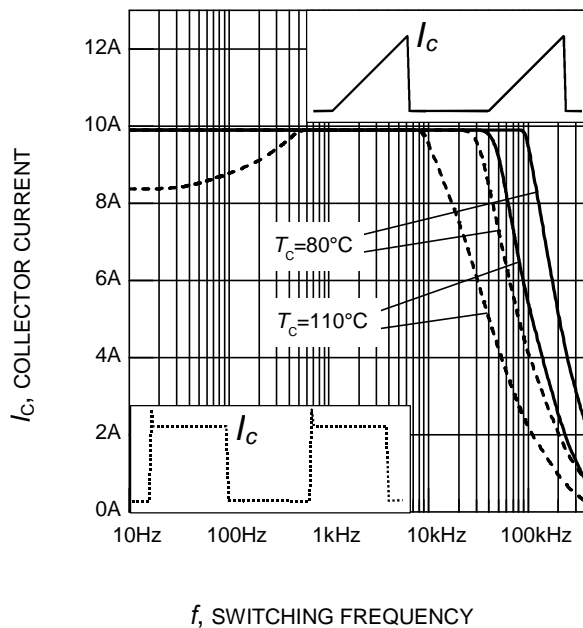


Figure 1. Collector current as a function of switching frequency

($T_j \leq 150^\circ\text{C}$, $D = 0.5$, $V_{CE} = 800\text{V}$,
 $V_{GE} = +15\text{V}/0\text{V}$, $R_G = 82\Omega$)

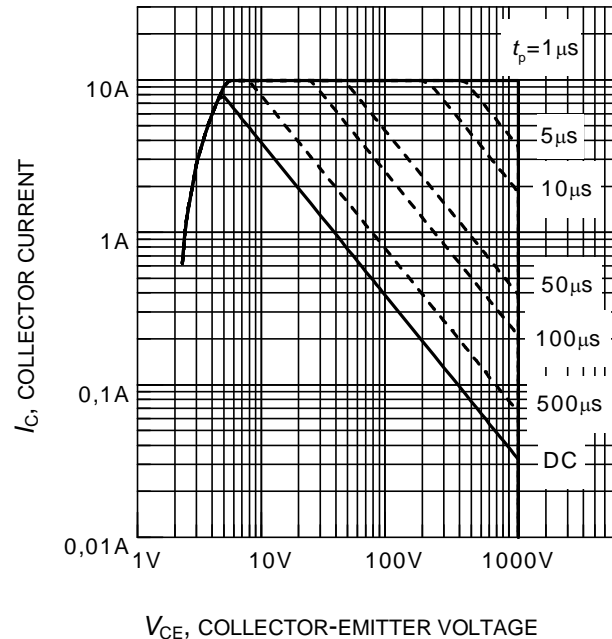


Figure 2. Safe operating area

($D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 150^\circ\text{C}$)

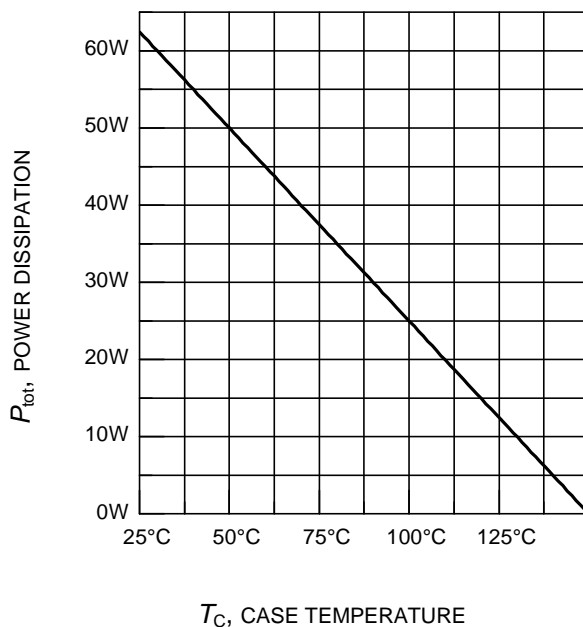


Figure 3. Power dissipation as a function of case temperature

($T_j \leq 150^\circ\text{C}$)

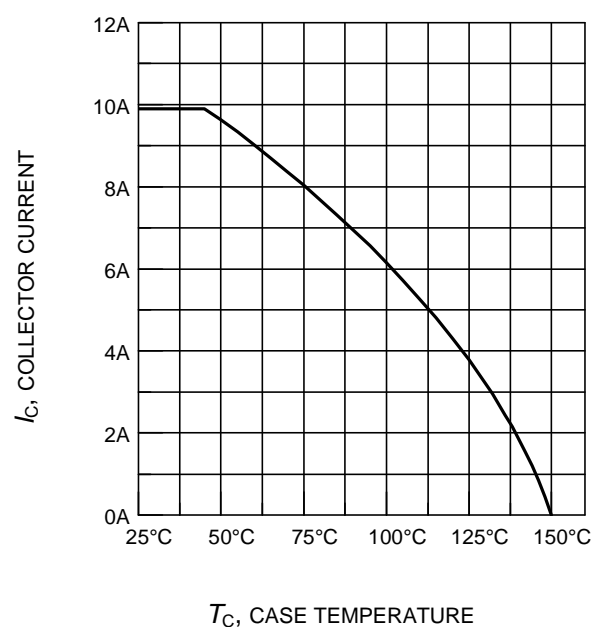
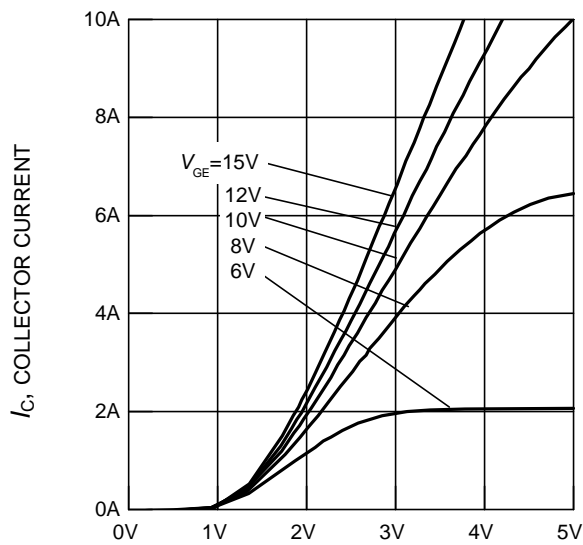


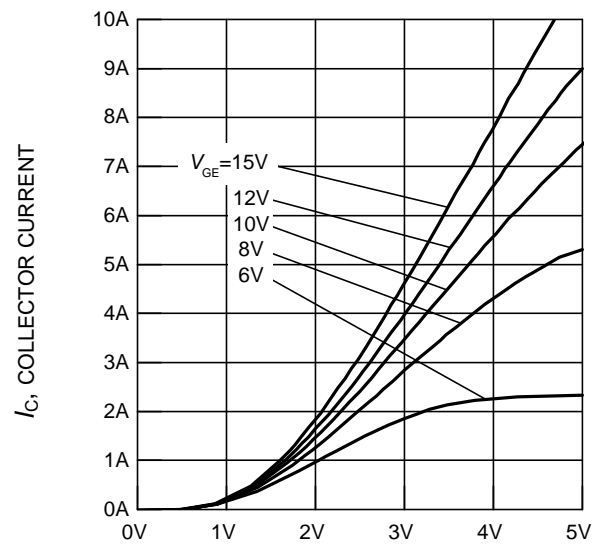
Figure 4. Collector current as a function of case temperature

($V_{GE} \leq 15\text{V}$, $T_j \leq 150^\circ\text{C}$)



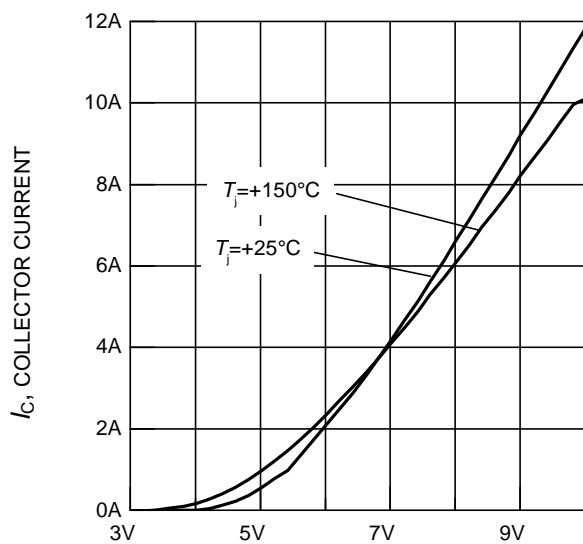
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 5. Typical output characteristics
($T_j = 25^\circ\text{C}$)



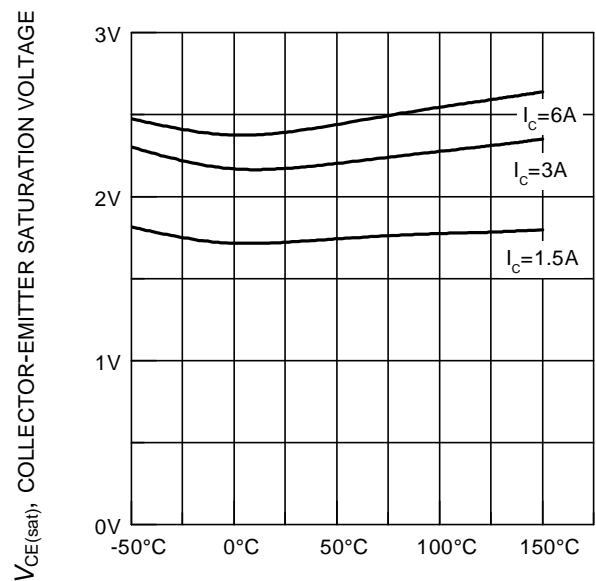
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 6. Typical output characteristics
($T_j = 150^\circ\text{C}$)



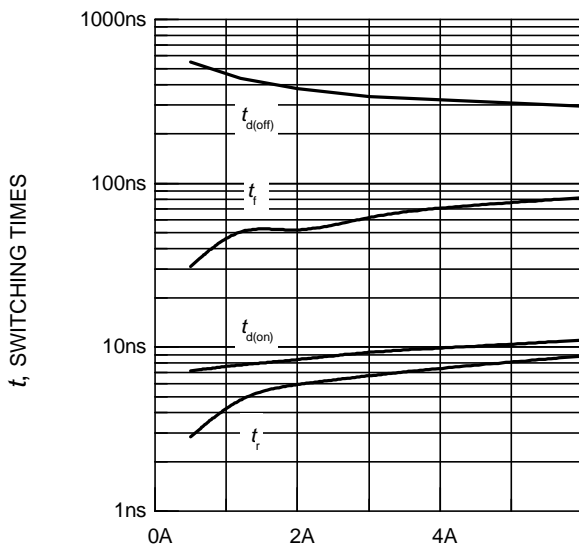
V_{GE} , GATE-EMITTER VOLTAGE

Figure 7. Typical transfer characteristics
($V_{CE} = 20\text{V}$)



T_j , JUNCTION TEMPERATURE

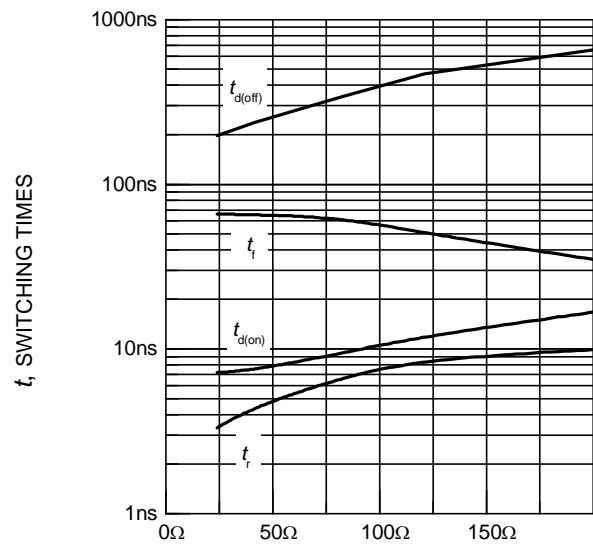
Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)



I_C , COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current

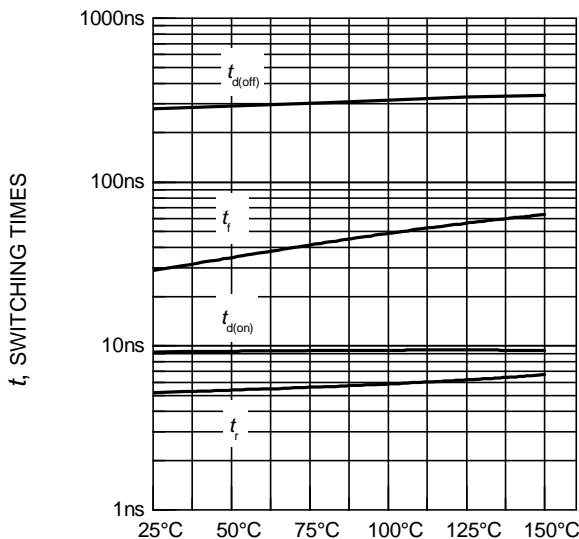
(inductive load, $T_j = 150^\circ\text{C}$,
 $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $R_G = 82\Omega$,
dynamic test circuit in Fig.E)



R_G , GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor

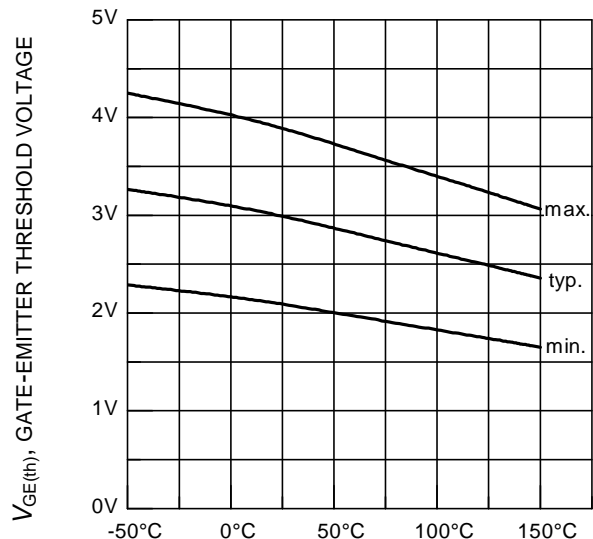
(inductive load, $T_j = 150^\circ\text{C}$,
 $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $I_C = 3\text{A}$,
dynamic test circuit in Fig.E)



T_j , JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature

(inductive load, $V_{CE} = 800\text{V}$,
 $V_{GE} = +15\text{V}/0\text{V}$, $I_C = 3\text{A}$, $R_G = 82\Omega$,
dynamic test circuit in Fig.E)



T_j , JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature

($I_C = 0.09\text{mA}$)

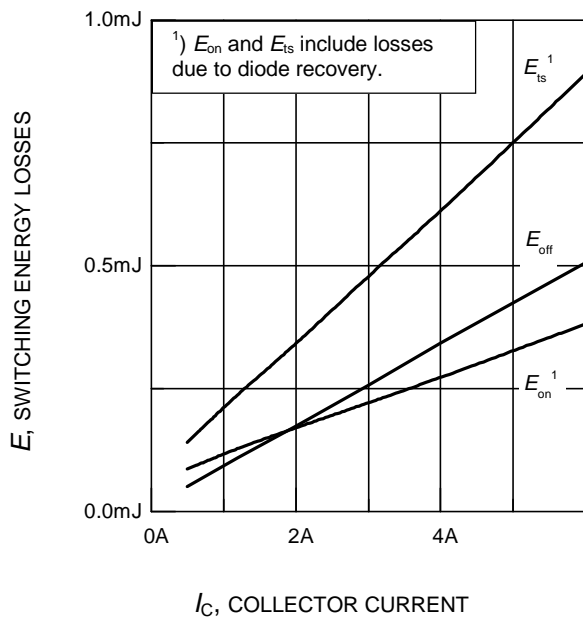


Figure 13. Typical switching energy losses as a function of collector current
(inductive load, $T_j = 150^\circ\text{C}$, $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $R_G = 82\Omega$, dynamic test circuit in Fig.E)

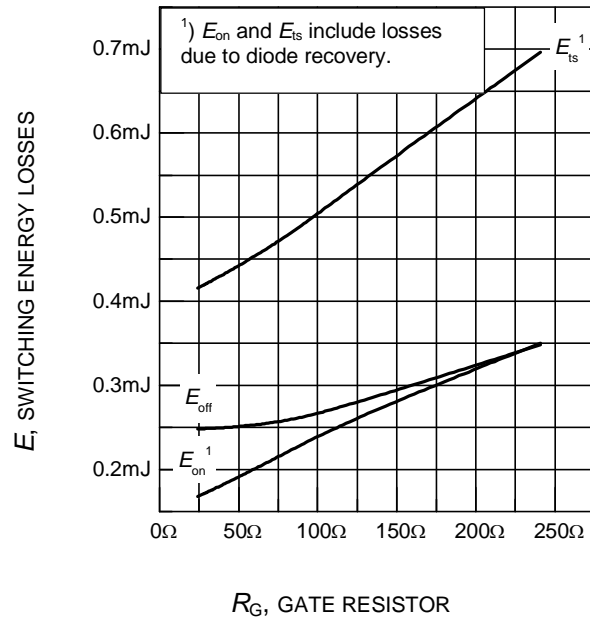


Figure 14. Typical switching energy losses as a function of gate resistor
(inductive load, $T_j = 150^\circ\text{C}$, $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $I_C = 3\text{A}$, dynamic test circuit in Fig.E)

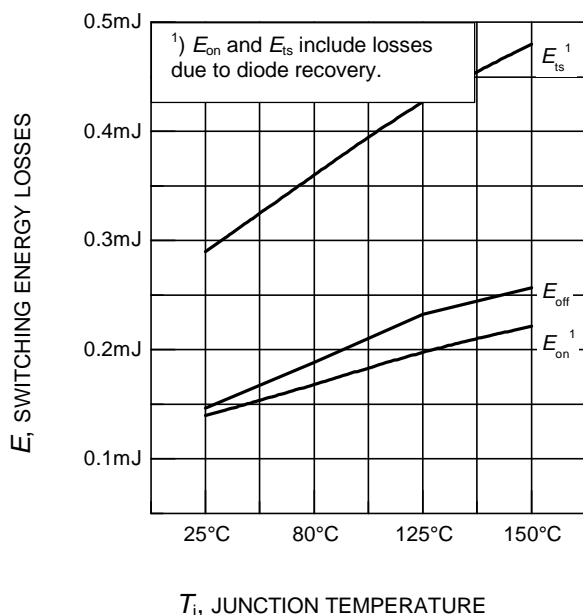


Figure 15. Typical switching energy losses as a function of junction temperature
(inductive load, $V_{CE} = 800\text{V}$, $V_{GE} = +15\text{V}/0\text{V}$, $I_C = 3\text{A}$, $R_G = 82\Omega$, dynamic test circuit in Fig.E)

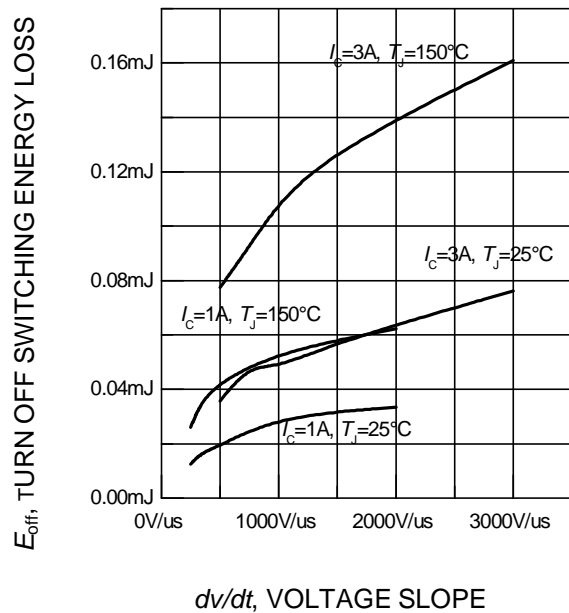


Figure 16. Typical turn off switching energy loss for soft switching
(dynamic test circuit in Fig. E)

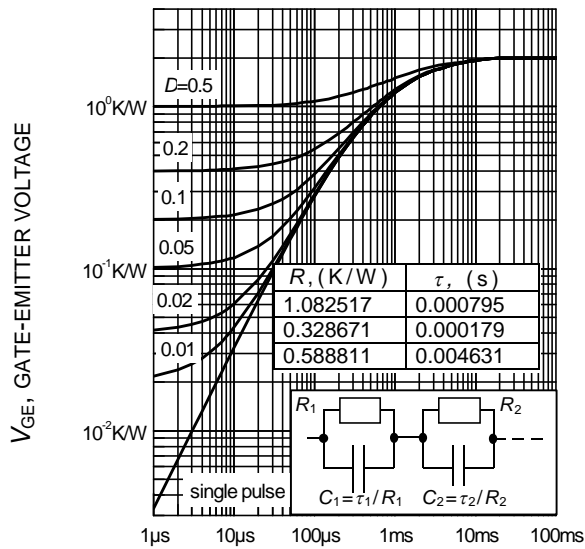


Figure 17. Typical gate charge
($I_C = 3A$)

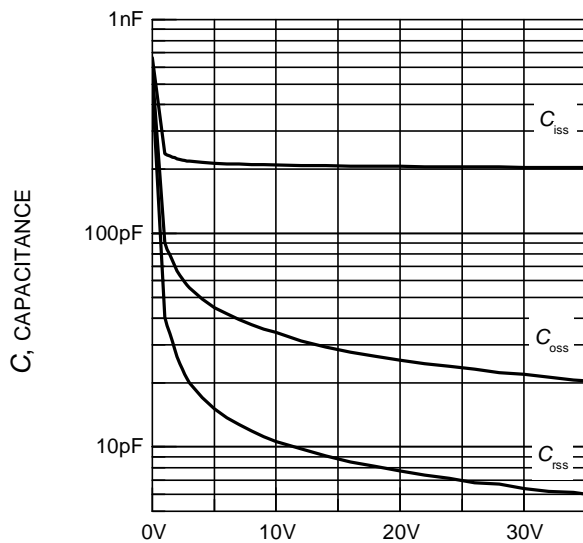


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0V$, $f = 1MHz$)

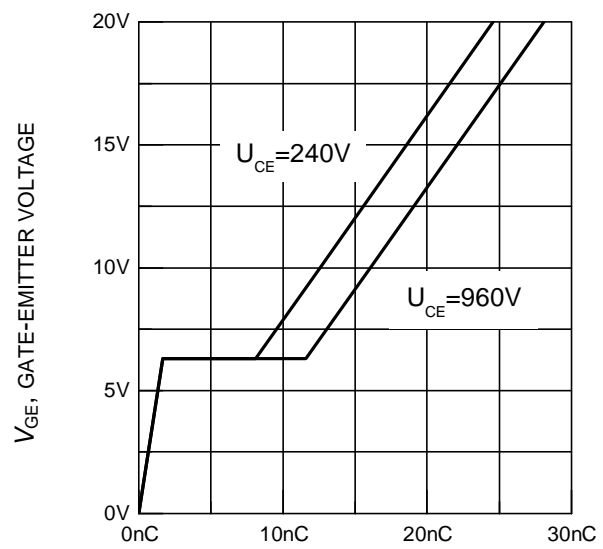


Figure 19. Typical turn on behavior
($I_C = 3A$)

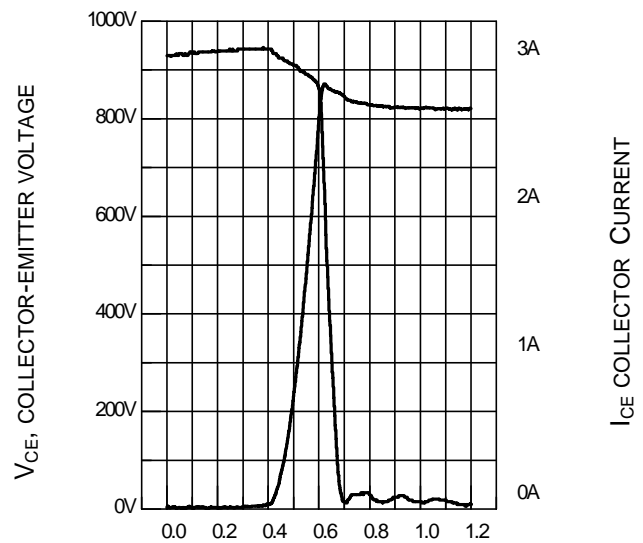


Figure 20. Typical turn off behavior, hard switching
($V_{GE} = 15/0V$, $R_G = 82\Omega$, $T_j = 150^\circ C$,
Dynamic test circuit in Figure E)

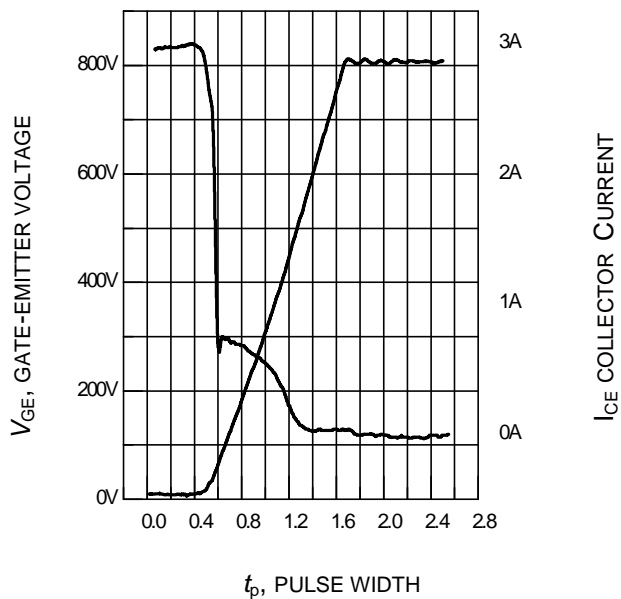


Figure 21. Typical turn off behavior, soft switching

($V_{GE}=15/0V$, $R_G=82\Omega$, $T_j = 150^\circ C$,
Dynamic test circuit in Figure E)

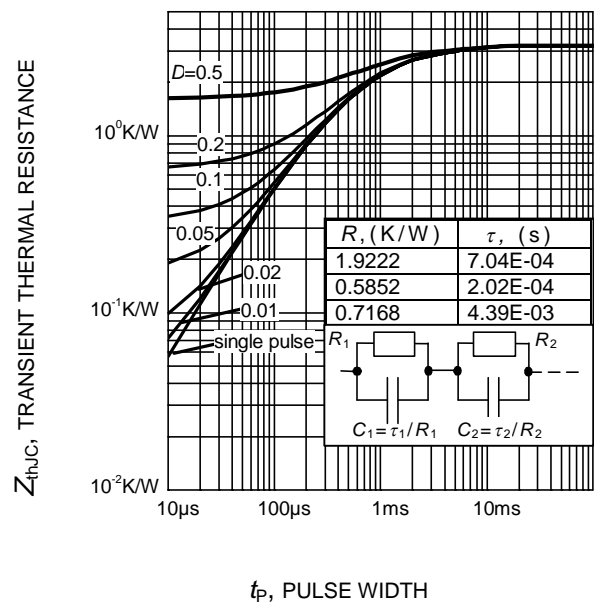


Figure 22. Diode transient thermal impedance as a function of pulse width ($D=t_p/T$)

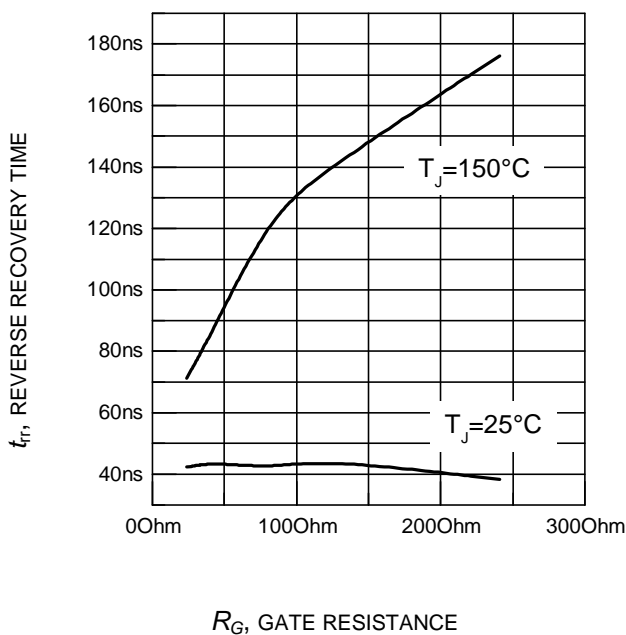


Figure 23. Typical reverse recovery time as a function of diode current slope

($V_R=800V$, $I_F=3A$,
Dynamic test circuit in Figure E)

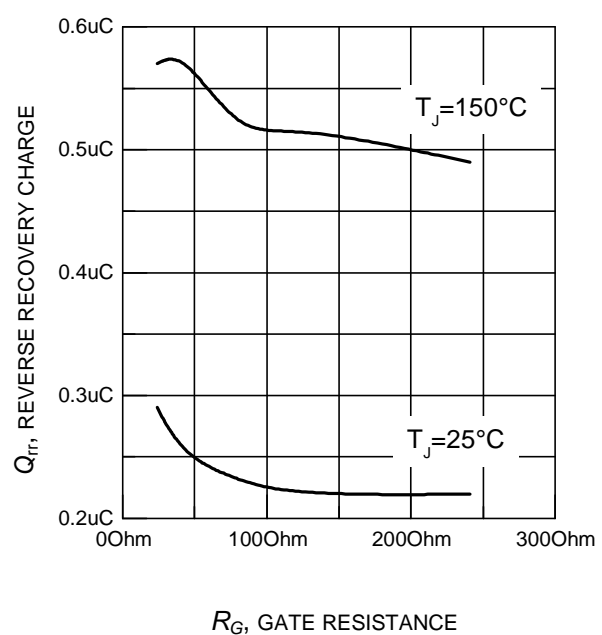
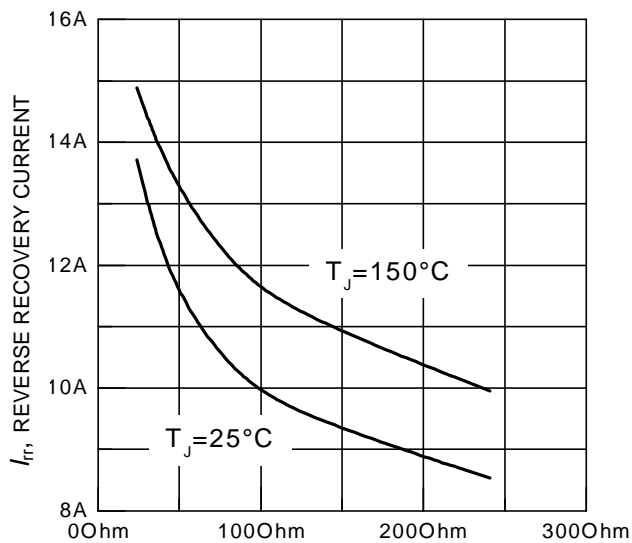


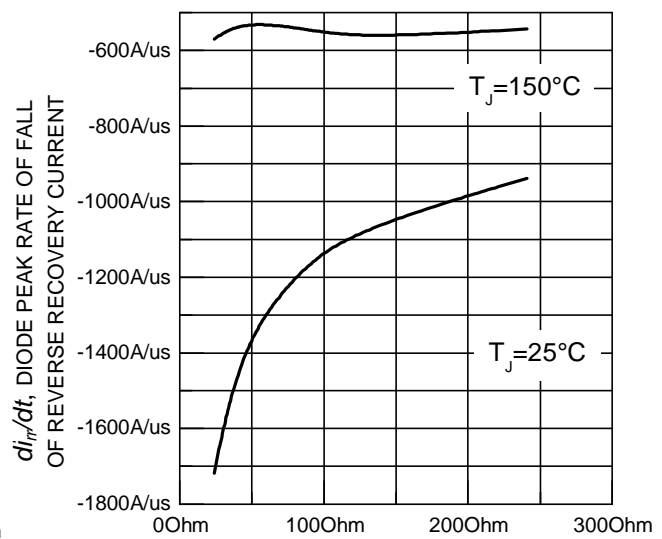
Figure 24. Typical reverse recovery charge as a function of diode current slope

($V_R=800V$, $I_F=3A$,
Dynamic test circuit in Figure E)



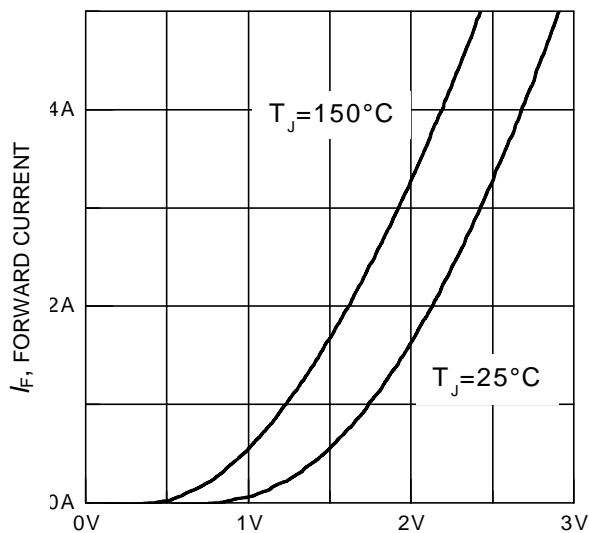
R_G , GATE RESISTANCE

Figure 25. Typical reverse recovery current as a function of diode current slope
($V_R=800V$, $I_F=3A$,
Dynamic test circuit in Figure E)



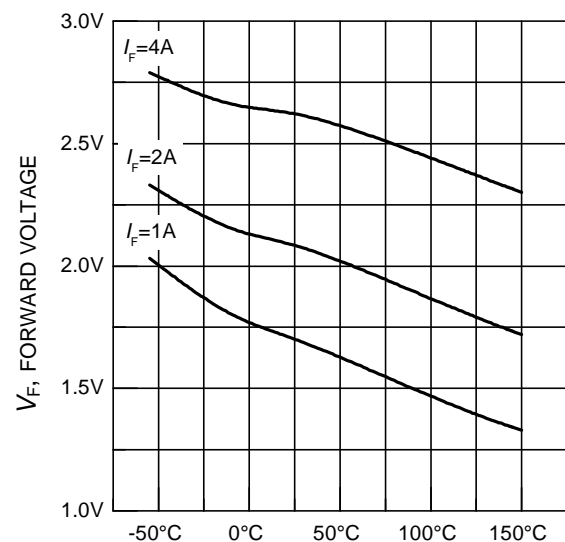
R_G , GATE RESISTANCE

Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope
($V_R=800V$, $I_F=3A$,
Dynamic test circuit in Figure E)



V_F , FORWARD VOLTAGE

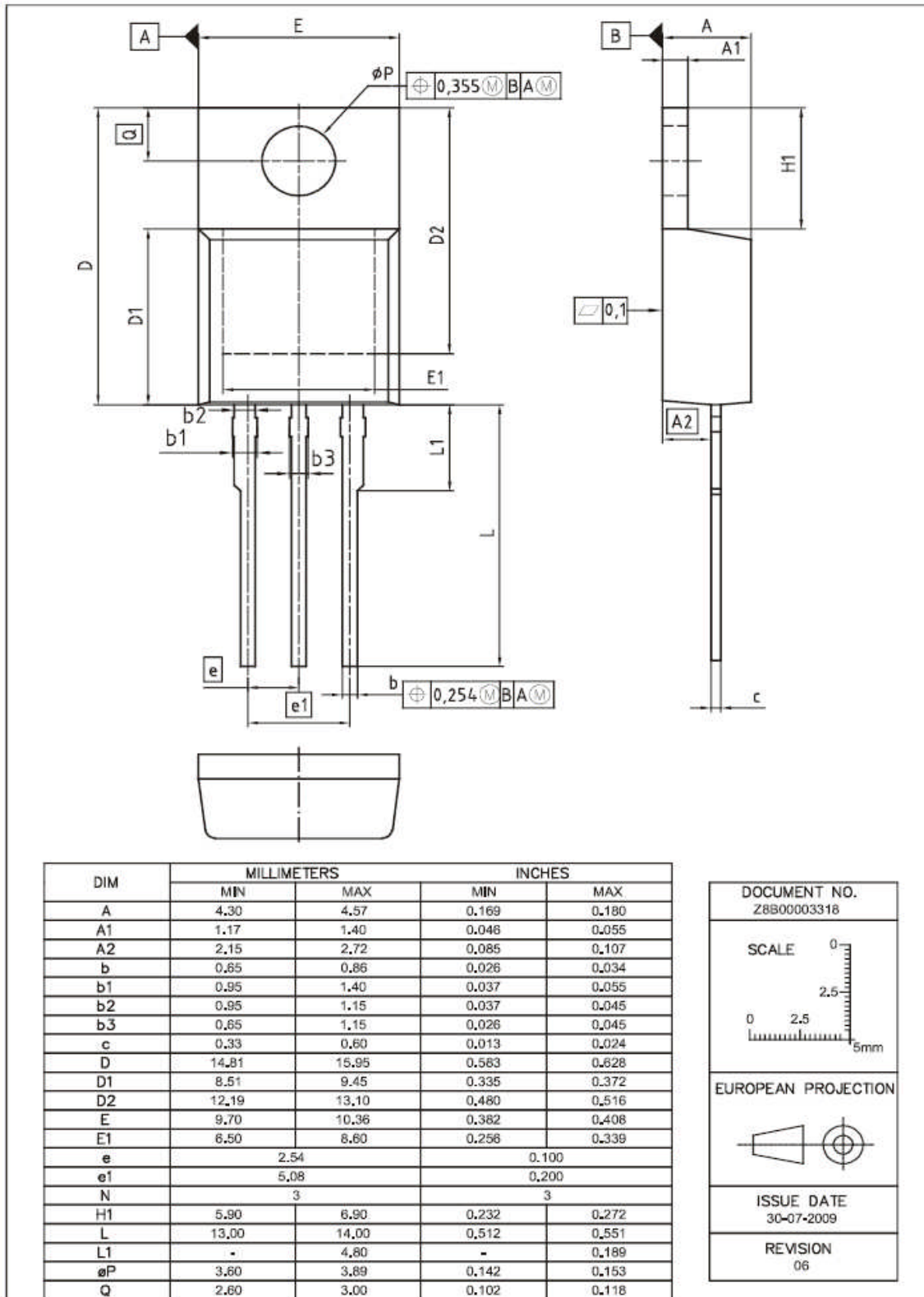
Figure 27. Typical diode forward current as a function of forward voltage



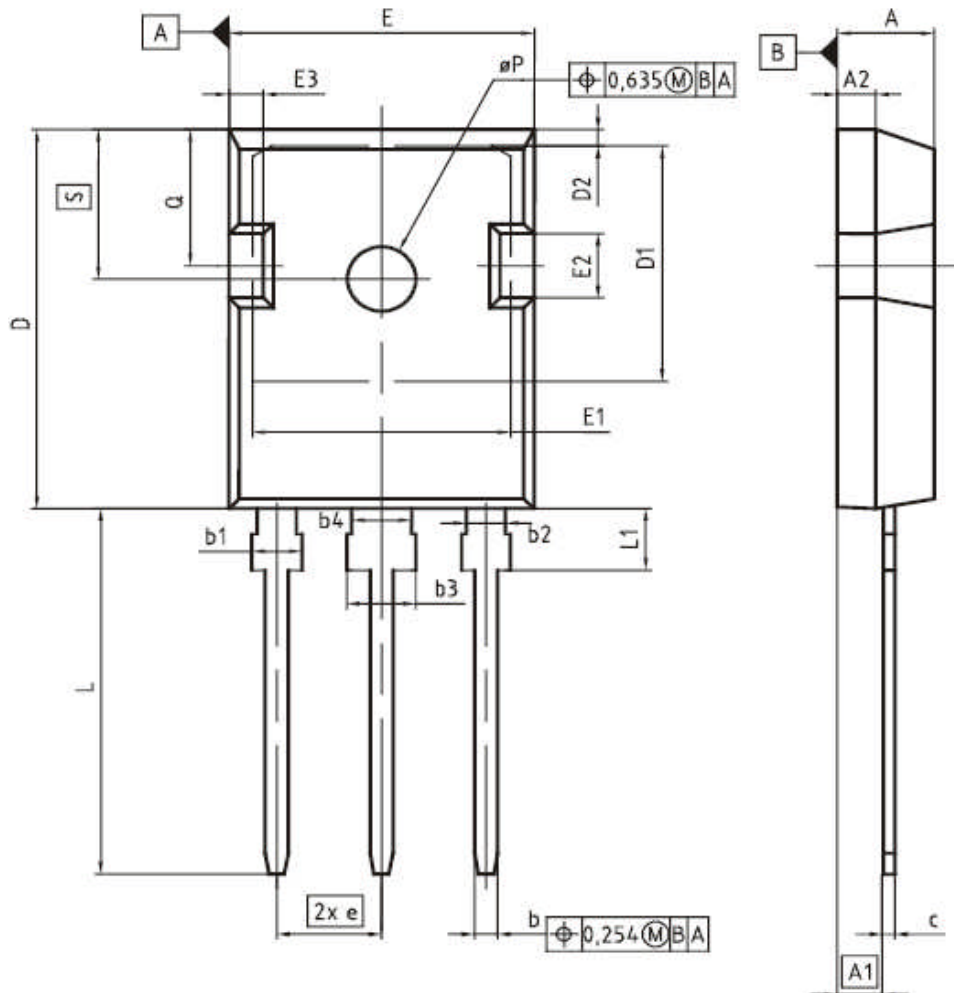
T_J , JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

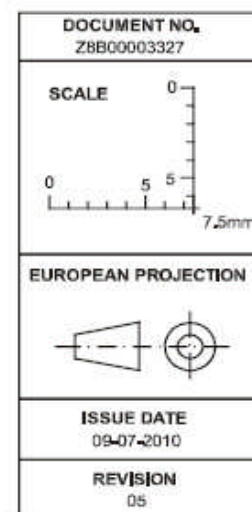
PG-TO220-3

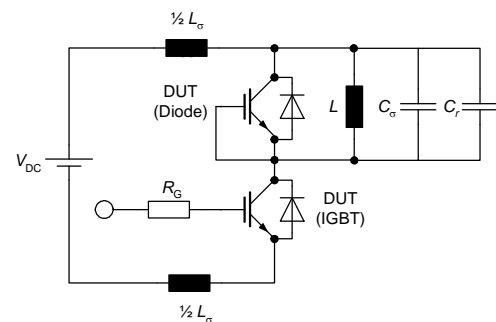
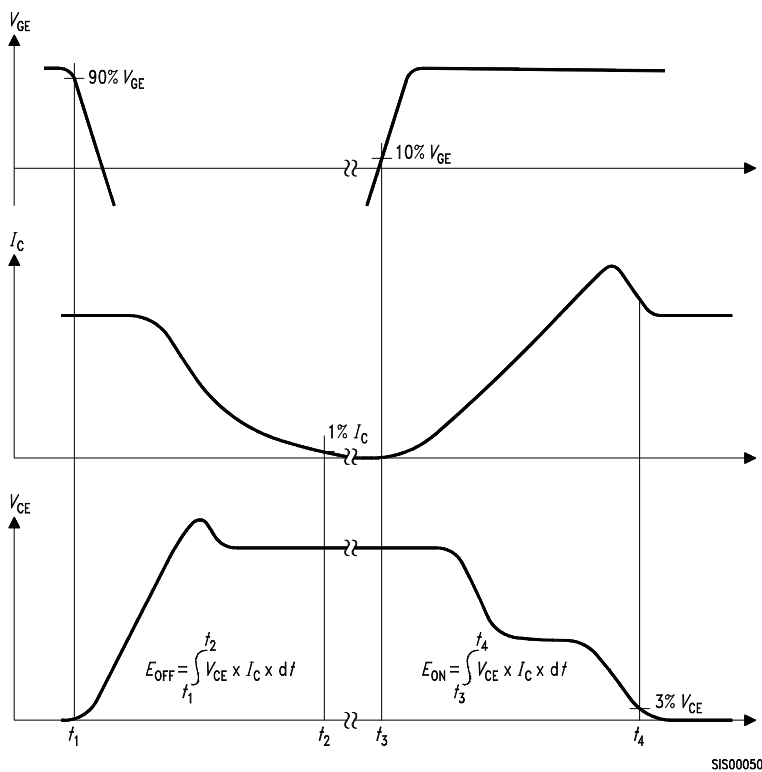
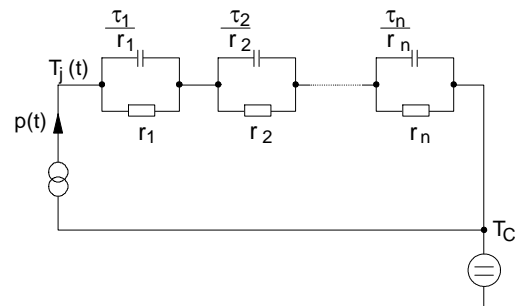
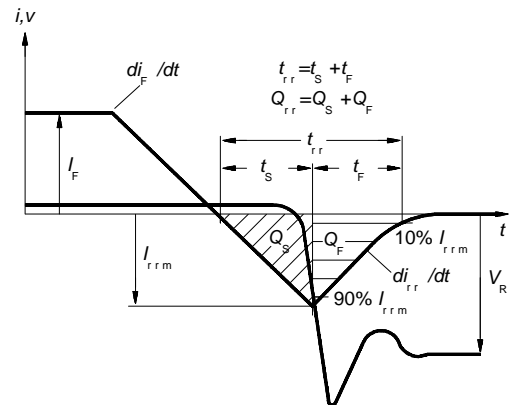
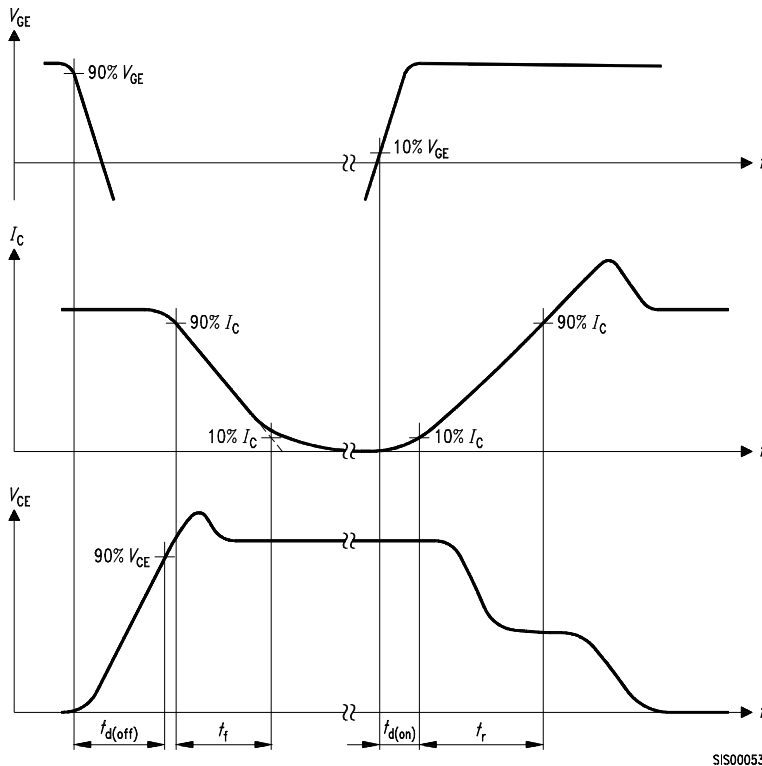


PG-TO247-3



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4,83	5,21	0,190	0,205
A1	2,27	2,54	0,089	0,100
A2	1,85	2,16	0,073	0,085
b	1,07	1,33	0,042	0,052
b1	1,90	2,41	0,075	0,095
b2	1,90	2,16	0,075	0,085
b3	2,87	3,38	0,113	0,133
b4	2,87	3,13	0,113	0,123
c	0,55	0,68	0,022	0,027
D	20,80	21,10	0,819	0,831
D1	16,25	17,85	0,640	0,695
D2	0,95	1,35	0,037	0,053
E	15,70	16,13	0,618	0,635
E1	13,10	14,15	0,516	0,557
E2	3,68	5,10	0,145	0,201
E3	1,00	2,60	0,039	0,102
e	5,44 (BSC)		0,214 (BSC)	
N	3		3	
L	19,80	20,32	0,780	0,800
L1	4,10	4,47	0,161	0,176
eP	3,50	3,70	0,138	0,146
Q	5,49	6,00	0,216	0,236
S	6,04	6,30	0,238	0,248





Published by
Infineon Technologies AG
81726 Munich, Germany
© 2013 Infineon Technologies AG
All Rights Reserved.

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

The Infineon Technologies component described in this Data Sheet may be used in life-support devices or systems and/or automotive, aviation and aerospace applications or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support, automotive, aviation and aerospace device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.