

#### Maximum Ratings: Total Device @TA = 25°C (unless otherwise specified)

Characteristic	Symbol	Value	Unit
Operating and Storage Temperature Range	$T_{OP},T_{stg}$	-55 to +150	°C

#### **Thermal Characteristics: Total Device**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R <sub>OJA</sub>	625	°C/W

#### Maximum Ratings: Sub-Component Devices @TA = 25°C unless otherwise specified

Characteristic	Symbol	Q1-PNP Transistor (MMBT2907A)	Q2-NPN Transistor (MMBTA06)	Unit
Collector-Base Voltage	$V_{CBO}$	-60	80	V
Collector-Emitter Voltage	$V_{CEO}$	-60	65	V
Emitter-Base Voltage	$V_{EBO}$	-5.5	6	V
Collector Current - Continuous (Note 5)	Ic	-600	500	mA

Note: 5. Device mounted on FR-4 substrate printed circuit board with 1 inch square 2oz copper pad area

#### Electrical Characteristics: PNP (MMBT2907A) Transistor (Q1) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-60		V	$I_{C} = -10\mu A, I_{E} = 0$	
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-60	ı	V	$I_C = -10 \text{mA}, I_B = 0$	
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5.5	ı	V	$I_E = -10\mu A, I_C = 0$	
Collector Cutoff Current	I <sub>CBO</sub>		-10	nA	$V_{CB} = -50V, I_{E} = 0$	
Collector Cutoff Current	I <sub>CEX</sub>	_	-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$	
Base Cutoff Current	I <sub>BL</sub>		-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$	
ON CHARACTERISTICS (Note 6)						
		100	_	_	$I_C = -100\mu A$ , $V_{CE} = -10V$	
		100	_	_	$I_C = -1.0 \text{mA}, V_{CE} = -10 \text{V}$	
DC Current Gain	$h_{FE}$	100	_		$I_C = -10 \text{mA}, V_{CE} = -10 \text{V}$	
		100 50	300	_	$I_C = -150 \text{mA}, V_{CE} = -10 \text{V}$	
		50			$I_C = -500 \text{mA}, V_{CE} = -10 \text{V}$	
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$		-0.3	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$	
	- OL(OAT)		-0.5		$I_C = -500 \text{mA}, I_B = -50 \text{mA}$	
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	-0.95	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$	
•	1 DE(0/11)		-1.3		$I_C = -500 \text{mA}, I_B = -50 \text{mA}$	
SMALL SIGNAL CHARACTERISTICS		1		1	Tv. 00/1 40 4	
Current Gain-Bandwidth Product	f⊤	100	_	MHz	$V_{CE} = -2.0V, I_{C} = -10mA,$ f = 100MHz	
SWITCHING CHARACTERISTICS						
Turn-On Time	t <sub>on</sub>	_	45	ns		
Delay Time	t <sub>d</sub>	_	10	ns	$V_{CE} = -30V, I_{C} = -150mA,$	
Rise Time	t <sub>r</sub>	_	40	ns	I <sub>B1</sub> = -15mA	
Turn-Off Time	t <sub>off</sub>	_	100	ns		
Storage Time	ts	_	80	ns	$V_{CC} = -6.0V, I_{C} = -150mA,$	
Fall Time		_	30	ns	$I_{B1} = I_{B2} = -15 \text{mA}$	
	t <sub>f</sub>	_	30	ns	I <sub>B1</sub> = I <sub>B2</sub> = -15mA	



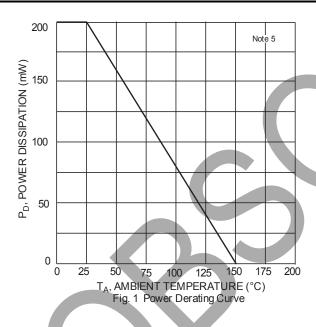
## Electrical Characteristics: NPN (MMBTA06) Transistor (Q2) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	80	_	_	V	$I_C = 100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	65	_	_	V	$I_{C} = 1mA, I_{B} = 0$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	6	_	_	V	$I_E = 100 \mu A, I_C = 0$
Collector-Base Cutoff Current	I <sub>CBO</sub>	I	_	100	nA	$V_{CB} = 80V, I_{E} = 0$
Collector Cutoff Current	I <sub>CES</sub>	_	_	100	nA	$V_{CE} = 90V, V_{BE} = 0$
Emitter-Base Cutoff Current	I <sub>EBO</sub>	_	_	100	nA	$V_{EB} = 5V, I_{C} = 0$
ON CHARACTERISTICS (Note 6)						
DC Current Gain	h	250	_	_	_	$V_{CE} = 1V$ , $I_C = 10mA$
DC Current Gain	h <sub>FE</sub>	100	_	_	_	V <sub>CE</sub> = 1V, I <sub>C</sub> = 100mA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	0.2	0.4	V	I <sub>C</sub> = 100mA, I <sub>B</sub> = 10mA
Base-Emitter Turn-on Voltage	V <sub>BE(ON)</sub>	0.7	0.75	8.0	V	$V_{CE} = 1V, I_{C} = 100mA$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	_	0.95	V	I <sub>C</sub> = 100mA, I <sub>B</sub> = 5mA
SMALL SIGNAL CHARACTERISTICS						
Current Gain-Bandwidth Product	f⊤	100	_	_	MHz	V <sub>CE</sub> = 20V, I <sub>C</sub> = 10mA, f = 100MHz

Notes:

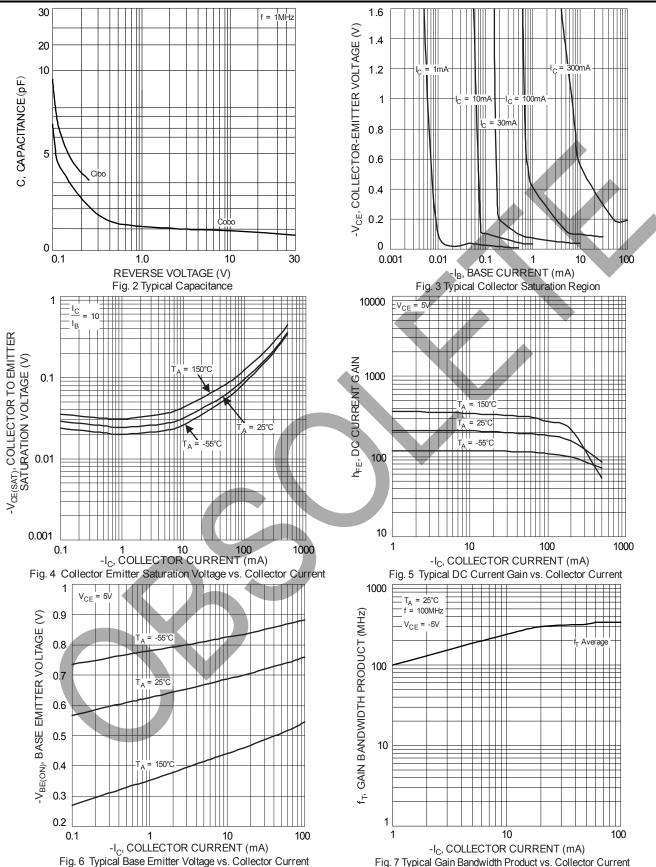
6. Short duration pulse test used to minimize self-heating effect.

### Typical Characteristics @TA = 25°C unless otherwise specified





#### PNP (MMBT2907A) Transistor (Q1) Plots





#### NPN (MMBTA06) Transistor (Q2) Plots

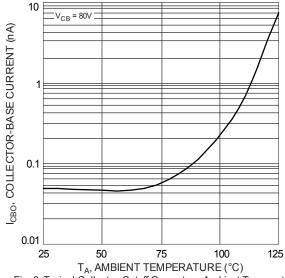


Fig. 8 Typical Collector-Cutoff Current vs. Ambient Temperature

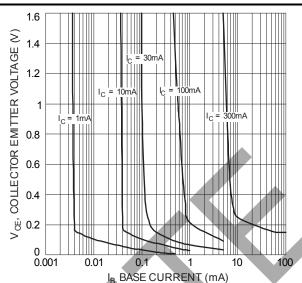


Fig. 9 Typical Collector Saturation Region

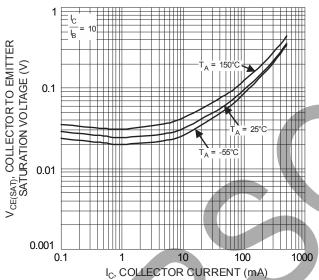
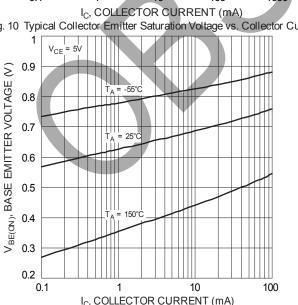


Fig. 10 Typical Collector Emitter Saturation Voltage vs. Collector Curren



 $I_{C}$ , COLLECTOR CURRENT (mA) Fig. 12 Typical Base Emitter Voltage vs. Collector Current

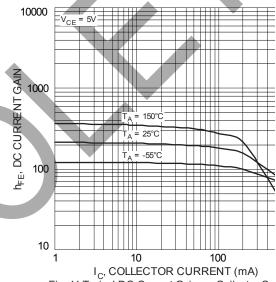


Fig. 11 Typical DC Current Gain vs. Collector Cu

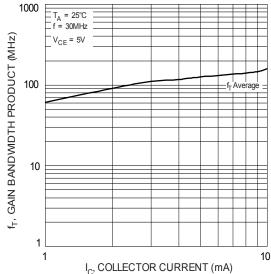
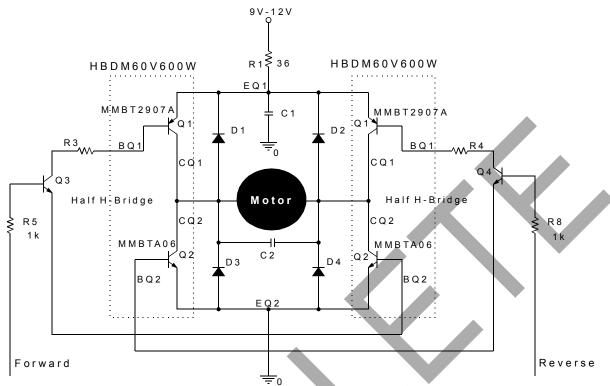


Fig. 13 Typical Gain Bandwidth Product vs. Collector Current



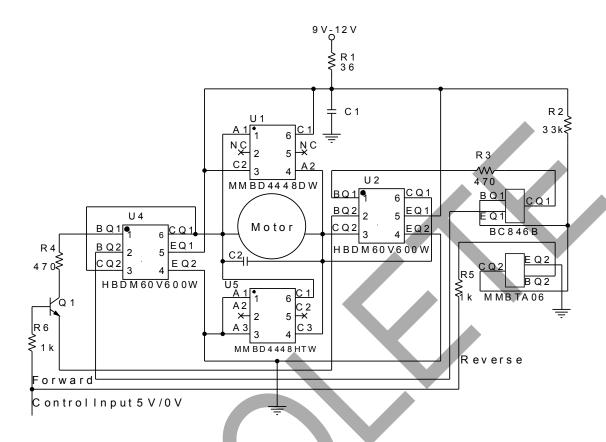
### **Current Schematic with Application Example**



Note: D1, D2, D3, D4: Switching Diodes (MMBD4448) Q3, Q4: NPN Transistors (MMBTA06)



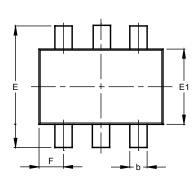
# Application Example Schematic (with Package Pinouts)

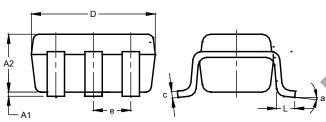




### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





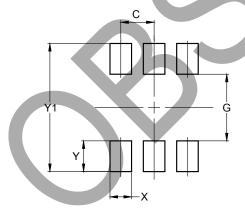
SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.10	0.30	0.25			
C	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

#### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT363**

**SOT363** 



Dimensions	Value				
Dilliciisions	(in mm)				
С	0.650				
G	1.300				
X	0.420				
Y	0.600				
Y1	2.500				



#### **IMPORTANT NOTICE**

- 1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com