

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	10.4 8.4	A
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	10	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	65	A
Avalanche Current, L = 0.3mH			I _{AS}	15.8	A
Avalanche Energy, L = 0.3mH			E _{AS}	37.5	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	106	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	1.84	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	68	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	9.2	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 48V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.7	—	2	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	8.4	11	mΩ	V _{GS} = 10V, I _D = 10A
		—	11.5	14		V _{GS} = 4.5V, I _D = 5A
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	1522	—	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	352	—		
Reverse Transfer Capacitance	C _{rss}	—	27.5	—		
Gate Resistance	R _g	—	1.4	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	10.7	—	nC	V _{DS} = 30V, I _D = 10A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	22.2	—		
Gate-Source Charge	Q _{gs}	—	3.3	—		
Gate-Drain Charge	Q _{gd}	—	4.2	—		
Turn-On Delay Time	t _{d(ON)}	—	4.4	—	ns	V _{GS} = 10V, V _{DS} = 30V, R _G = 6Ω, I _D = 10A
Turn-On Rise Time	t _r	—	6.7	—		
Turn-Off Delay Time	t _{d(OFF)}	—	25.5	—		
Turn-Off Fall Time	t _f	—	12.5	—		
Body Diode Reverse Recovery Time	t _{RR}	—	25.8	—	ns	I _F = 10A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	—	15.1	—	nC	

- Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

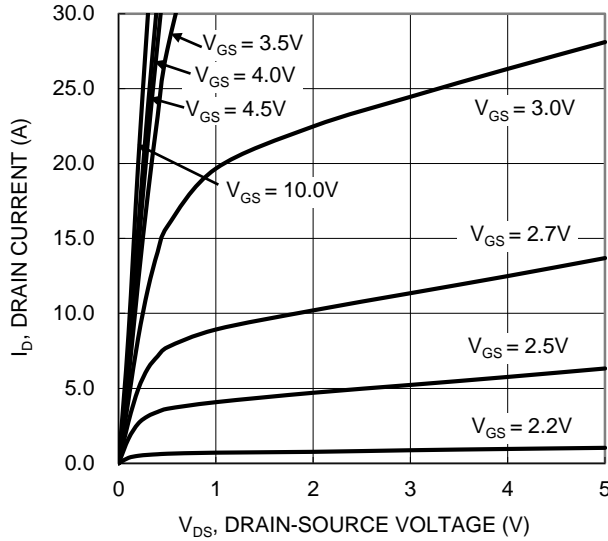


Figure 1. Typical Output Characteristic

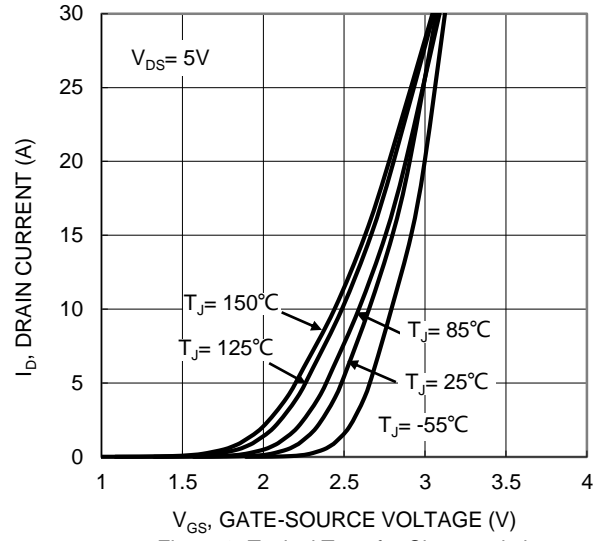


Figure 2. Typical Transfer Characteristic

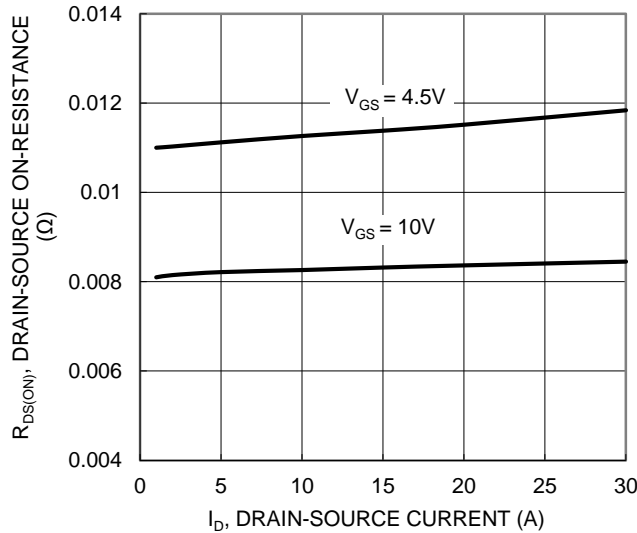


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

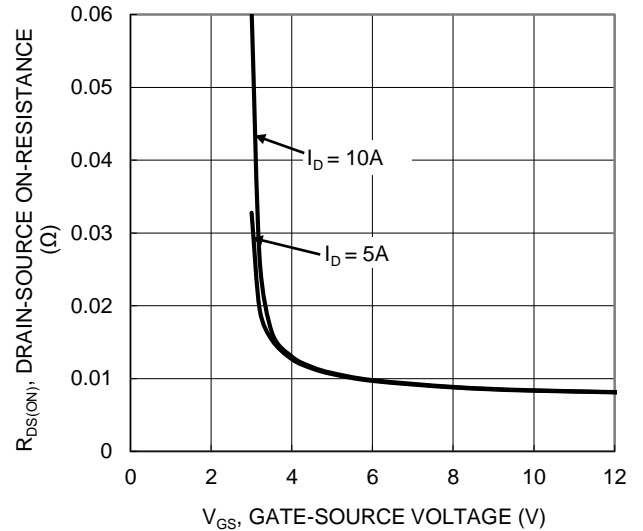


Figure 4. Typical Transfer Characteristic

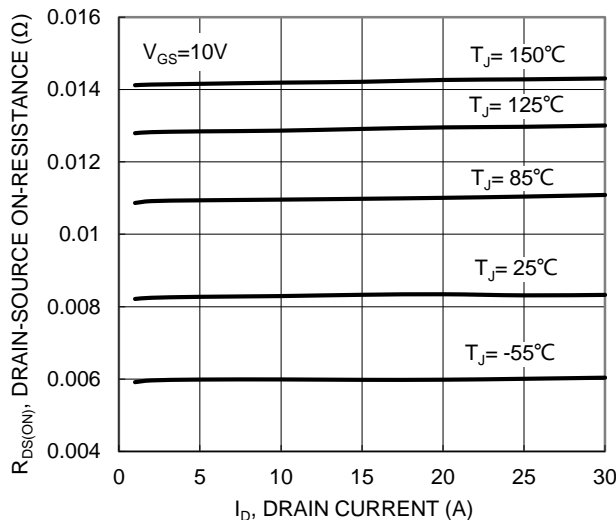


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

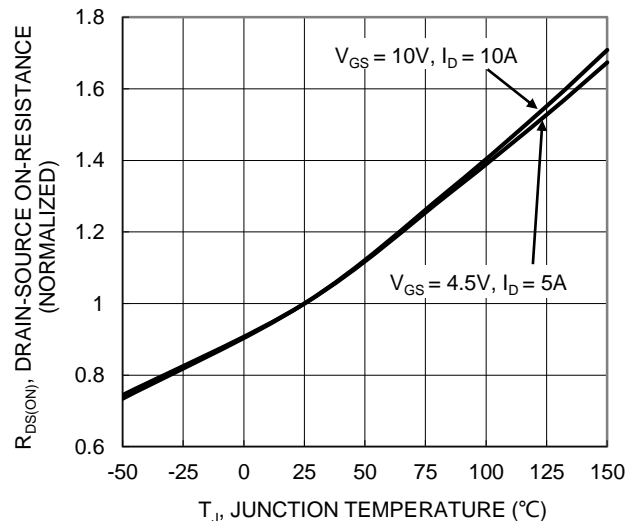
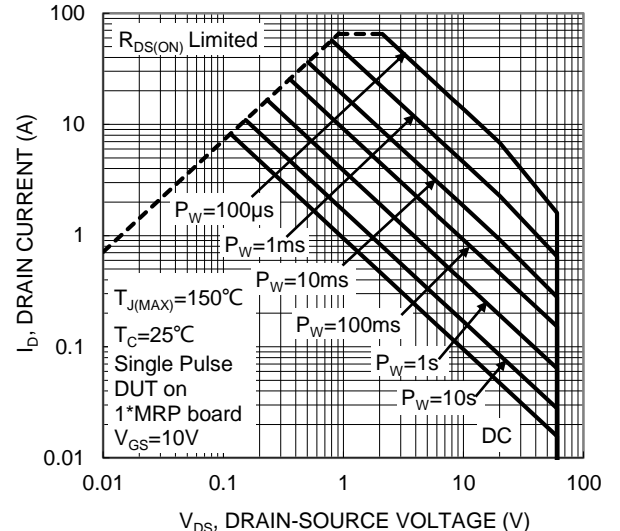
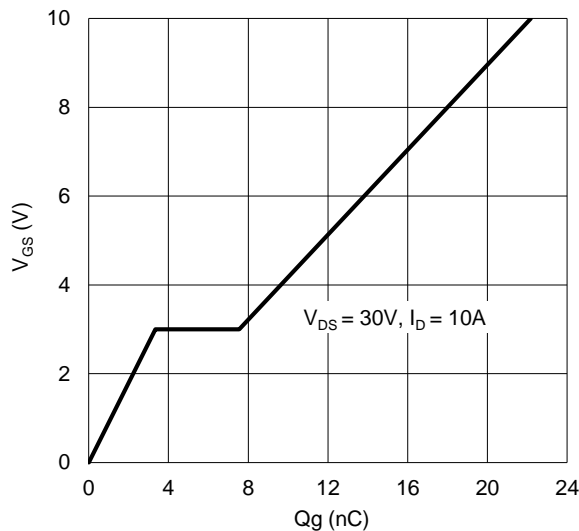
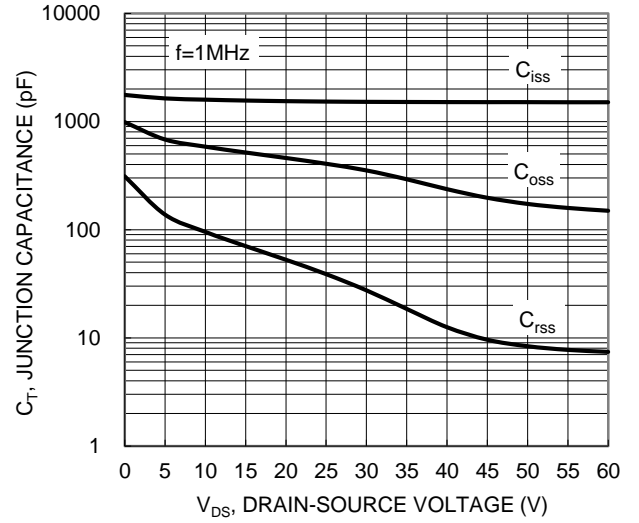
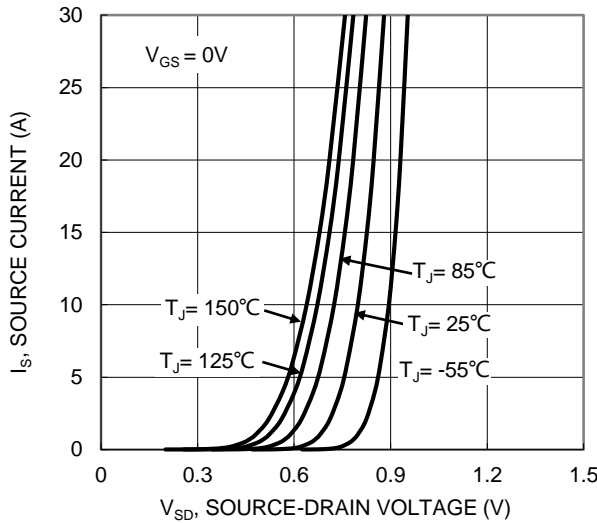
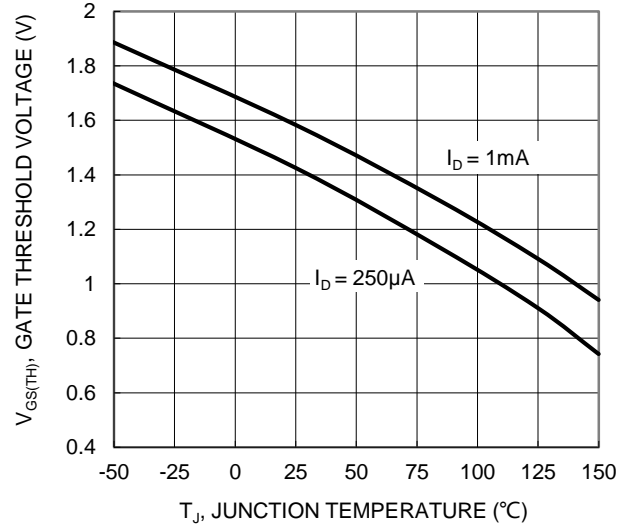
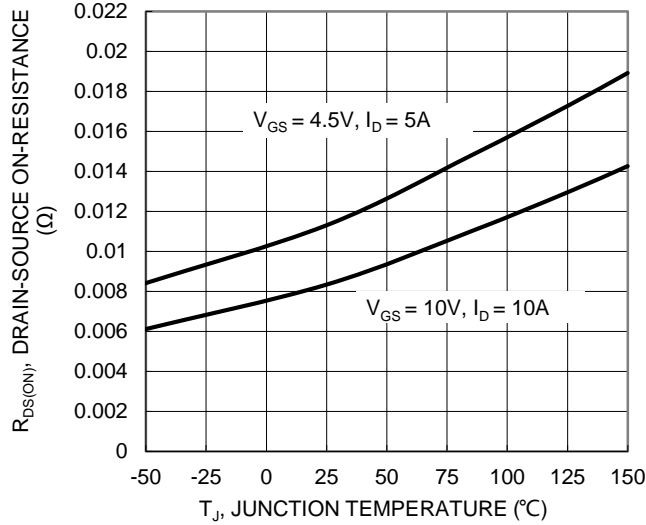


Figure 6. On-Resistance Variation with Temperature



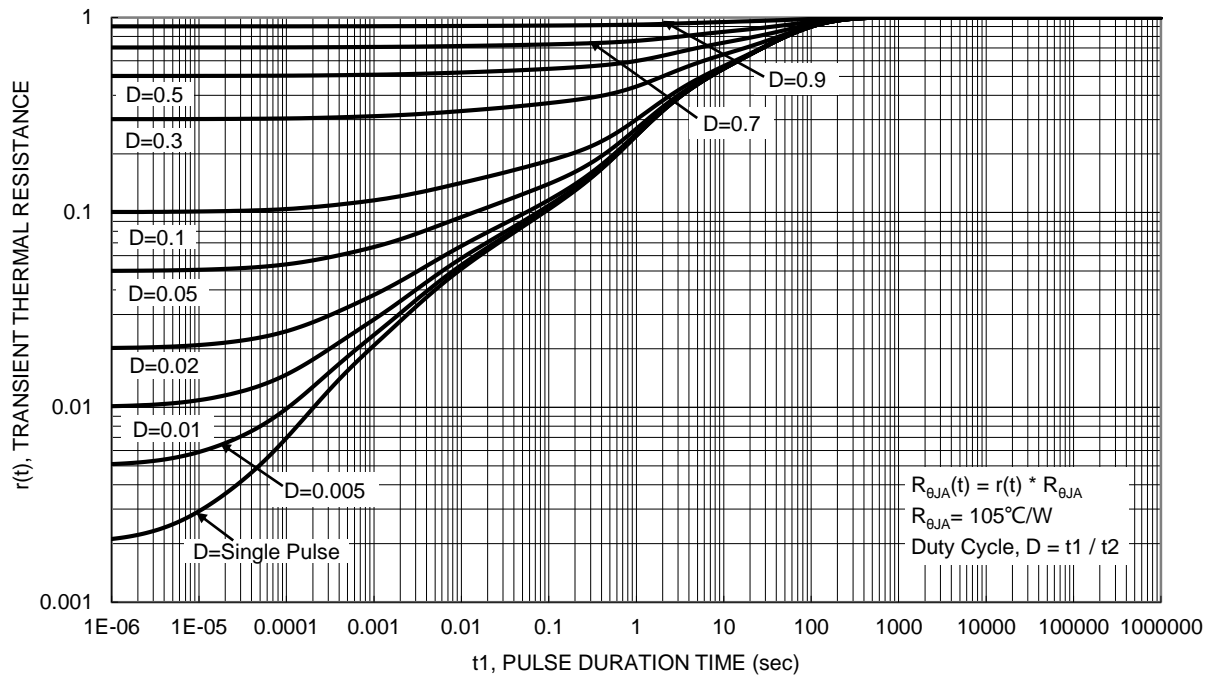
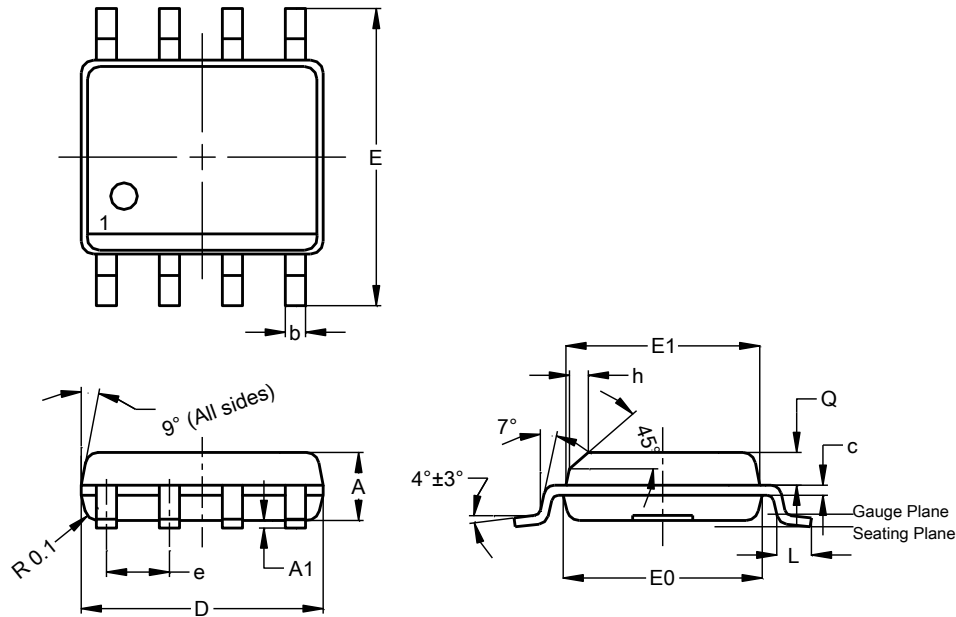


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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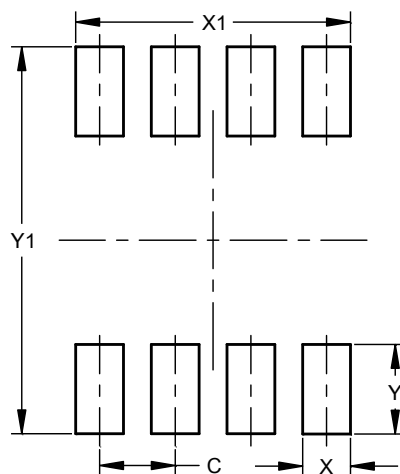


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Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	--	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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