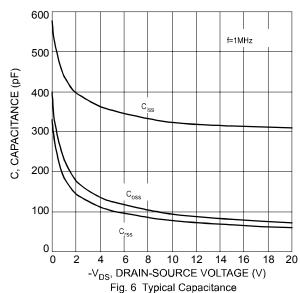


Fig. 4 On-Resistance vs. Drain Current and Gate Voltage





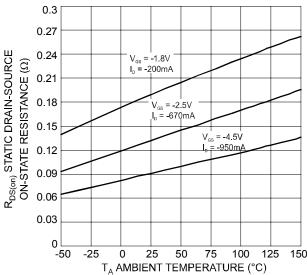
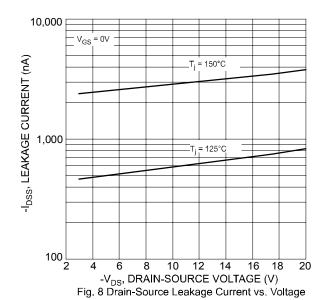
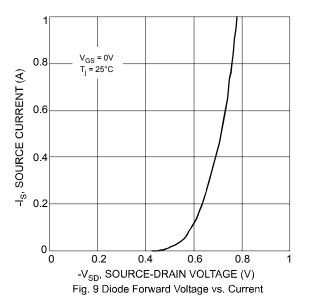


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature





Ordering Information (Note 5)

Part Number	Case	Packaging
DMP2104LP-7	DFN1411-3	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



PA = Marking Code YM = Date Code Marking Y = Year ex: U = 2007 M = Month ex: 9 = September

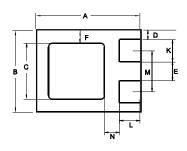
Date Code Key

Year	20	07	20	08	20	09	20	10	20	11	20	12
Code	Ų	J	V		W		Χ		Υ		Z	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



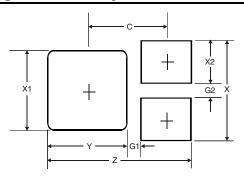
Package Outline Dimensions





DFN1411-3						
Dim	Min	Max	Тур			
Α	1.35	1.48	1.40			
В	1.05	1.18	1.10			
С	0.65	0.85	0.75			
D			0.125			
Е	_	_	0.25			
F	_	_	0.175			
G	0.47	0.53	0.50			
Н	0	0.05	0.02			
K	0.25	0.35	0.30			
L	0.22	0.33	0.275			
M	_	_	0.55			
N	_	_	0.20			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.38
G1	0.15
G2	0.15
Х	0.95
X1	0.75
X2	0.40
Y	0.75
С	0.76

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