

# **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			$V_{DSS}$	30	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current (Note 6) V - 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	4.2 3.3	Α
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ΙD	5.1 4	Α
Continuous Drain Current (Note 6) V - 4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	3.7 2.8	Α
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	4.3 3.3	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	60	Α		
Maximum Body Diode Forward Current (Note 6)			Is	2	Α

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	Pn	0.78	W
	T <sub>A</sub> = +70°C	PD	0.5	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	П	160	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	115	°C/W
Total Dawar Dissipation (Note 6)	T <sub>A</sub> = +25°C	Б	1.3	W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	$P_{D}$	0.8	
Thermal Desistance, Junction to Ambient (Note 6)	Steady state	П	96	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	68	°C/W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	18	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

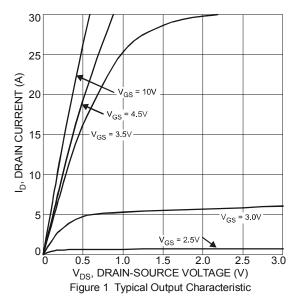
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

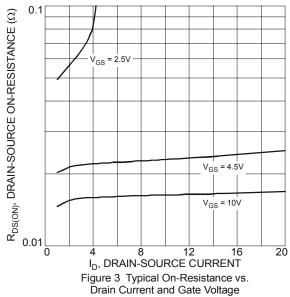
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				I.			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	V <sub>DS</sub> =24V, V <sub>GS</sub> = 0V	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)			<u>.                                    </u>				
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.1	_	2.1	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Ot ii D. i. Oassaa Oa Baaistaa		_	24	40	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.2A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	30	50		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2A	
Forward Transfer Admittance	IY <sub>fs</sub> I	_	2.7	_	S	V <sub>DS</sub> = 5V, I <sub>D</sub> =4.2A	
Diode Forward Voltage	V <sub>SD</sub>	_	0.75	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A	
DYNAMIC CHARACTERISTICS (Note 8)			<u>,                                    </u>		I		
Input Capacitance	C <sub>iss</sub>	_	697	_	pF		
Output Capacitance	Coss	_	97	_	pF	$V_{DS} = 15V, V_{GS} = 0V$	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	67	_	pF	f = 1.0MHz	
Gate Resistance	R <sub>g</sub>	_	1.47	_	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_g$	_	6	_		, == .	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	13.2	_	nC	V - 15V I 0A	
Gate-Source Charge	$Q_{gs}$		2.2	_	110	$V_{DS} = 15V, I_{D} = 9A$	
Gate-Drain Charge	$Q_{gd}$	_	1.8	_			
Turn-On Delay Time	$t_{D(ON)}$		4.3		ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		4.4	_	ns	$V_{DD}$ =15V, $V_{GEN}$ =10V, $R_{GEN}$ =6 $\Omega$ ,	
Turn-On Rise Time	t <sub>r</sub>	_	20.1	_	ns	R <sub>L</sub> =15Ω	
Turn-Off Fall Time	t <sub>f</sub>	_	4.1	_	ns	1	
Reverse Recovery Time	t <sub>rr</sub>	_	7.3	_	Ns	IF = 9A, di/dt = 500A/μs	
Reverse Recovery Charge	Q <sub>rr</sub>	_	7.9	_	nC	IF = 9A, di/dt = 500A/µs	

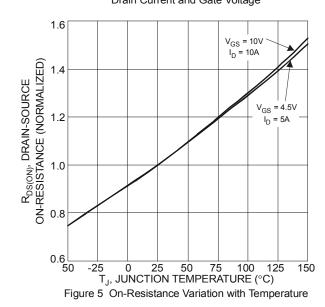
Notes: 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided. The power dissipation P<sub>D</sub> is based on t<10s R<sub>BJA</sub>.

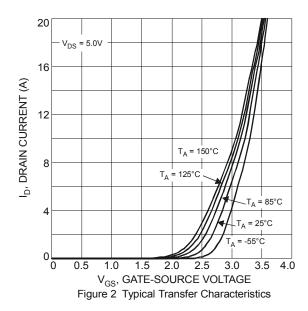
- 6. Device mounted on 1"  $\times$  1" FR-4 PCB with high coverage 2 oz. Copper, single sided. The power dissipation PD is based on t<10s R<sub>BJA</sub>.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.











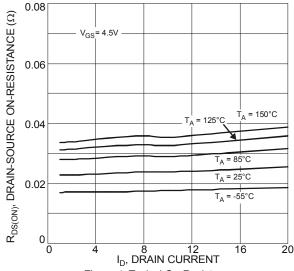
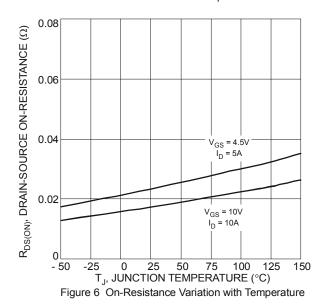


Figure 4 Typical On-Resistance vs. Drain Current and Temperature





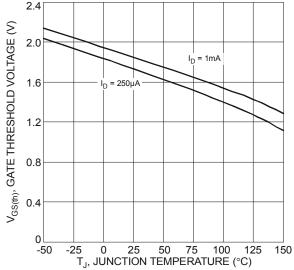


Figure 7 Gate Threshold Variation vs. Ambient Temperature

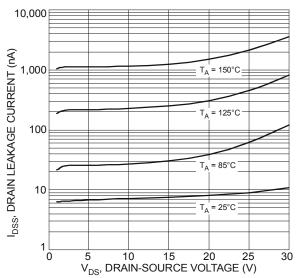
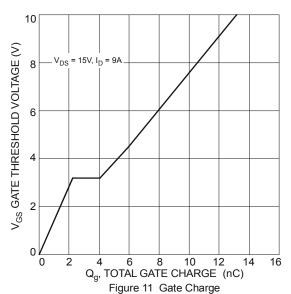
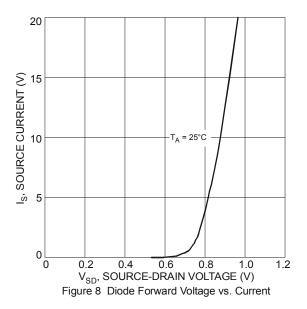
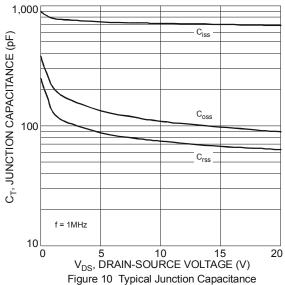


Figure 9 Typical Drain-Source Leakage Current vs. Voltage



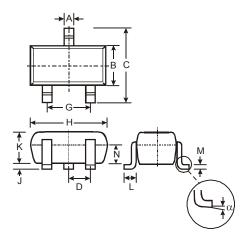






# **Package Outline Dimensions**

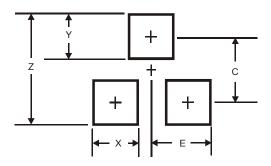
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SC59					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	-	-	0.95		
G	-	-	1.90		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.4
X	0.8
Υ	1.0
С	2.4
E	1.35



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