July 2021

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		VDSS	30	V
Gate-Source Voltage		Vgss	±20	V
	T _A = +25°C	,	16	- A
Continuous Drain Current, V _{GS} = 10V (Note 6)	$T_A = +70$ °C	ID	13	
	T _C = +25°C	- I _D	45	А
	Tc = +70°C		35	
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)	IDM	80	Α	
Maximum Continuous Body Diode Forward Current (Note 6)		Is	20	Α
Avalanche Current, L = 0.1mH		las	33	Α
Avalanche Energy, L = 0.1mH		Eas	55	mJ

Thermal Characteristics

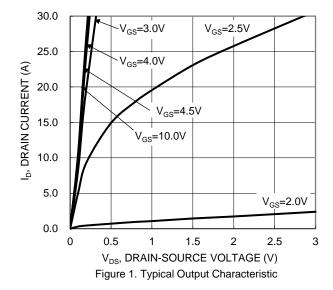
Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	Pp	0.9	W	
	T _A = +70°C	טיי	0.6] vv	
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	137	°C/W	
Total Power Dissipation (Note 6)	T _A = +25°C	Pn	2.1	W	
	T _A = +70°C	PD	1.4] vv	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	59	°C/W	
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	7.8	°C/W	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	V _G S = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 24V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	Vgs(TH)	1	_	2.5	V	V _{DS} = V _{GS} , I _D = 250μA	
Static Drain-Source On-Resistance	RDS(ON)	_	3.4	5.5	mΩ	Vgs = 10V, ID = 20A	
Static Dialii-Source Off-Resistance		_	4.4	9		Vgs = 4.5V, ID = 16A	
Diode Forward Voltage	V_{SD}	_	0.7	1	V	$V_{GS} = 0V$, $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		2,000	_	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	315	_	pF		
Reverse Transfer Capacitance	Crss	_	248	_	pF		
Gate Resistance	Rg	_	2.2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	20	_	nC	V _{DS} = 15V, I _D = 15A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	42	_	nC		
Gate-Source Charge	Qgs	_	4.7	_	nC		
Gate-Drain Charge	Q_{gd}	_	7.4	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	3.9	_	ns		
Turn-On Rise Time	t _R	_	4.1	_	ns	$V_{DD} = 15V, V_{GS} = 10V,$ $R_{G} = 3.3\Omega, I_{D} = 15A$	
Turn-Off Delay Time	tD(OFF)	_	31	_	ns		
Turn-Off Fall Time	tF	_	14.6	_	ns		
Reverse Recovery Time	t _{RR}	_	15	_	ns	-I _F = 15A, di/dt = 100A/μs	
Reverse Recovery Charge	Qrr	_	6	_	nC		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect. Notes:





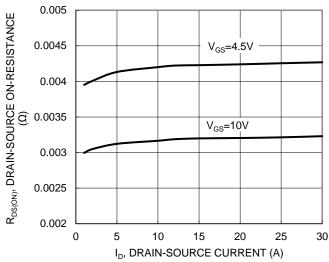


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

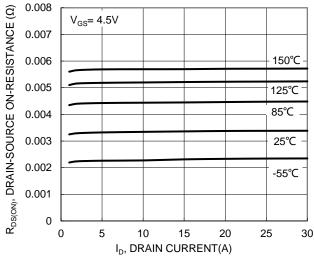
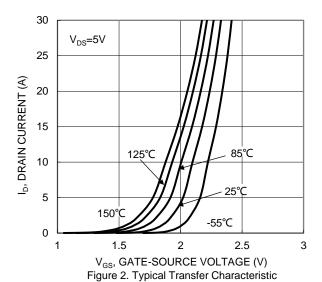
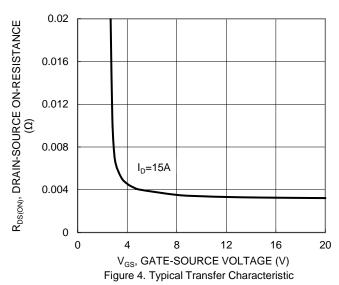


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





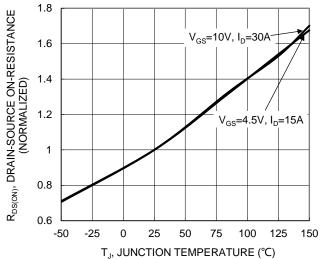


Figure 6. On-Resistance Variation with Temperature

DMN3009SFG



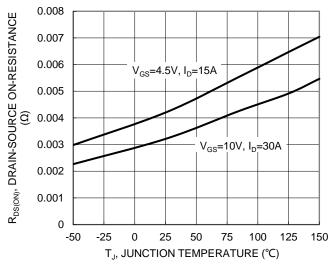
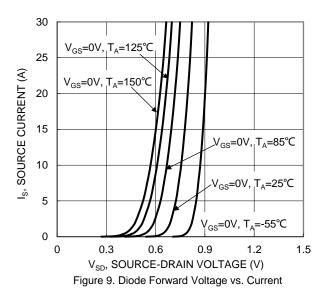
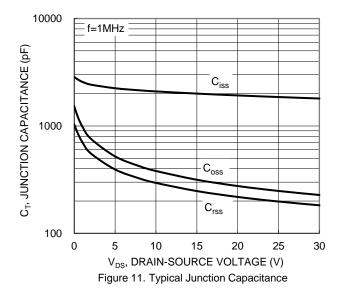


Figure 7. On-Resistance Variation with Temperature





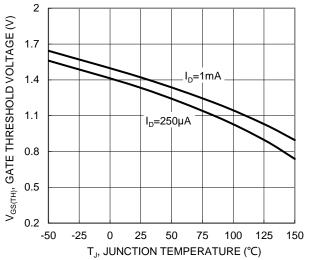


Figure 8. Gate Threshold Variation vs. Junction Temperature

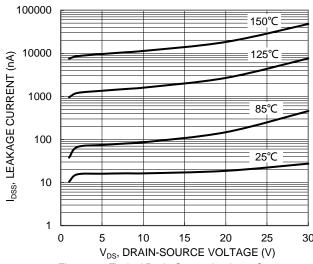
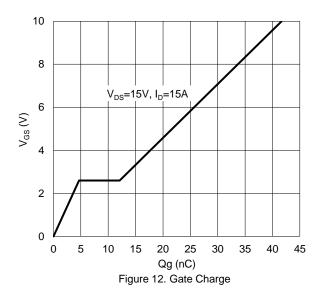
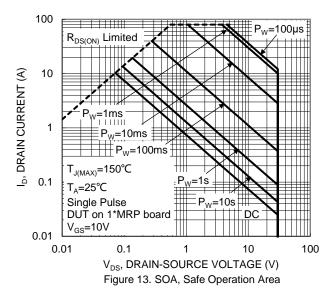


Figure 10. Typical Drain-Source Leakage Current vs. Voltage







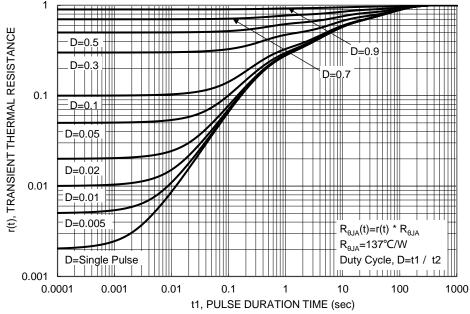


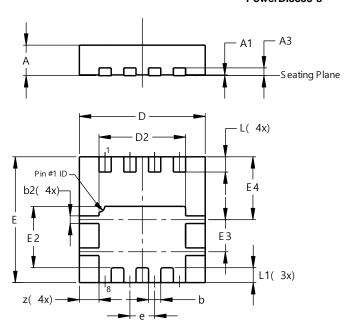
Figure 14. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

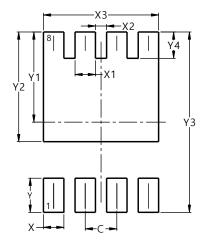


PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3	-	-	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
D	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
Е	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
е	-	_	0.65		
L	0.35	0.45	0.40		
L1	_	_	0.39		
Z	_	_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Υ	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



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