## Maximum Ratings (@$T_A$ = +25°C, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Unit |
|---|---|---|---|---|---|
| Drain-Source Voltage | | | $V_{DSS}$ | 20 | V |
| Gate-Source Voltage | | | $V_{GSS}$ | ±8 | V |
| Continuous Drain Current (Note 6) $V_{GS}$ = 4.5V | Steady State | $T_A$ = +25°C<br>$T_A$ = +100°C | $I_D$ | 1.0<br>0.7 | A |
| Pulsed Drain Current (10µs pulse, duty cycle = 1%) | | | $I_{DM}$ | 6 | A |

## Thermal Characteristics (@$T_A$ = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|---|---|---|---|
| Total Power Dissipation (Note 5) | $P_D$ | 0.52 | W |
| Total Power Dissipation (Note 6) | $P_D$ | 1.07 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{\theta JA}$ | 240 | °C/W |
| Thermal Resistance, Junction to Ambient (Note 6) | $R_{\theta JA}$ | 117 | °C/W |
| Operating and Storage Temperature Range | $T_J$, $T_{STG}$ | -55 to +150 | °C |

## Electrical Characteristics (@$T_A$ = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|---|---|---|---|---|---|---|
| **OFF CHARACTERISTICS (Note 7)** | | | | | | |
| Drain-Source Breakdown Voltage | $BV_{DSS}$ | 20 | - | - | V | $V_{GS}$ = 0V, $I_D$ = 250µA |
| Zero Gate Voltage Drain Current $T_J$ = +25°C | $I_{DSS}$ | - | - | 1 | µA | $V_{DS}$ = 20V, $V_{GS}$ = 0V |
| Gate-Source Leakage | $I_{GSS}$ | - | - | 10 | µA | $V_{GS}$ = ±8V, $V_{DS}$ = 0V |
| **ON CHARACTERISTICS (Note 7)** | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | 0.50 | - | 0.95 | V | $V_{DS}$ = $V_{GS}$, $I_D$ = 250µA |
| Static Drain-Source On-Resistance | $R_{DS\ (ON)}$ | - | - | 320 | mΩ | $V_{GS}$ = 4.5V, $I_D$ = 500mA |
| | | - | - | 500 | | $V_{GS}$ = 2.5V, $I_D$ = 400mA |
| | | - | - | 1,000 | | $V_{GS}$ = 1.8V, $I_D$ = 100mA |
| Diode Forward Voltage | $V_{SD}$ | - | 0.7 | 1.2 | V | $V_{GS}$ = 0V, $I_S$ = 300mA |
| **DYNAMIC CHARACTERISTICS (Note 8)** | | | | | | |
| Input Capacitance | $C_{iss}$ | - | 71 | - | pF | $V_{DS}$ = 10V, $V_{GS}$ = 0V, f = 1.0MHz |
| Output Capacitance | $C_{oss}$ | - | 12 | - | pF | |
| Reverse Transfer Capacitance | $C_{rss}$ | - | 9.4 | - | pF | |
| Gate Resistance | $R_g$ | - | 69 | - | Ω | $V_{DS}$ = 0V, $V_{GS}$ = 0V, f = 1MHz |
| Total Gate Charge | $Q_g$ | - | 0.89 | - | nC | $V_{GS}$ = 4.5V, $V_{DS}$ = 10V, $I_D$ = 1A |
| Gate-Source Charge | $Q_{gs}$ | - | 0.14 | - | nC | |
| Gate-Drain Charge | $Q_{gd}$ | - | 0.16 | - | nC | |
| Turn-On Delay Time | $t_{D(on)}$ | - | 4.9 | - | ns | $V_{DS}$ = 10V, $I_D$ = 1A, $V_{GS}$ = 4.5V, $R_G$ = 6Ω |
| Turn-On Rise Time | $t_r$ | - | 6.9 | - | ns | |
| Turn-Off Delay Time | $t_{D(off)}$ | - | 21.7 | - | ns | |
| Turn-Off Fall Time | $t_f$ | - | 10.6 | - | ns | |

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 25mm X 25mm square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

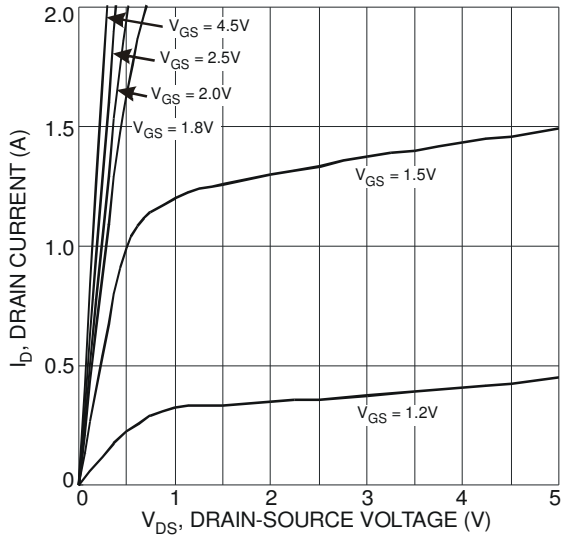



Fig. 1 Typical Output Characteristic

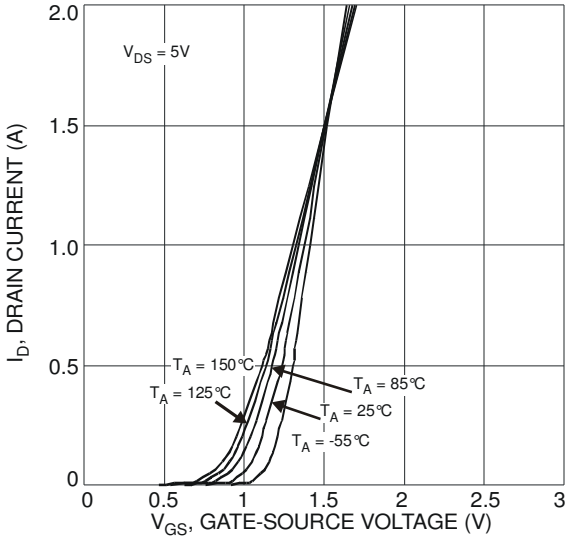



Fig. 2 Typical Transfer Characteristic

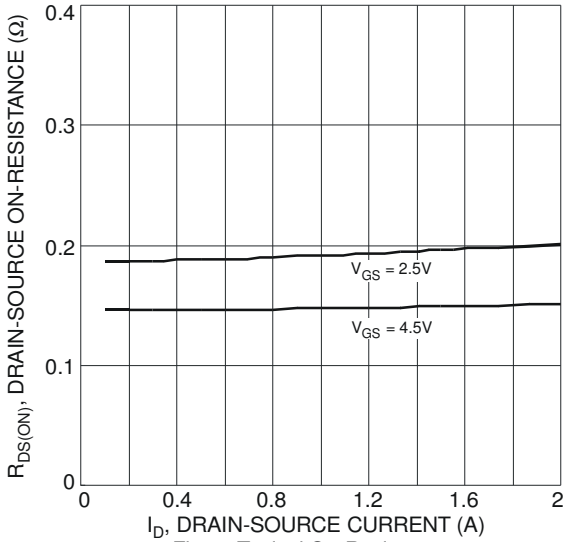



Fig. 3 Typical On-Resistance
vs. Drain Current and Gate Voltage

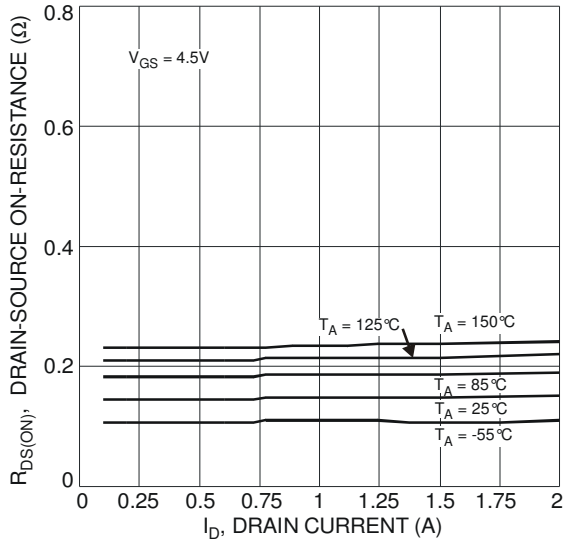



Fig. 4 Typical On-Resistance
vs. Drain Current and Temperature

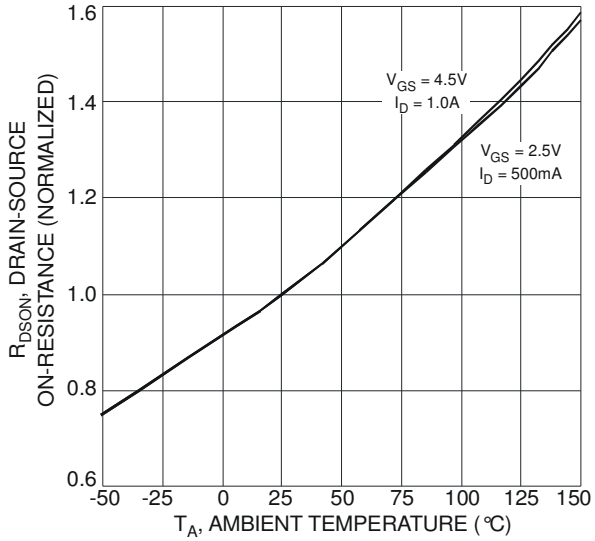



Fig. 5 On-Resistance Variation with Temperature

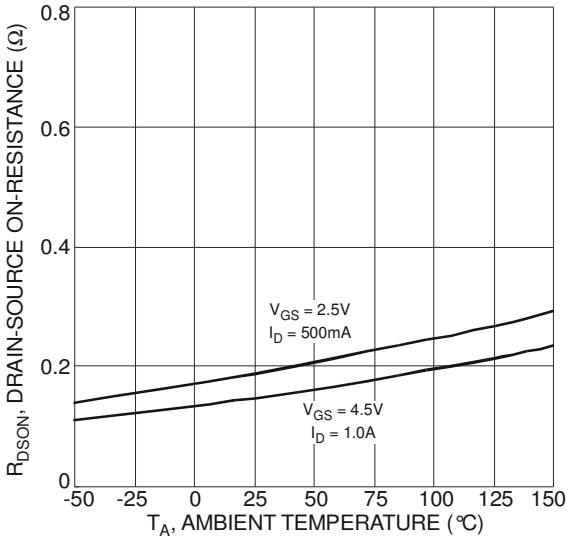



Fig. 6 On-Resistance Variation with Temperature

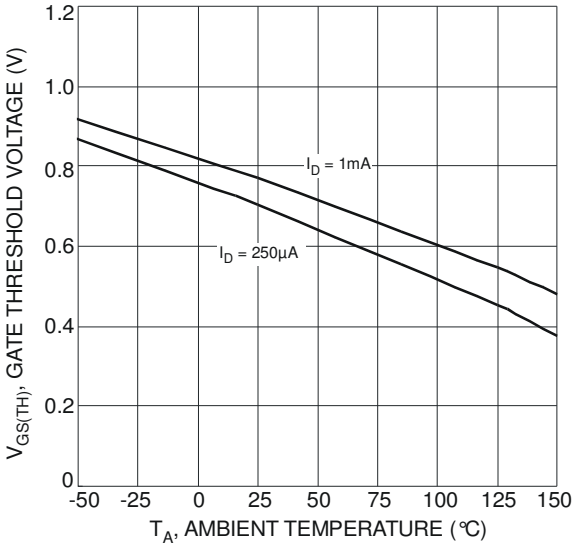



Fig. 7 Gate Threshold Variation vs. Ambient Temperature

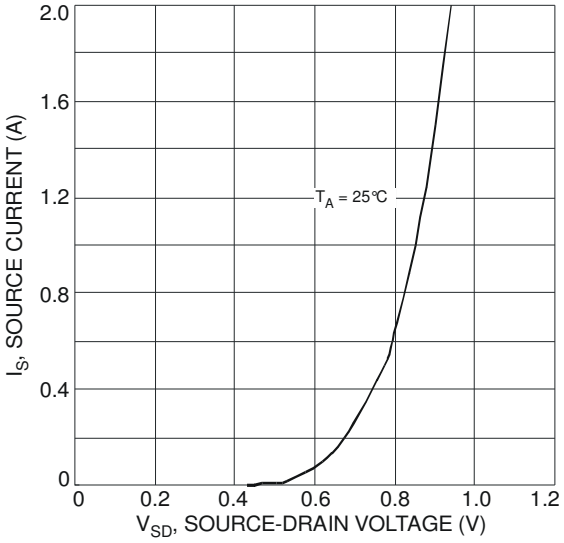



Fig. 8 Diode Forward Voltage vs. Current

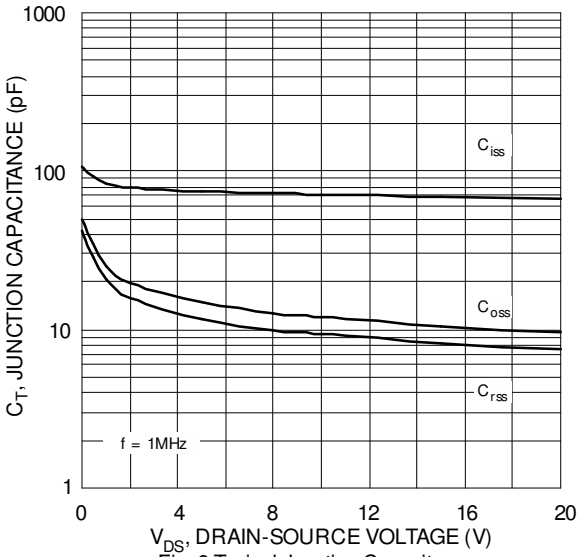



Fig. 9 Typical Junction Capacitance

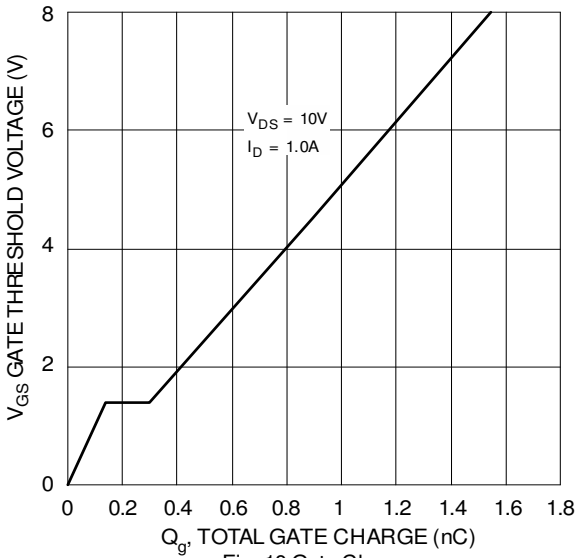



Fig. 10 Gate Charge

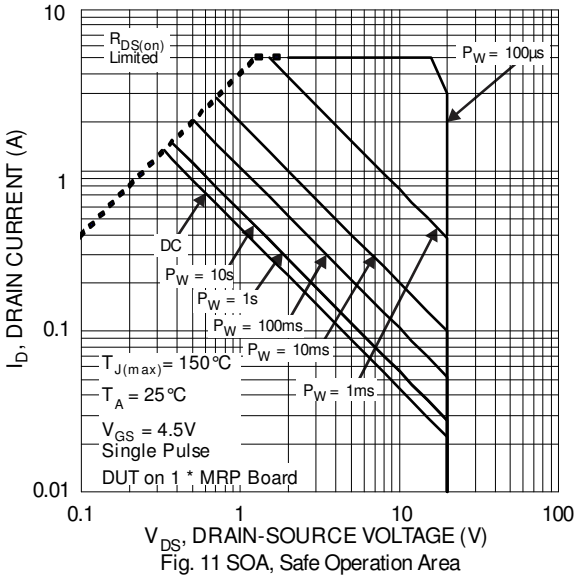



Fig. 11 SOA, Safe Operation Area

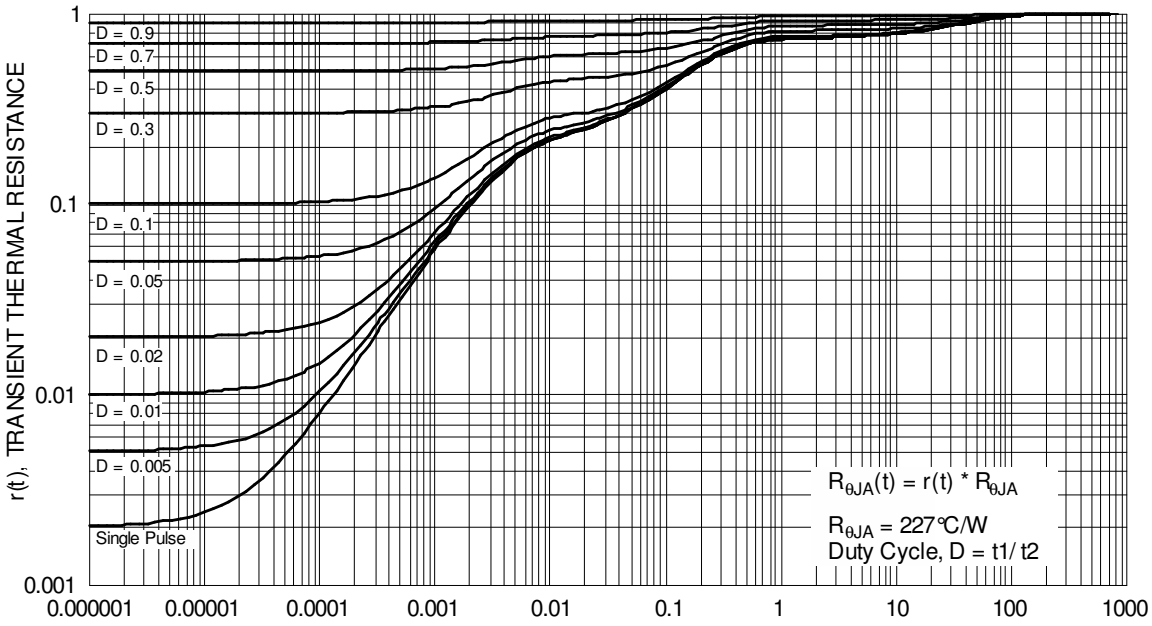
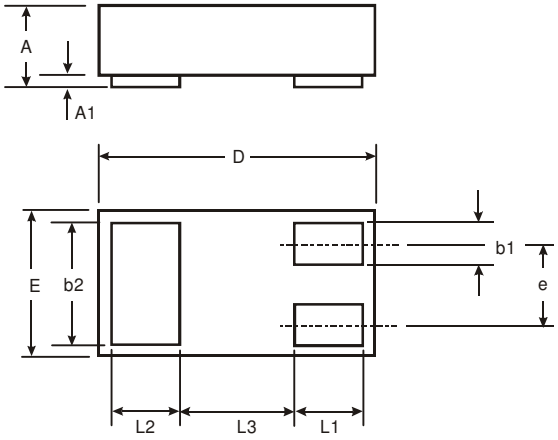



Fig. 12 Transient Thermal Resistance

## Package Outline Dimensions

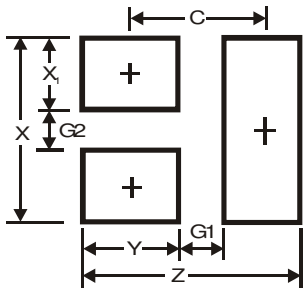
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.




| X2-DFN1006-3 | | | |
|---|---|---|---|
| **Dim** | **Min** | **Max** | **Typ** |
| **A** | — | 0.40 | — |
| **A1** | 0 | 0.05 | 0.03 |
| **b1** | 0.10 | 0.20 | 0.15 |
| **b2** | 0.45 | 0.55 | 0.50 |
| **D** | 0.95 | 1.05 | 1.00 |
| **E** | 0.55 | 0.65 | 0.60 |
| **e** | — | — | 0.35 |
| **L1** | 0.20 | 0.30 | 0.25 |
| **L2** | 0.20 | 0.30 | 0.25 |
| **L3** | — | — | 0.40 |
| **All Dimensions in mm** | | | |

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.




| Dimensions | Value (in mm) |
|---|---|
| **Z** | 1.1 |
| **G1** | 0.3 |
| **G2** | 0.2 |
| **X** | 0.7 |
| **X1** | 0.25 |
| **Y** | 0.4 |
| **C** | 0.7 |

**IMPORTANT NOTICE**

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

**LIFE SUPPORT**

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.



**www.diodes.com**