

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	30	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Forward Current @ T <sub>T</sub> = +121°C	I <sub>F(AV)</sub>	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	33	A

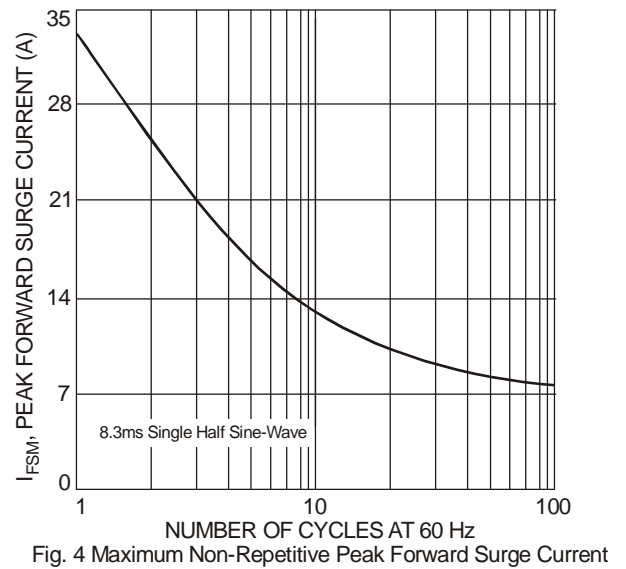
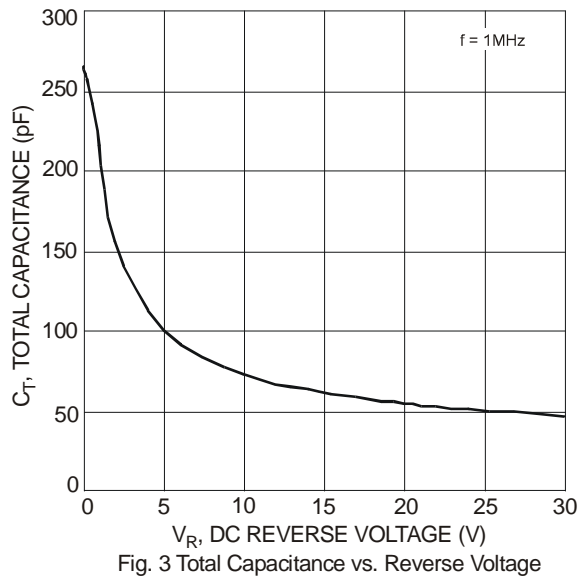
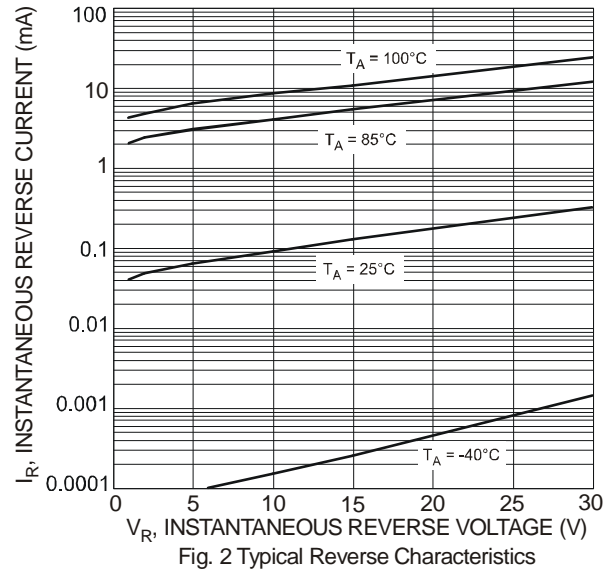
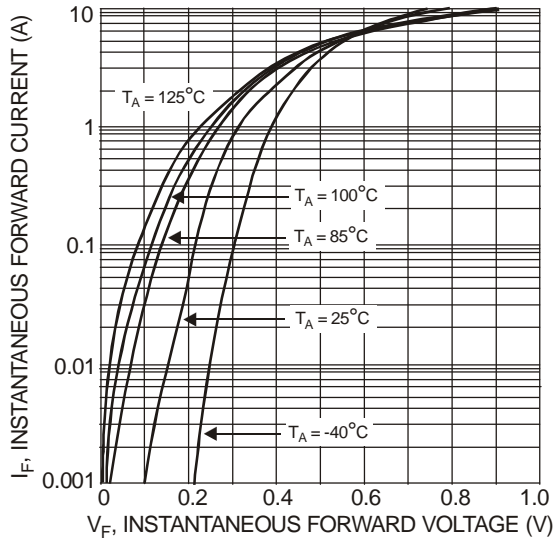
## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	1.67	W
Power Dissipation (Note 6)	P <sub>D</sub>	556	mW
Thermal Resistance Junction to Ambient (Note 5)	R <sub>θJA</sub>	60	°C/W
Thermal Resistance Junction to Ambient (Note 6)	R <sub>θJA</sub>	180	°C/W
Thermal Resistance Junction to Soldering (Note 7)	R <sub>θJS</sub>	10	°C/W
Operating Temperature Range	T <sub>J</sub>	-40 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-40 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V <sub>(BR)R</sub>	30	—	—	V	I <sub>R</sub> = 1.0mA
Forward Voltage	V <sub>F</sub>	—	0.310	—	V	I <sub>F</sub> = 1.0A
		—	0.375	0.420		I <sub>F</sub> = 2.0A
Leakage Current (Note 8)	I <sub>R</sub>	—	0.260	—	mA	V <sub>R</sub> = 5V, T <sub>A</sub> = +25°C
		—	—	1.0		V <sub>R</sub> = 30V, T <sub>A</sub> = +25°C
Total Capacitance	C <sub>T</sub>	—	76	—	pF	V <sub>R</sub> = 10V, f = 1.0MHz

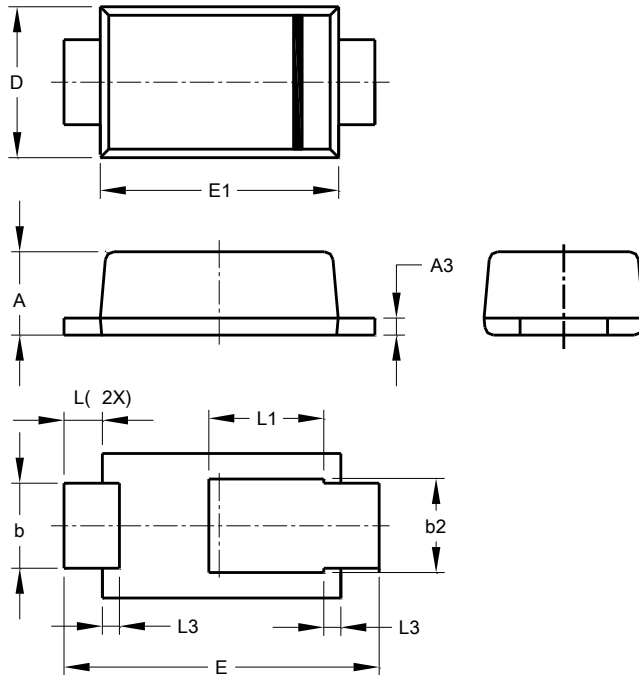
- Notes:
- Part mounted on 2"x2" GETEK board with 1"x1" copper pad, 25% anode, 75% cathode. T<sub>A</sub> = +25°C.
  - Part mounted on FR-4 board with recommended pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  - Theoretical R<sub>θJS</sub> calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
  - Short duration pulse test used to minimize self-heating effect.



## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI123**

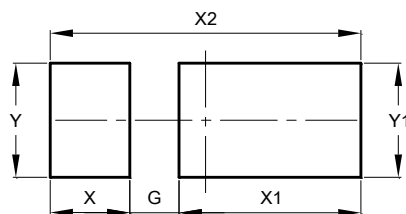


PowerDI123			
Dim	Min	Max	Typ
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI123**



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

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