

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Operating Supply Voltage	V _P - V _N	6.0	V	-
DC Voltage at any Channel Input	-	$(V_N - 0.5)$ to $(V_P + 0.5)$	V	-
Peak Pulse Current	I _{PP}	5	Α	8/20µs, Per Figure 3
ESD Protection – Contact Discharge	V _{ESD_Contact}	±8	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V_{ESD_Air}	±15	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

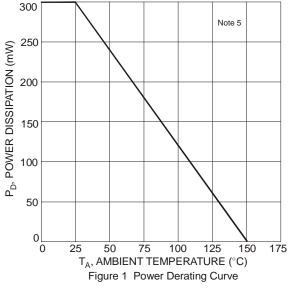
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

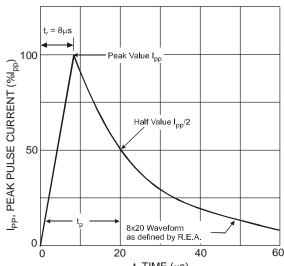
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Operating Supply Voltage	V _P	-	3.3	5.5	V	_
Operating Supply Current (Note 6)	lρ	-	-	8.0	μΑ	$(V_P - V_N) = 3.3V$
Channel Leakage Current (Note 6)	I _R	-	0.1	1.0	μA	$V_P = 5V, V_N = 0V$
Reverse breakdown voltage	V _{BR}	6.0	-	-	V	$I_R = 1 \text{mA}$
Clamping Voltage, Positive Transients	V _{CL1}	-	10.0	-	V	I _{PP} = 1A (Note 7)
Clamping Voltage, Negative Transients	V _{CL2}	-	-1.7	-	V	I _{PP} = -1A (Note 7)
Forward Voltage for Top Diode	V _{FD1}	0.60	0.80	0.95	V	I _F = 8mA, any channel to V _P
Forward Voltage for Bottom Diode	V _{FD2}	0.60	0.80	0.95	V	I _F = 8mA, V _N to and channel
Dynamic Resistance	R _{DYN}	-	0.9	-	Ω	I _{PP} = 1A (Note 7)
Channel Input Capacitance	Ст		0.85	1.2	pF	$V_{IN} = 1.65V, V_P = 3.3V,$ $V_N = 0V, f = 1MHz$

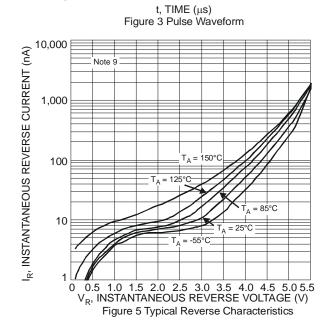
Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an $8x20\mu s$ peak pulse current (I_{pp}) waveform.
- 8. Measured from any channel to V_N.
- 9. Measured from VP to VN.
- 10. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destools/appnote_dnote.html.









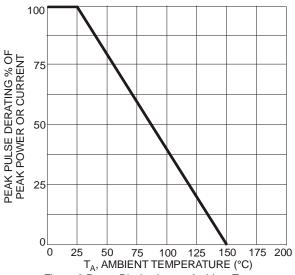
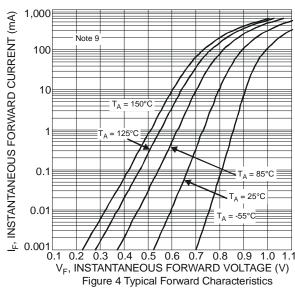


Figure 2 Power Dissipation vs. Ambient Temperature



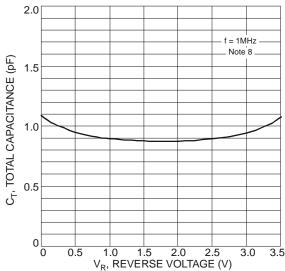
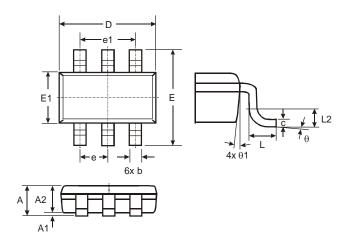


Figure 6 Typical Total Capacitance vs. Reverse Voltage



Package Outline Dimensions

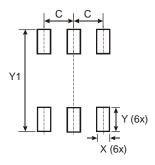
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TSOT26					
Dim	Min	Max	Тур		
Α	-	1.00	_		
A1	0.01	0.10	-		
A2	0.84	0.90	_		
D	_	_	2.90		
Е	_	-	2.80		
E1	_	_	1.60		
b	0.30	0.45	_		
С	0.12	0.20	_		
е	-	_	0.95		
e1	_	-	1.90		
J	0.30	0.50			
L2	-	_	0.25		
θ	0°	8°	4°		
θ1	4°	12°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
Y1	3.199



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