

# CM4732V201R-10

UNCONTROLLED  
DOCUMENT

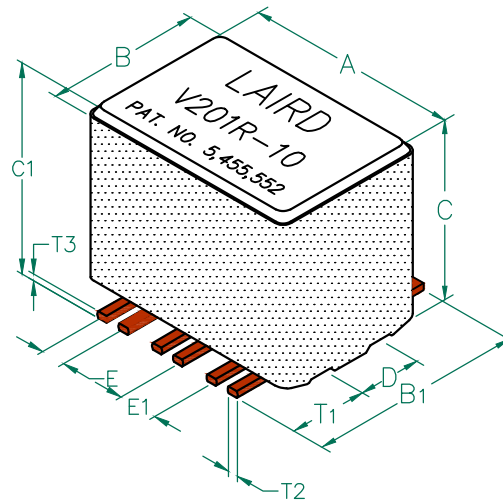
RoHS

## PHYSICAL DIMENSIONS:

A	11.94 [.470]	+	0.18 [.007]
B	8.13 [.320]	+	0.13 [.005]
B <sub>1</sub>	10.92 [.430]		MAX
C	9.45 [.372]	+	0.15 [.006]
C <sub>1</sub>	10.08 [.397]		MAX
D	4.06 [.160]	+	0.05 [.002]
E	1.27 [.050]	+	0.13 [.005]
E <sub>1</sub>	2.03 [.080]	+	0.13 [.005]

## WIRE DIMENSIONS:

T <sub>1</sub>	3.30 [.130]	+	0.38 [.015]
T <sub>2</sub>	0.64 [.025]		TYP
T <sub>3</sub>	0.38 [.015]		TYP



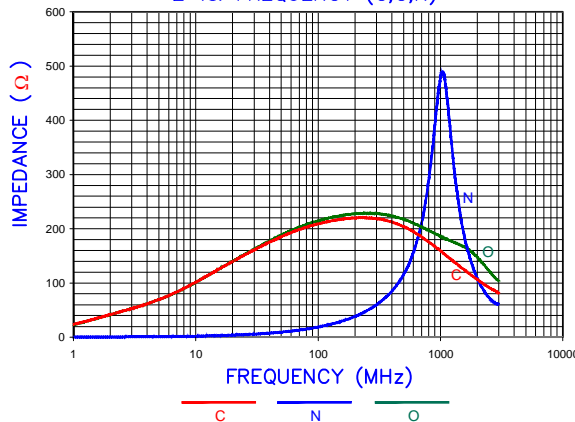
## ELECTRICAL CHARACTERISTICS:

Z @ 100MHz ( $\Omega$ )	DCR ( $\Omega$ )	Rated Current
Nominal	200	
Minimum	150	
Maximum	250	0.01 8,000 mA

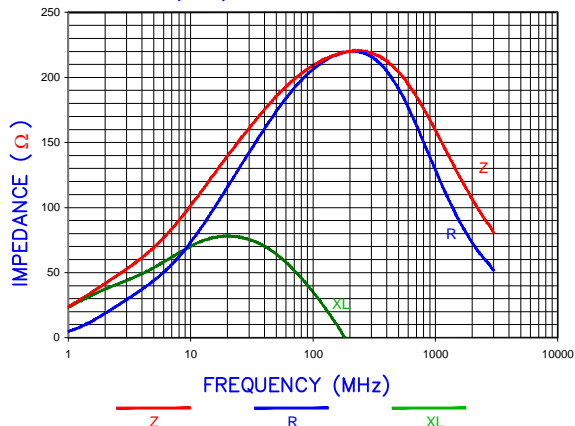
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS  
13" REELS, 400 PCS/REEL.
2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED  
BEFORE SOLDERING.
3. REF. CARRIER TAPE SPECIFICATION #CART4732-23.
4. TERMINATION FINISH IS 100% TIN.
5. THIS PART HAS NO PIN POLARITY.
6. OPERATION TEMPERATURE (INCLUDING SELF-HEATING):  
-40 ~ +125°C.

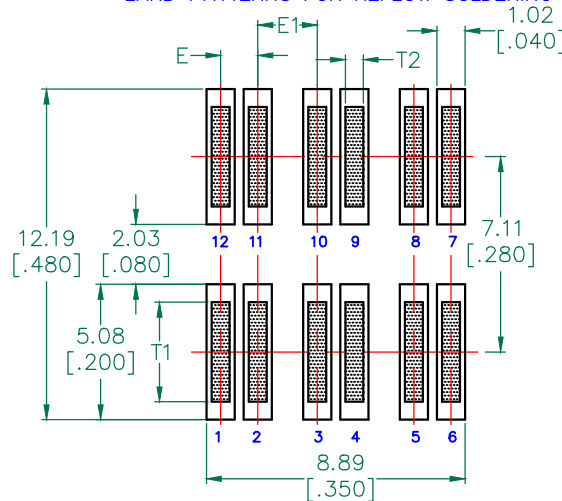
Z vs. FREQUENCY (C,O,N)



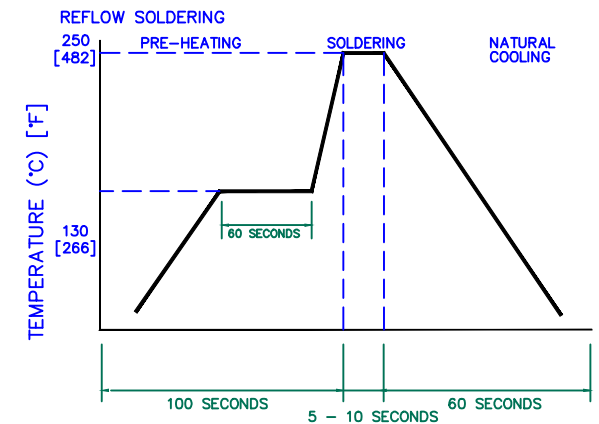
Z, R, XL vs. FREQUENCY



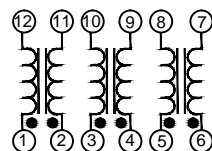
LAND PATTERNS FOR REFLOW SOLDERING



RECOMMENDED SOLDERING CONDITIONS



EQUIVALENT CIRCUIT



DIMENSIONS ARE IN mm (INCHES).

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<b>Laird</b>			
D	ADD NOTE 6	08/30/12	QIU
C	UPDATE COMPANY LOGO & KAPTON KAPTON LABEL ADD EQUIV. CIRCUIT	11/11/08	JRK
B	UPDATE COMPANY LOGO	12/11/07	JRK
A	ORIGINAL DRAFT	06/03/04	JRK
REV	DESCRIPTION	DATE	INT
PROJECT/PART NUMBER: CM4732V201R-10			
DATE: 06/03/04		SCALE: NTS	SHEET: 1 of 2
CAD # CM4732V201R-10-D-1		TOOL # H0470-2	