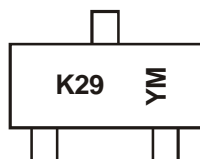


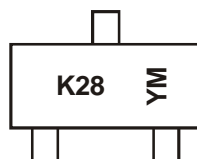
Marking Information

SOT23



K29 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: D = 2016)
 M = Month (ex: 9 = September)

SC59



K28 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: D = 2016)
 M = Month (ex: 9 = September)

Date Code Key

Year	2013	2014	2015	2016	2017	2018	2019
Code	A	B	C	D	E	F	G

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	600	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	50 40	mA
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	70 55	mA
Continuous Drain Current (Note 5) V _{GS} = 5V	Steady State	T _A = +25°C T _A = +70°C	I _D	45 35	mA
Continuous Drain Current (Note 6) V _{GS} = 5V	Steady State	T _A = +25°C T _A = +70°C	I _D	65 50	mA
Pulsed Drain Current @ T _{SP} = +25°C (Note 7)			I _{DM}	0.16	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation, @T _A = +25°C (Note 5)	P _D	0.61	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C (Note 5)	R _{θJA}	204	°C/W
Power Dissipation, @T _A = +25°C (Note 6)	P _D	1.25	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C (Note 6)	R _{θJA}	100	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 - Device mounted on 1" x 1" FR-4 PCB with high coverage 2 oz. Copper, single sided.
 - Repetitive rating, pulse width limited by junction temperature, 10μs pulse, duty cycle = 1%.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	600	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	0.1	μA	V _{DS} = 600V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	3	—	4.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	80	160	Ω	V _{GS} = 10V, I _D = 16mA
		—	95	190		V _{GS} = 5.0V, I _D = 16mA
Forward Transfer Admittance	Y _{fs}	—	76	—	mS	V _{DS} = 10V, I _D = 16mA
Diode Forward Voltage	V _{SD}	—	—	1.5	V	V _{GS} = 0V, I _S = 16mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	21.8	—	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	2.2	—		
Reverse Transfer Capacitance	C _{rss}	—	0.3	—		
Total Gate Charge	Q _g	—	1.08	—	nC	V _{GS} = 10V, V _{DD} = 300V, I _D = 0.01A
Gate-Source Charge	Q _{gs}	—	0.08	—		
Gate-Drain Charge	Q _{gd}	—	0.50	—		
Turn-On Delay Time	t _{D(ON)}	—	5.0	—	ns	V _{DD} = 300V, V _{GS} = 10V, R _{GEN} = 6Ω, I _D = 10mA
Turn-On Rise Time	t _R	—	7.2	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	28.7	—	ns	
Turn-Off Fall Time	t _F	—	168	—	ns	V _R = 300V, I _F = 0.016A, di/dt = 100A/μs
Reverse Recovery Time	t _{RR}	—	131	—	ns	
Reverse Recovery Charge	Q _{RR}	—	32	—	nC	

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to production testing.

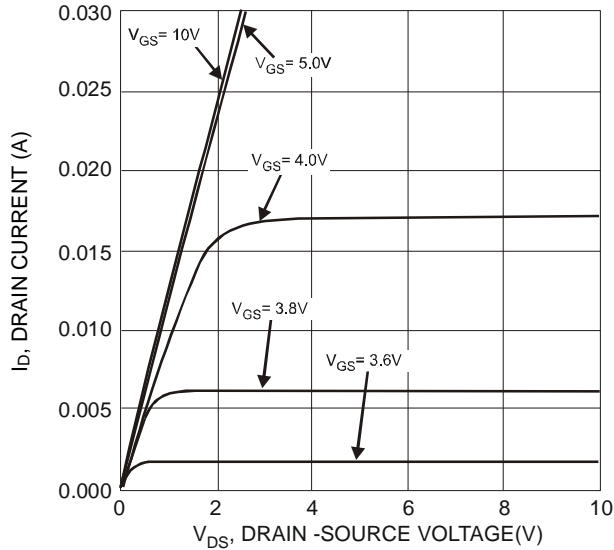


Figure 1 Typical Output Characteristics

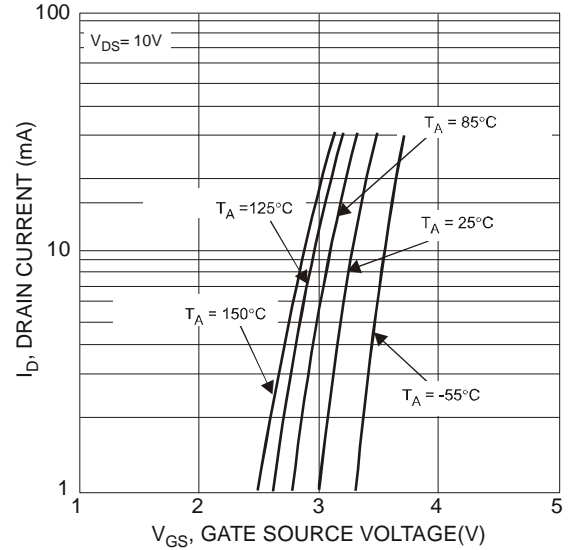


Figure 2 Typical Transfer Characteristics

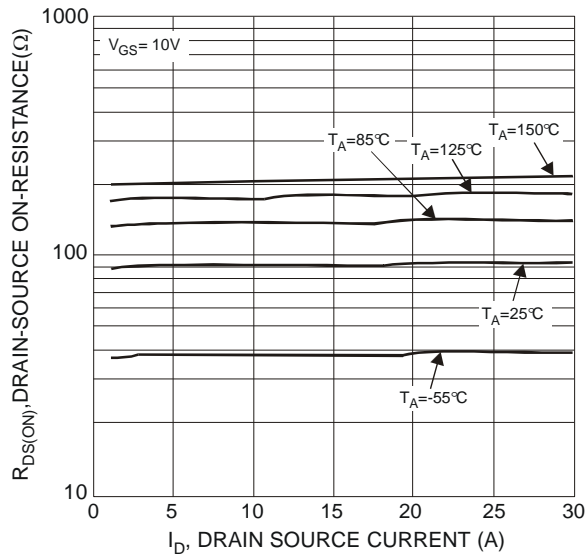


Figure 3 Typical On-Resistance vs. Drain Current and Temperature

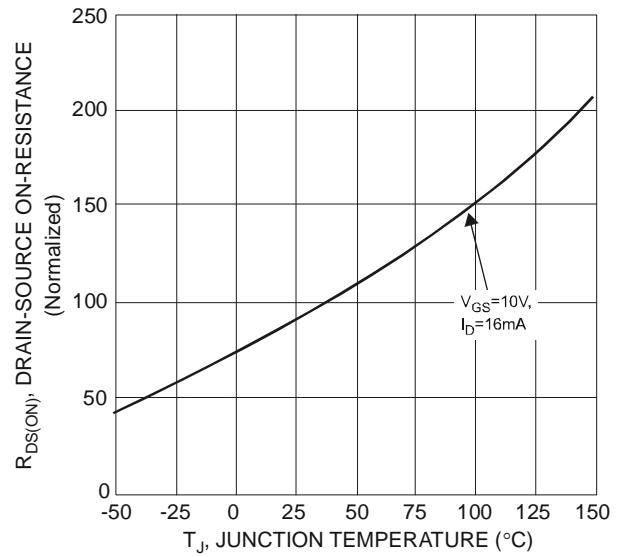


Figure 4 On-Resistance Variation with Temperature

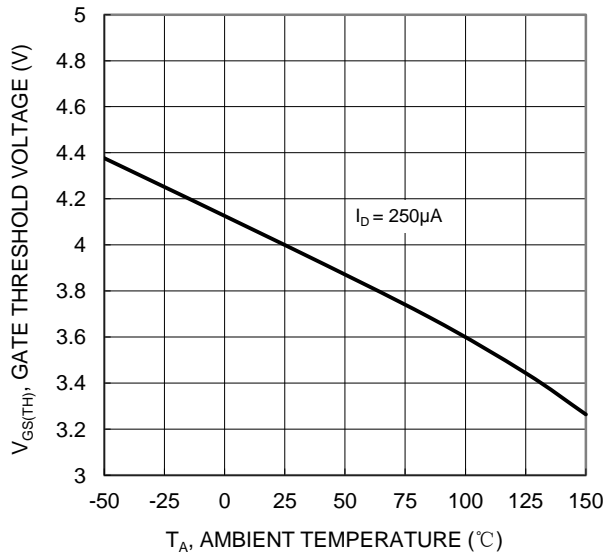


Figure 5. Gate Threshold Variation vs. Ambient Temperature

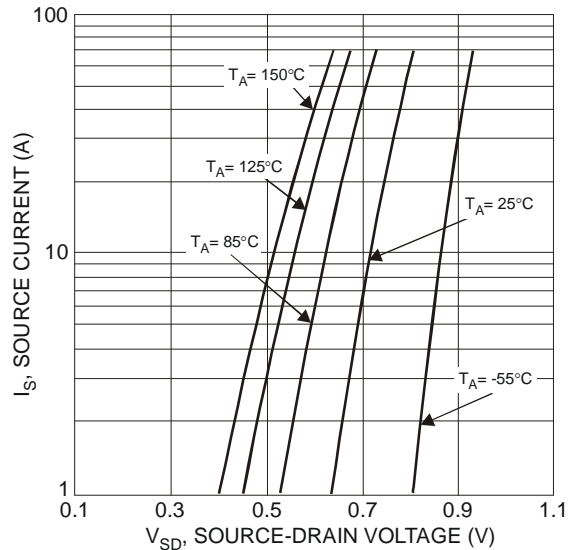


Figure 6 Diode Forward Voltage vs. Current

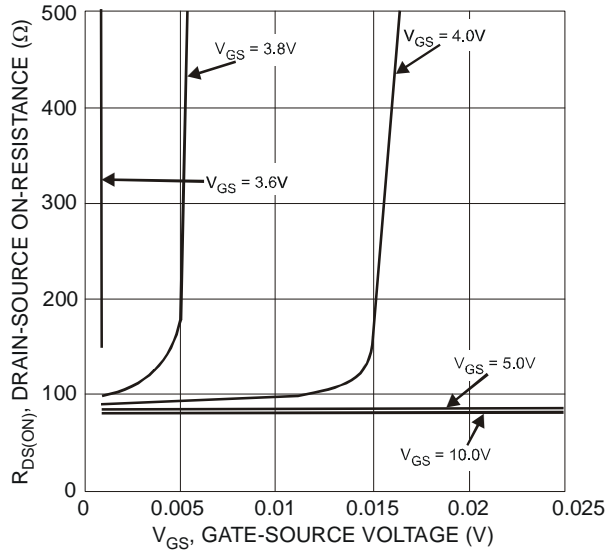


Figure 7 Typical On-Resistance vs. Drain Current and Gate Voltage

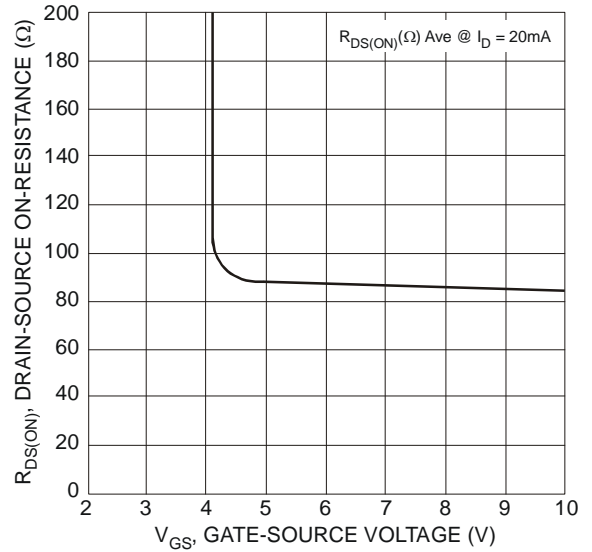


Figure 8 Typical Transfer Characteristic

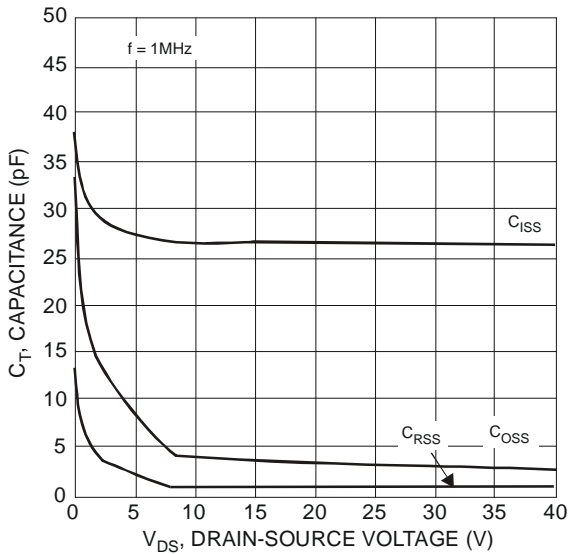


Figure 9 Typical Junction Capacitance

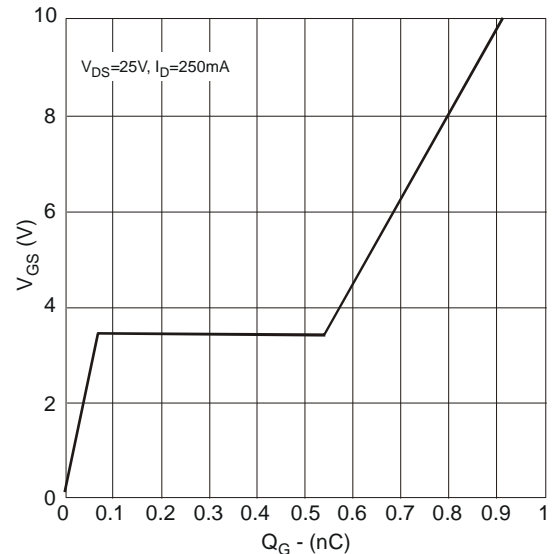


Figure 10 Gate Charge Characteristics

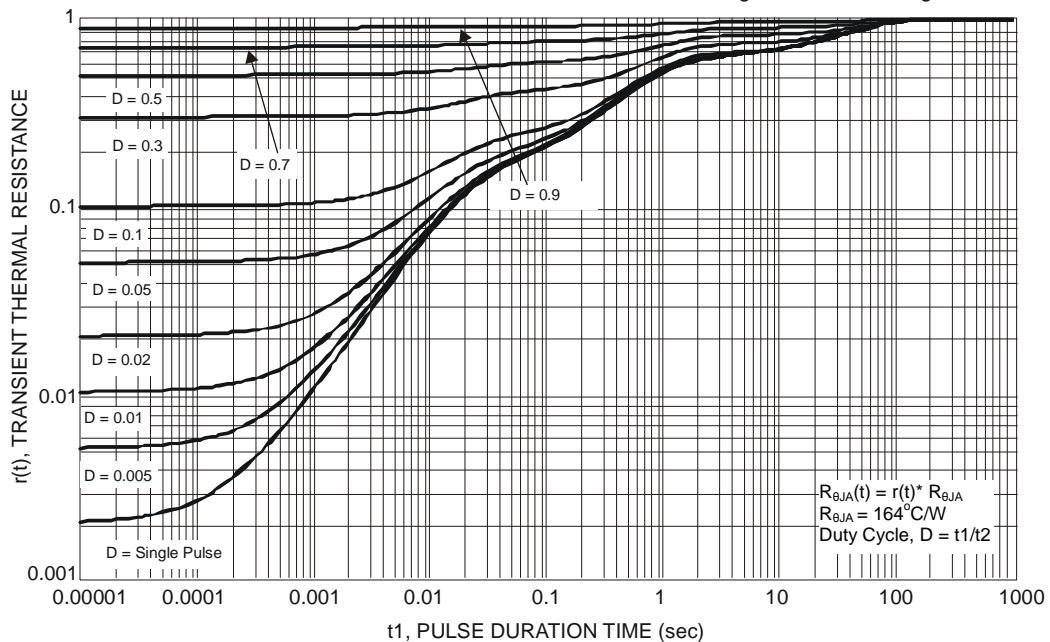
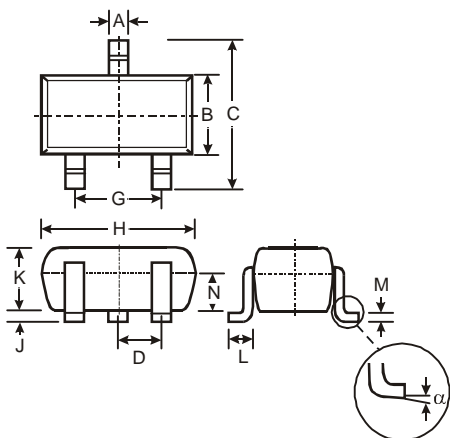


Figure 11 Transient Thermal Resistance

Package Outline Dimensions

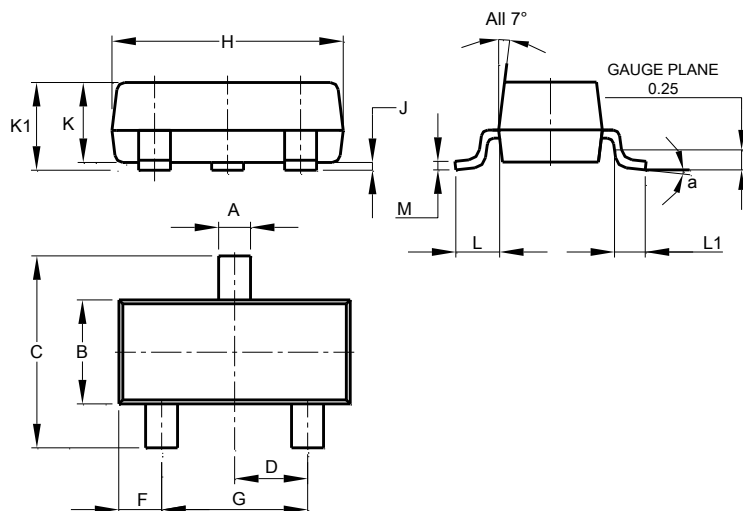
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SC59



SC59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

SOT23

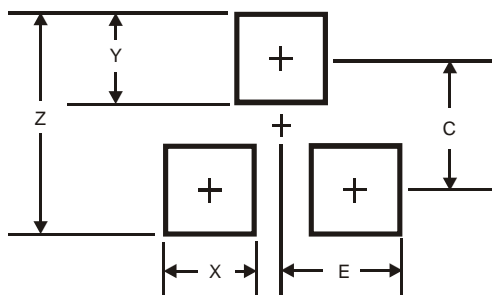


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SC59

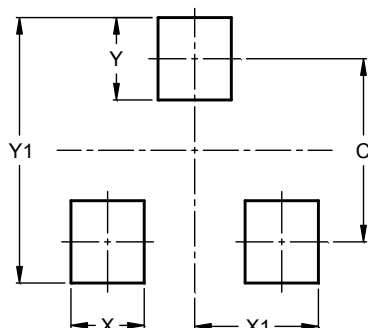


Dimensions	Value (in mm)
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

Suggested Pad Layout (cont.)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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