

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V _{RM}	100	V
Peak Repetitive Reverse Voltage	V _{RRM}	75	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	53	V
Forward Continuous Current (Note 6)	I _{FM}	215	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0μs	2.0	A
	@ t = 1.0ms	1.0	
	@ t = 1.0s	0.5	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	200	mW
Power Dissipation (Note 7)	P _D	300	mW
Thermal Resistance Junction to Ambient Air (Note 6)	R _{θJA}	625	°C/W
Thermal Resistance Junction to Ambient Air (Note 7)	R _{θJA}	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V _{(BR)R}	75	—	V	I _R = 2.5μA
Forward Voltage	V _F	—	0.715	V	I _F = 1.0mA
		—	0.855		I _F = 10mA
		—	1.0		I _F = 50mA
		—	1.25		I _F = 150mA
		—	2.5		V _R = 75V
Reverse Current (Note 8)	I _R	—	50	μA	V _R = 75V, T _J = +150°C
		—	30	μA	V _R = 25V, T _J = +150°C
		—	25	nA	V _R = 20V
		—	—	—	—
Total Capacitance	C _T	—	2.0	pF	V _R = 0, f = 1.0MHz
Reverse Recovery Time	t _{rr}	—	4.0	ns	I _F = I _R = 10mA, I _{rr} = 0.1 x I _R , R _L = 100Ω

- Notes:
- Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com>.
 - Device mounted on Alumina PCB, 0.4 inch x 0.3 inch x 0.024 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com>.
 - Short duration pulse test used to minimize self-heating effect.

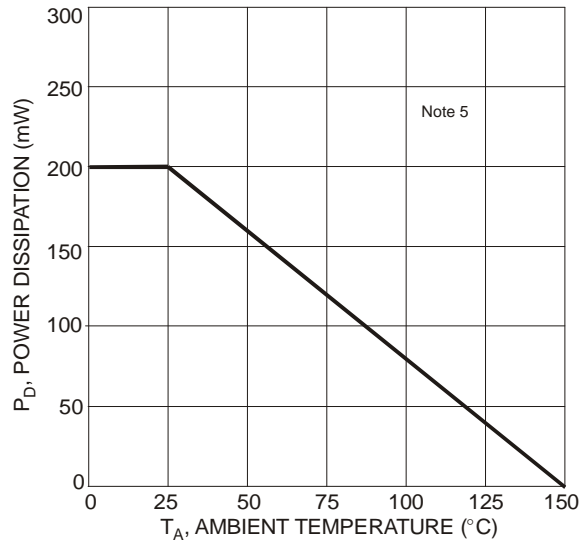


Fig. 1 Power Derating Curve, Total Package

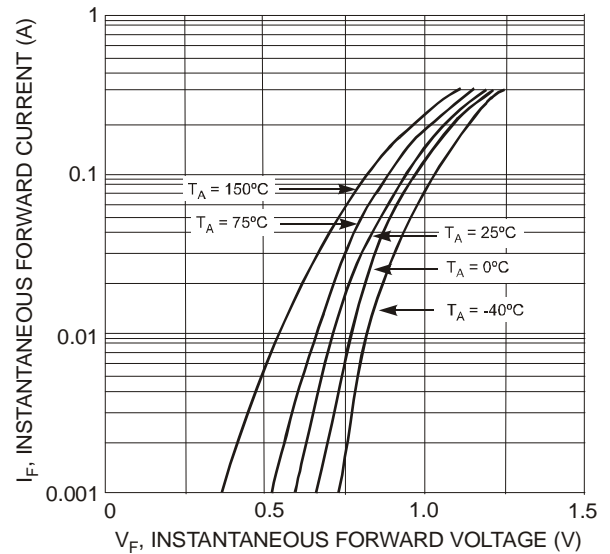


Fig. 2 Typical Forward Characteristics, Per Element

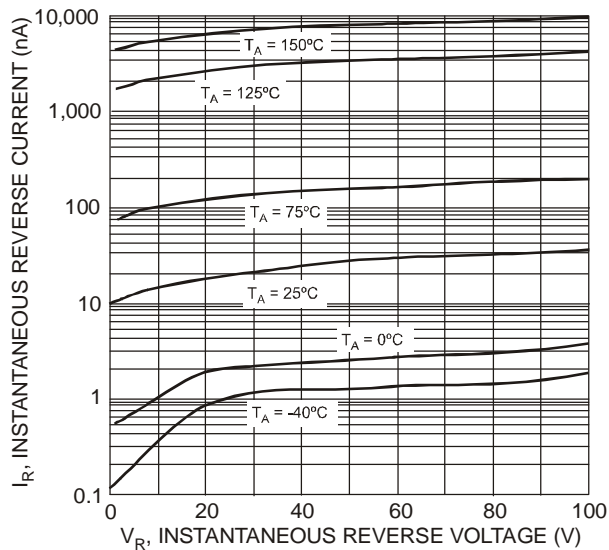


Fig. 3 Typical Reverse Characteristics, Per Element

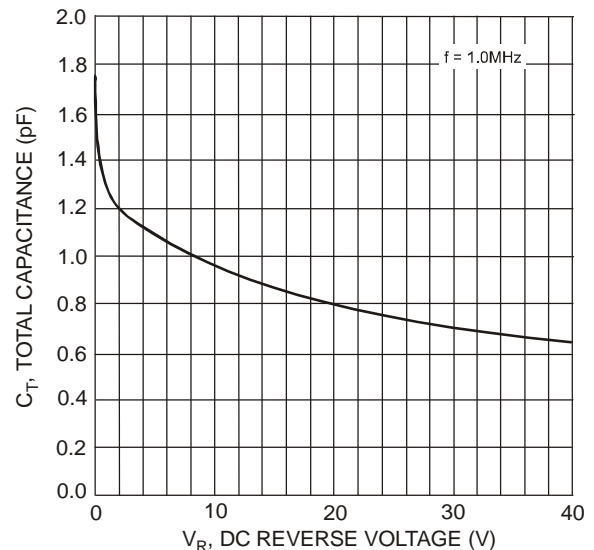
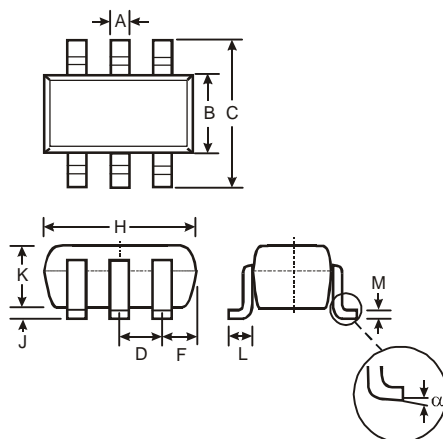


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Package Outline Dimensions

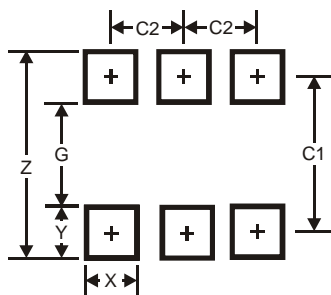
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT363		
Dim	Min	Max
A	0.10	0.30
B	1.15	1.35
C	2.00	2.20
D	0.65 Typ	
F	0.40	0.45
H	1.80	2.20
J	0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.22
α	0°	8°
All Dimensions in mm		

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

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