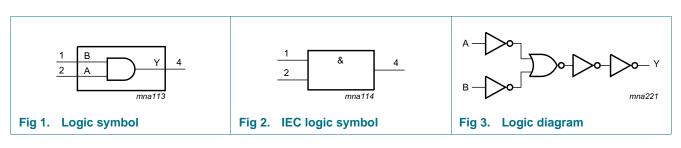
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### 4. Marking

Table 2.   Marking codes	
Type number	Marking <sup>[1]</sup>
74AHC1G08GW	AE
74AHC1G08GV	A08
74AHCT1G08GW	CE
74AHCT1G08GV	C08

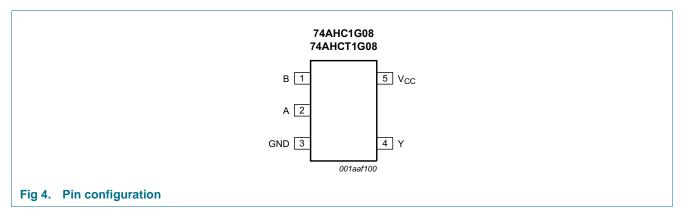
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 5. Functional diagram



## 6. Pinning information

### 6.1 Pinning



### 6.2 Pin description

Table 3. Pin description							
Symbol	Pin	Description					
В	1	data input					
A	2	data input					
GND	3	ground (0 V)					
Y	4	data output					
V <sub>CC</sub>	5	supply voltage					

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## 7. Functional description

#### Table 4. Function table

*H* = *HIGH* voltage level; *L* = *LOW* voltage level

Inputs	Output	
Α	В	Y
L	L	L
L	Н	L
н	L	L
Н	Н	Н

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
VI	input voltage			-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < -0.5 V		-20	-	mA
I <sub>OK</sub>	output clamping current	$V_{O}$ < -0.5 V or $V_{O}$ > $V_{CC}$ + 0.5 V	<u>[1]</u>	-	±20	mA
I <sub>O</sub>	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$		-	±25	mA
I <sub>CC</sub>	supply current			-	75	mA
I <sub>GND</sub>	ground current			-75	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to +125 \ ^{\circ}C$	[2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For both TSSOP5 and SC-74A packages: above 87.5 °C the value of Ptot derates linearly with 4.0 mW/K.

## 9. Recommended operating conditions

### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74	AHC1G	08	74AHCT1G08			Unit
			Min	Тур	Max	Min	Тур	Max	
V <sub>CC</sub>	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	-40	+25	+125	°C
$\Delta t / \Delta V$	input transition rise	$V_{CC}$ = 3.3 V $\pm$ 0.3 V	-	-	100	-	-	-	ns/V
	and fall rate	$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	-	20	-	-	20	ns/V

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## **10. Static characteristics**

#### Table 7.Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Мах	
For type	74AHC1G08	1				1		1	I	
VIH	HIGH-level	V <sub>CC</sub> = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
input voltage	V <sub>CC</sub> = 3.0 V	2.1	-	-	2.1	-	2.1	-	V	
	V <sub>CC</sub> = 5.5 V	3.85	-	-	3.85	-	3.85	-	V	
VIL	LOW-level	V <sub>CC</sub> = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V <sub>CC</sub> = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V <sub>CC</sub> = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	$I_{O} = -50 \ \mu A; \ V_{CC} = 2.0 \ V$	1.9	2.0	-	1.9	-	1.9	-	V
		$I_{O} = -50 \ \mu A; \ V_{CC} = 3.0 \ V$	2.9	3.0	-	2.9	-	2.9	-	V
		$I_{O} = -50 \ \mu A; \ V_{CC} = 4.5 \ V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.40	-	V
		$I_{O} = -8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.8	-	3.70	-	V
V <sub>OL</sub> LOW-level	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 50 \ \mu A; \ V_{CC} = 3.0 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 50 \ \mu A; \ V_{CC} = 4.5 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		$I_{O} = 8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
I	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	1.0	-	10	-	40	μA
Cı	input capacitance		-	1.5	10	-	10	-	10	pF
For type	74AHCT1G08	1				1			1	
V <sub>IH</sub>	HIGH-level input voltage	$V_{CC}$ = 4.5 V to 5.5 V	2.0	-	-	2.0	-	2.0	-	V
V <sub>IL</sub>	LOW-level input voltage	$V_{CC}$ = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -8.0 mA	3.94	-	-	3.8	-	3.70	-	V
V <sub>OL</sub>	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
lı	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ

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### Table 7. Static characteristics ...continued

Voltages are referenced to GND (ground = 0 V).

Symbol Parameter		Conditions	25 °C		–40 °C to +85 °C		–40 °C to +125 °C		Unit	
			Min	Тур	Мах	Min	Max	Min	Max	
I <sub>CC</sub>		$V_{I} = V_{CC} \text{ or GND; } I_{O} = 0 \text{ A;}$ $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Δl <sub>CC</sub>	additional supply current	per input pin; V <sub>I</sub> = 3.4 V; other inputs at V <sub>CC</sub> or GND; $I_O = 0 A$ ; V <sub>CC</sub> = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
CI	input capacitance		-	1.5	10	-	10	-	10	pF

### **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

GND = 0 V;  $t_f = t_f = \le 3.0$  ns. For test circuit see <u>Figure 6</u>.

Symbol	Parameter	Conditions			25 °C		–40 °C to +85 °C		–40 °C to +125 °C		Unit
				Min	Тур	Max	Min	Max	Min	Max	
For type	74AHC1G08	1						1			
t <sub>pd</sub>	propagation	A and B to Y; see Figure 5	<u>[1]</u>								
	delay	$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	[2]								
		C <sub>L</sub> = 15 pF		-	4.6	8.8	1.0	10.5	1.0	12.0	ns
		C <sub>L</sub> = 50 pF		-	6.5	12.3	1.0	14.0	1.0	16.0	ns
		$V_{CC}$ = 4.5 V to 5.5 V	<u>[3]</u>								
		C <sub>L</sub> = 15 pF		-	3.2	5.9	1.0	7.0	1.0	8.0	ns
		C <sub>L</sub> = 50 pF		-	4.6	7.9	1.0	9.0	1.0	10.5	ns
C <sub>PD</sub>	power dissipation capacitance	per buffer; $C_L = 50 \text{ pF}$ ; $f = 1$ MHz; $V_I = \text{GND}$ to $V_{CC}$	<u>[4]</u>	-	17	-	-	-	-	-	pF
For type	74AHCT1G0	8									
t <sub>pd</sub>	propagation	A and B to Y; see Figure 5	<u>[1]</u>								
	delay	$V_{CC}$ = 4.5 V to 5.5 V	<u>[3]</u>								
		C <sub>L</sub> = 15 pF		-	3.6	6.2	1.0	7.1	1.0	8.0	ns
		C <sub>L</sub> = 50 pF		-	5.1	7.9	1.0	9.0	1.0	10.5	ns
C <sub>PD</sub>	power dissipation capacitance	per buffer; $C_L = 50 \text{ pF}$ ; f = 1 MHz; $V_I = \text{GND}$ to $V_{CC}$	<u>[4]</u>	-	19	-	-	-	-	-	pF

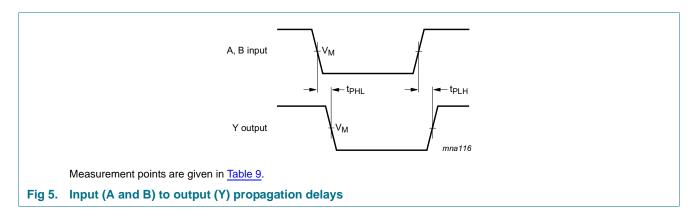
[1]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

- [2] Typical values are measured at V<sub>CC</sub> = 3.3 V.
- [3] Typical values are measured at  $V_{CC}$  = 5.0 V.

[4]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D (\mu W)$ .  $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:  $f_i = \text{input frequency in MHz}; f_o = \text{output frequency in MHz};$   $C_L = \text{output load capacitance in pF};$  $V_{CC} = \text{supply voltage in Volts}$ 

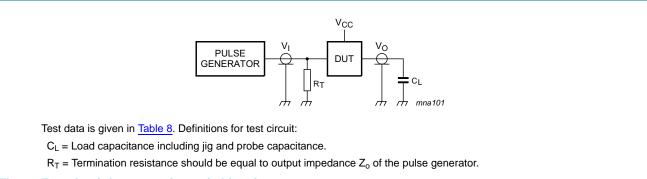
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## 12. Waveforms



#### Table 9.Measurement point

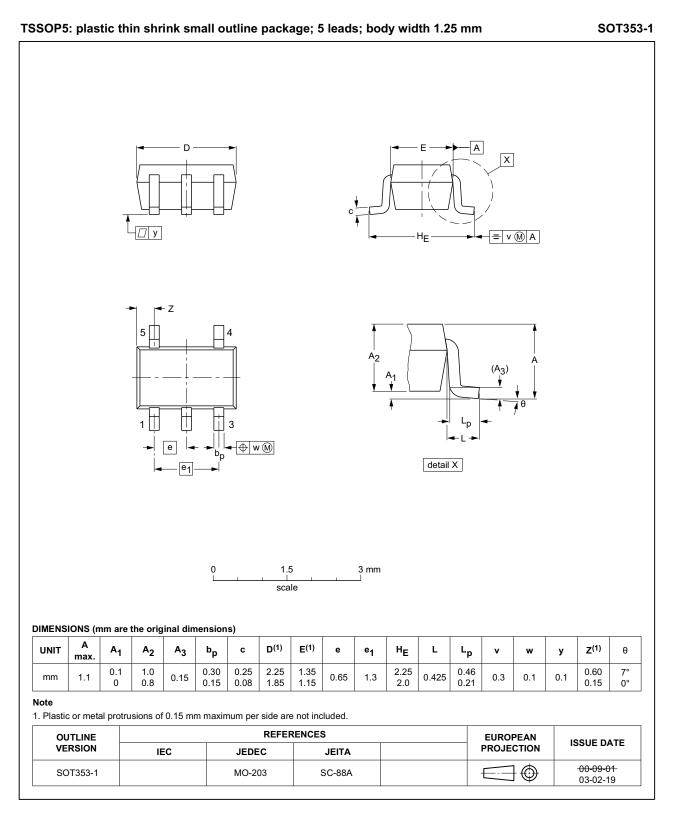
Туре	Input	Output	
	VI	V <sub>M</sub>	V <sub>M</sub>
74AHC1G08	GND to V <sub>CC</sub>	$0.5  imes V_{CC}$	$0.5  imes V_{CC}$
74AHCT1G08	GND to 3.0 V	1.5 V	$0.5 \times V_{CC}$



#### Fig 6. Test circuit for measuring switching times

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## 13. Package outline

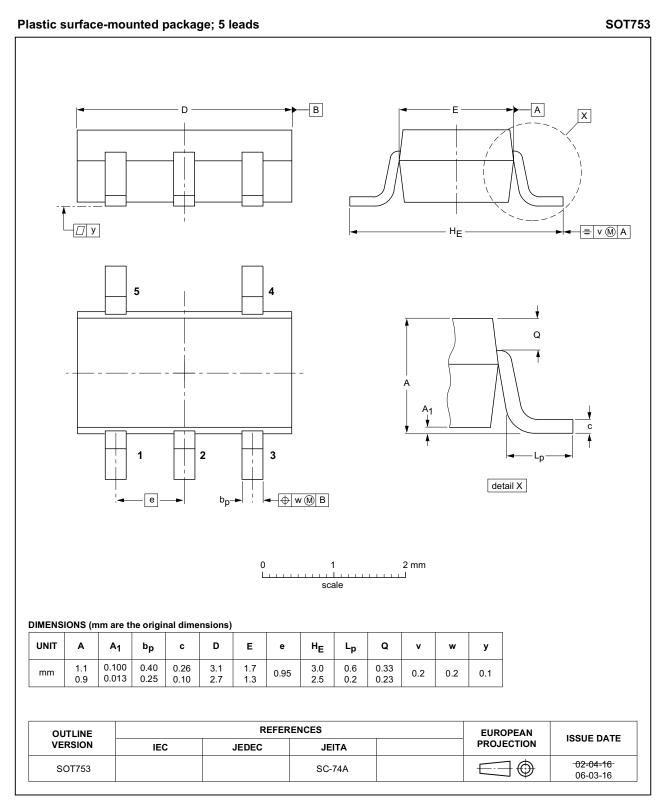


### Fig 7. Package outline SOT353-1 (TSSOP5)

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#### Fig 8. Package outline SOT753 (SC-74A)

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## 14. Abbreviations

Table 10. Abbreviations							
Acronym	Description						
CDM	Charged Device Model						
DUT	Device Under Test						
ESD	ElectroStatic Discharge						
НВМ	Human Body Model						
MM	Machine Model						
TTL	Transistor-Transistor Logic						

## 15. Revision history

#### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes					
74AHC_AHCT1G08 v.7	20141118	Product data sheet	-	74AHC_AHCT1G08 v.6					
Modifications:	• Section 4: t	ion <u>4</u> : table note added.							
74AHC_AHCT1G08 v.6	20070629	Product data sheet	-	74AHC_AHCT1G08 v.5					
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> </ul>								
	<ul> <li>Legal texts</li> </ul>	have been adapted to the new	company name wh	ere appropriate.					
	Package S	OT353 changed to SOT353-1 i	n Section 3 and Sec	<u>ction 13</u> .					
	Quick reference	ence data and Soldering section	ons removed.						
74AHC_AHCT1G08 v.5	20020606	Product specification	-	74AHC_AHCT1G08 v.4					
74AHC_AHCT1G08 v.4	20020221	Product specification	-	74AHC_AHCT1G08 v.3					
74AHC_AHCT1G08 v.3	20010209	Product specification	-	74AHC_AHCT1G08 v.2					
74AHC_AHCT1G08 v.2	19990127	Product specification	-	74AHC_AHCT1G08_N v.1					
74AHC_AHCT1G08_N v.1	19981125	Preliminary specification	-	-					

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Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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