MICROCHIP

USX2064 ☆

Sampling Options

Status: In Production

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Features:

- High performance, ultra low-power, small footprint hub controller IC with 4 downstream ports
- Fully compliant with the USB 2.0 specification
- Enhanced OEM configuration options available through either a single serial I2C® EEPROM, or SMBus slave port
- MultiTRAK - High-performance multiple transaction translator which provides one transaction translator per port
- PortMap - Flexible port mapping and disable sequencing
- PortSwap - Programmable USB differential-pair pin locations ease PCB design by aligning USB signal lines directly to connectors

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Device Overview

Summary

Microchip's USX2064 is part of a family of versatile, cost-effective, and power-efficient USB 2.0 hub controllers. Leveraging Microchip's innovative MultiTRAK™ technology that delivers industry-leading data throughput in mixed-speed USB environments, the USX2064 is a USB port expansion solution for applications that demand ultra low power and a small footprint without compromising on performance.

Well-suited for consumer and mobile applications, all members of the USX2064 is available in a space-saving package. The common 36-pin package shared among the 2/3/4 port hub controllers measures only 6x6 mm and provides an ultra small footprint for space-constrained designs while allowing scalable port expansion from two to four ports.

Over 30 programmable features including Microchip's unique PortMap, PortSwap, and PHYBoost are designed to aid system designers in simplifying PCB layout and optimizing bill-of-material cost. Every downstream port of the USX2064 hub can be enabled to support USB Battery Charging 1.1 specification as a Charging Downstream Port (CDP). A CDP provides universal battery charging capability to a compliant mobile phone or portable electronic device using a standard USB port.

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Parametrics

Value
USB 2.0
USB 2.0
4
I2C
3.3
Yes
Yes
Yes
Yes
70