

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-12	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current V _{GS} = -4.5V (Note 10)	Steady State	$T_C = +25$ °C $T_C = +70$ °C	l _D	-20 -16	А
	t<5s (Note 6)	$T_A = +25$ °C	I _D	-12.6	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-55	Α
Maximum Body Diode Continuous Current (Note 6)			Is	-2.8	Α
Avalanche Current (Note 7), L = 0.1mH			I _{AS}	-21	Α
Avalanche Energy (Note 7,) L = 0.1mH			E _{AS}	22	mJ

Thermal Characteristics

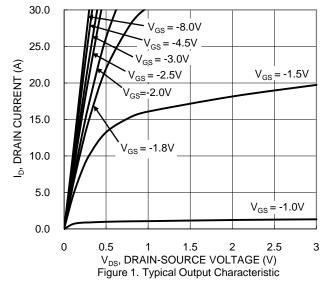
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	D	0.72	W
	$T_A = +70$ °C	P_{D}	0.46	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	р	172	°C/W
	t<5s	$R_{\theta JA}$	130	
Total Power Dissipation (Note 6)	$T_A = +25$ °C	P_{D}	2.11	W
	$T_A = +70^{\circ}C$	PD	1.36	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	р	59	°C/W
Thermal Resistance, Junction to Ambient (Note o)	t<5s	$R_{\theta JA}$	44	
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{ heta JC}$	9.0	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-12	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		I	-1	μA	$V_{DS} = -10V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	I	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-0.3	-0.5	-0.9	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			11	15	- mΩ	$V_{GS} = -4.5V$, $I_D = -5A$	
Static Drain-Source On-Resistance	D		12	20		$V_{GS} = -3.7V$, $I_{D} = -5A$	
Static Dialif-Source Off-Nesistance	R _{DS(ON)}		15	30		$V_{GS} = -2.5V$, $I_{D} = -4A$	
			20	40		$V_{GS} = -1.8V, I_{D} = -1A$	
Diode Forward Voltage	V _{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -10A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	1344	_		VDS = -10V, VGS = 0V, f = 1.0MHz	
Output Capacitance	Coss		342		pF		
Reverse Transfer Capacitance	C _{rss}	_	297	_			
Gate Resistance	R_g	_	15	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	19.5	_			
Total Gate Charge (V _{GS} = -8V)	Qg	_	31		nC	$V_{DS} = -6V, I_{D} = -10A$	
Gate-Source Charge	Q _{gs}	_	2.1	_	IIC		
Gate-Drain Charge	Q _{gd}	_	7.9	_			
Turn-On Delay Time	t _{D(ON)}	_	6.0	_		$V_{DS} = -6V, V_{GS} = -4.5V,$ $R_g = 1\Omega, I_D = -8A$	
Turn-On Rise Time	t _R	_	32	_			
Turn-Off Delay Time	t _{D(OFF)}	_	71	_	ns		
Turn-Off Fall Time	t _F	_	85	_			
Reverse Recovery Time	t _{RR}	_	46	_	ns	1 400 11/14 5000/	
Reverse Recovery Charge	Q _{RR}	_	44		nC	I _F = -12A, di/dt = 500A/μs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.
 Package limited. Notes:





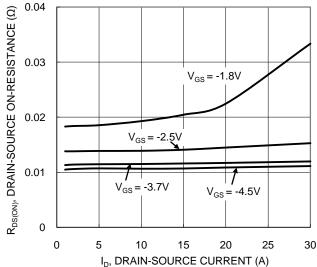


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

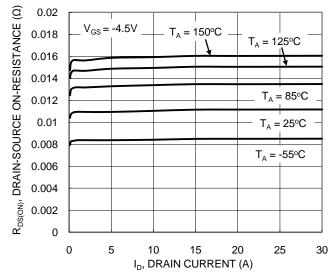


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

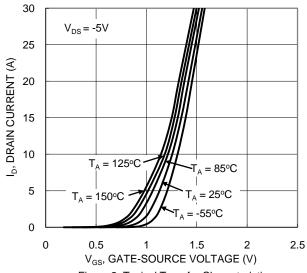
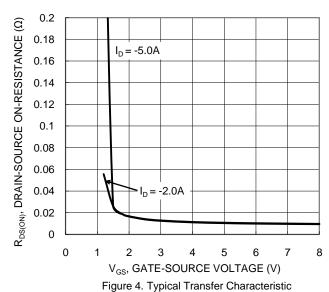


Figure 2. Typical Transfer Characteristic



1.5 $V_{GS} = -4.5V, I_{D} = -5A$ R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) 1.2 $V_{GS} = -2.5V, I_{D} = -4A$ 0.9 0.6 -50 0 25 50 75 100 125 150 T_J , JUNCTION TEMPERATURE ($^{\circ}$ C)

Figure 6. On-Resistance Variation with Temperature



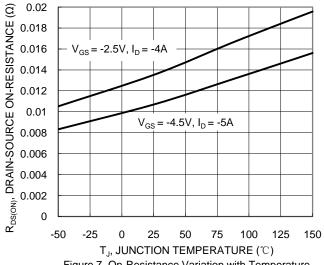


Figure 7. On-Resistance Variation with Temperature

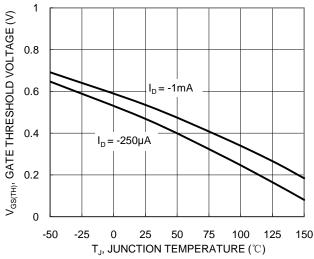
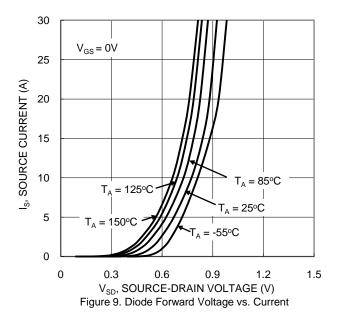


Figure 8. Gate Threshold Variation vs. Junciton Temperature

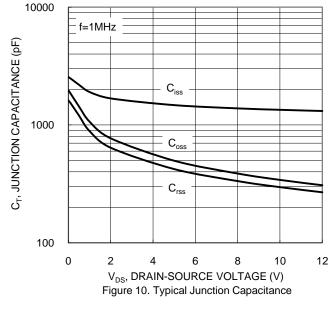


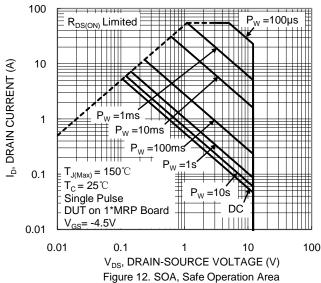
8 6 $V_{GS}(V)$ 4 $V_{DS} = -6V, I_{D} = -10A$ 2 0

10

20

 Q_g (nC) Figure 11. Gate Charge





30

0



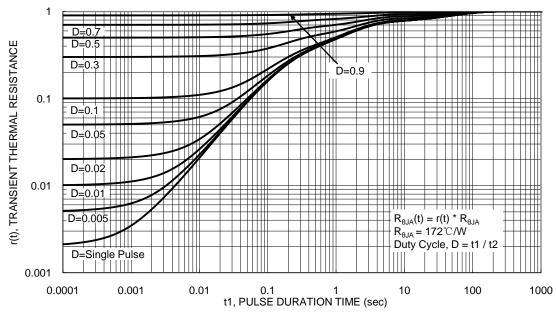


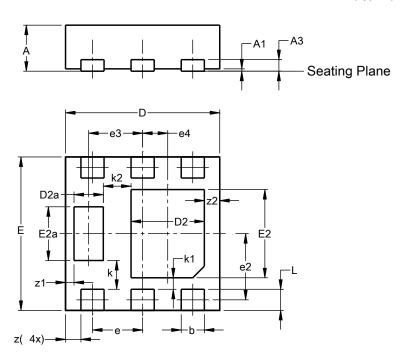
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

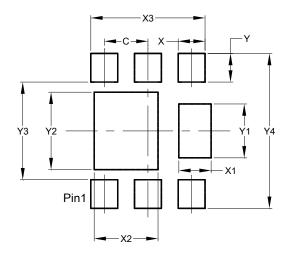


U-DFN2020-6					
(Type F)					
Dim	Min Max Typ				
Α	0.57 0.63		0.60		
A 1	0.00	0.00 0.05			
A3	-	-	0.15		
b	0.25	0.35	0.30		
D	1.95	2.05	2.00		
D2	0.85	1.05	0.95		
D2a	0.33	0.43	0.38		
Е	1.95	2.05	2.00		
E2	1.05	1.25	1.15		
E2a	0.65	0.75	0.70		
е	0.65 BSC				
e2	0.863 BSC				
е3	0.70 BSC				
e4	0.325 BSC				
k	0.37 BSC				
k1	0.15 BSC				
k2	0.36 BSC				
L		0.325			
Z	0.20 BSC				
z1	0.110 BSC				
z2	0.20 BSC				
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value				
Dillielisions	(in mm)				
С	0.650				
Х	0.400				
X1	0.480				
X2	0.950				
Х3	1.700				
Υ	0.425				
Y1	0.800				
Y2	1.150				
Y3	1.450				
Y4	2.300				



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