

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	330	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ extsf{ heta}JA}$	379	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Maximum Ratings N-CHANNEL – Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±6	V
Continuous Drain Current (Note 6)	Steady State	T _A = +25°C T _A = +85°C	ID	1066 690	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	3.2	А	

Maximum Ratings P-CHANNEL – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±6	V
Continuous Drain Current (Note 6)	Steady State	T _A = +25°C T _A = +85°C	ID	-845 -548	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-2.2	A

Electrical Characteristics N-CHANNEL – Q1 (@T_A = +25°C, unless otherwise specified.)

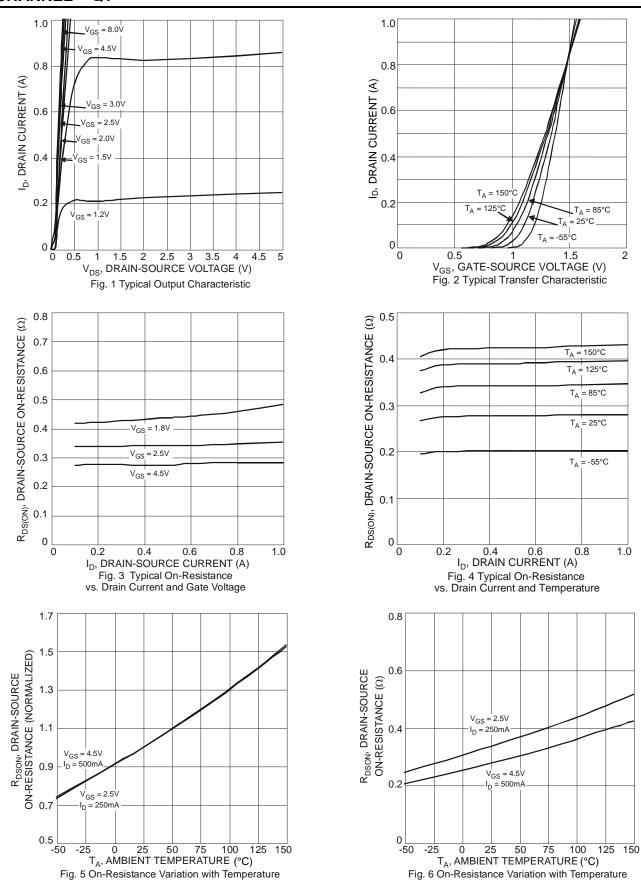
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	_	V	V _{GS} = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	l _{DSS}	_	—	100	nA	V_{DS} =20V, V_{GS} = 0V	
Gate-Source Leakage	IGSS		—	±1.0	μA	$V_{GS} = \pm 4.5 V$, $V_{DS} = 0 V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
			0.3	0.45		$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	—	0.4	0.6	Ω	$V_{GS} = 2.5V, I_D = 500mA$	
			0.5	0.75		V _{GS} = 1.8V, I _D = 350mA	
Forward Transfer Admittance	Y _{fs}	-	1.4	_	S	V _{DS} = 10V, I _D = 400mA	
Diode Forward Voltage (Note 7)	V _{SD}	-	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 8)					•		
Input Capacitance	Ciss	_	60.67	_	pF		
Output Capacitance	Coss	-	9.68	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		5.37	_	pF		
Total Gate Charge	Qg		736.6	—	nC		
Gate-Source Charge	Q _{gs}		93.6	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $-I_D = 250mA$	
Gate-Drain Charge	Q _{gd}		116.6	—	nC		
Turn-On Delay Time	t _{D(ON)}		5.1	—	ns		
Turn-On Rise Time	t _R		7.4	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	-	26.7	—	ns	$R_L = 47\Omega, R_G = 10\Omega$	
Turn-Off Fall Time	t _F		12.3	—	ns		

Notes: 6. Device mounted on FR-4 PCB with minimum recommended pad layout. 7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.



N-CHANNEL - Q1



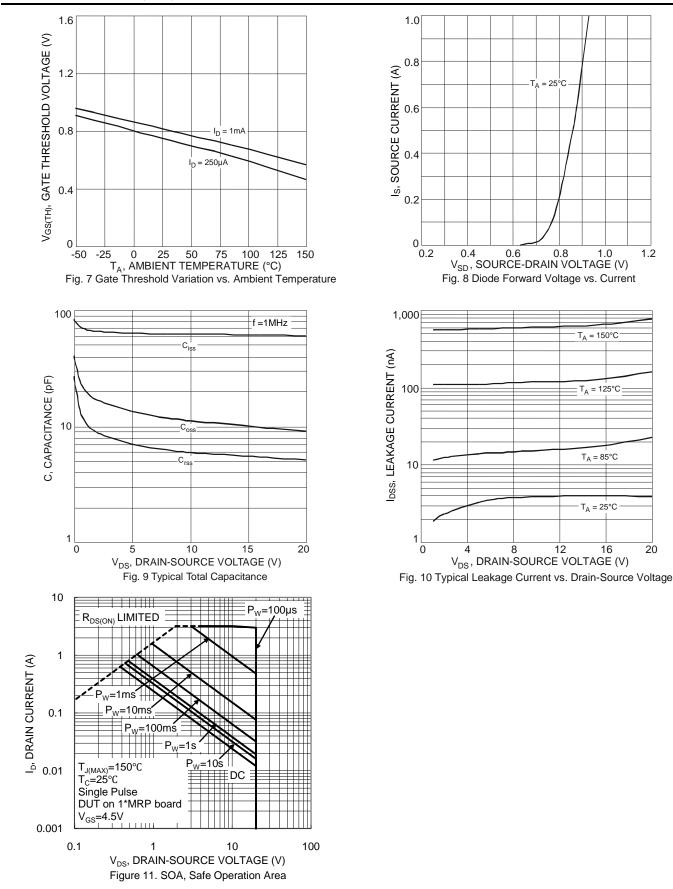
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N-CHANNEL – Q1 (Cont.)

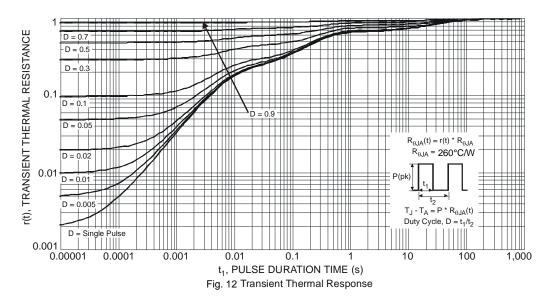
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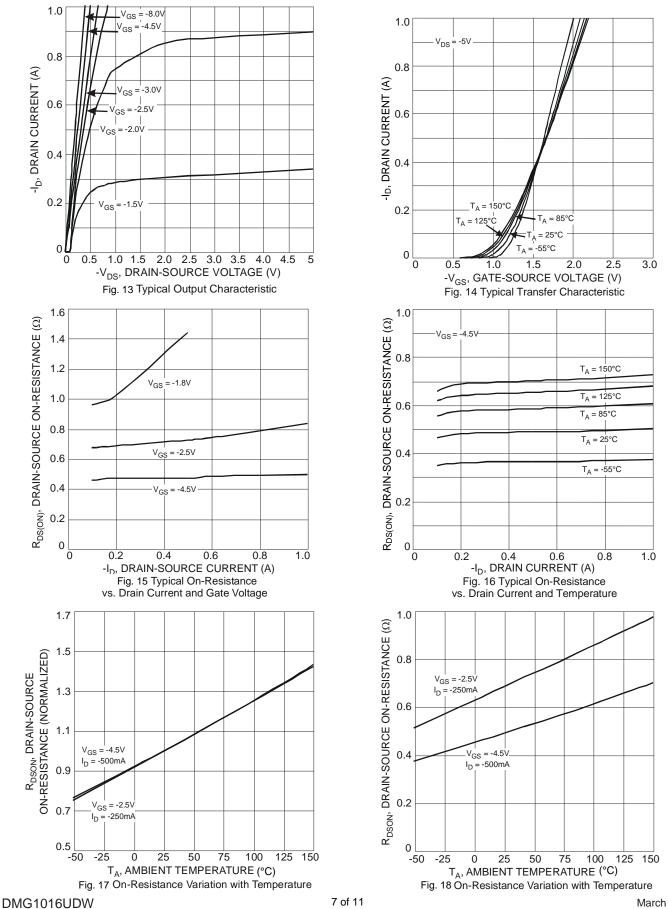
Electrical Characteristics P-CHANNEL – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Currench al	Min	Tura	Max	11	Toot Condition
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			1	[1	
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	I _{DSS}		—	-100	nA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}			±2.0	μA	$V_{GS} = \pm 4.5 V$, $V_{DS} = 0 V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
		_	0.5	0.75	Ω	$V_{GS} = -4.5V, I_D = -430mA$
Static Drain-Source On-Resistance	R _{DS(ON)}		0.7	1.05		$V_{GS} = -2.5V, I_D = -300mA$
			1.0	1.5		$V_{GS} = -1.8V, I_D = -150mA$
Forward Transfer Admittance	Y _{fs}	_	0.9	_	S	V _{DS} = -10V, I _D = -250mA
Diode Forward Voltage (Note 7)	V _{SD}	—	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -150mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	59.76	_	pF	
Output Capacitance	Coss	-	12.07	_	pF	$V_{DS} = -16V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	6.36	—	pF	
Total Gate Charge	Qg		622.4	_	рС	
Gate-Source Charge	Q _{gs}		100.3	_	рС	$V_{GS} = -4.5V, V_{DS} = -10V,$
Gate-Drain Charge	Q _{gd}	_	132.2	_	рС	$I_{\rm D} = -250 {\rm mA}$
Turn-On Delay Time	t _{D(ON)}	—	5.1	-	ns	
Turn-On Rise Time	t _R	_	8.1	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}		28.4	_	ns	$R_G = 10\Omega, R_L = 47\Omega$
Turn-Off Fall Time	t _F	_	20.72	_	ns	

 Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing Notes:



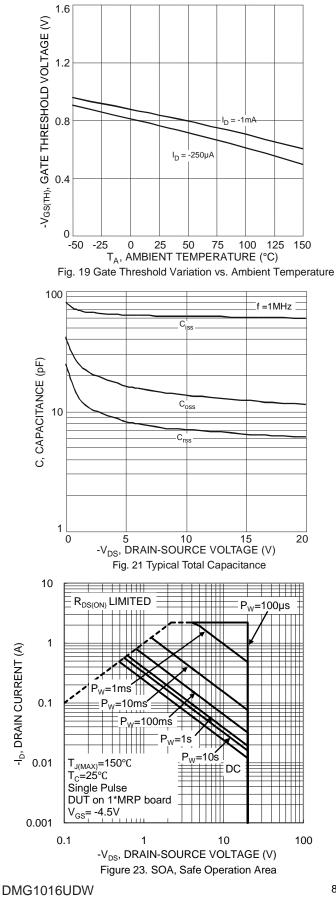
P-CHANNEL - Q2



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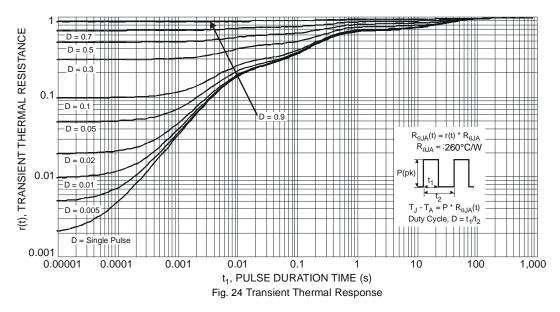


-Is, SOURCE CURRENT (A) T_A = 25°C 0.6 0.4 0.2 0_____ 0.6 0.8 1.0 1.2 0.4 -V_{SD}, SOURCE-DRAIN VOLTAGE (V) Fig. 20 Diode Forward Voltage vs. Current 1,000 T_A = 150°C -IDSS, LEAKAGE CURRENT (nA) $T_A = 125^{\circ}C$ 100 10 $T_A = 85^{\circ}C$ $T_A = 25^{\circ}C$ 1 8 12 20 0 16 4 -V_{DS}, DRAIN-SOURCE VOLTAGE (V) Fig. 22 Typical Leakage Current vs. Drain-Source Voltage

1.0

0.8

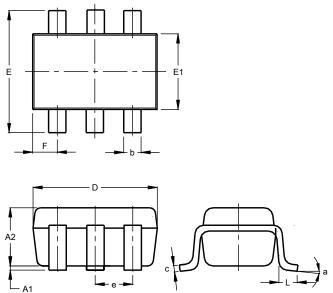






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



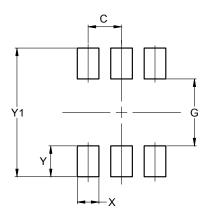
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SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	e 0.650 BSC						
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500



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