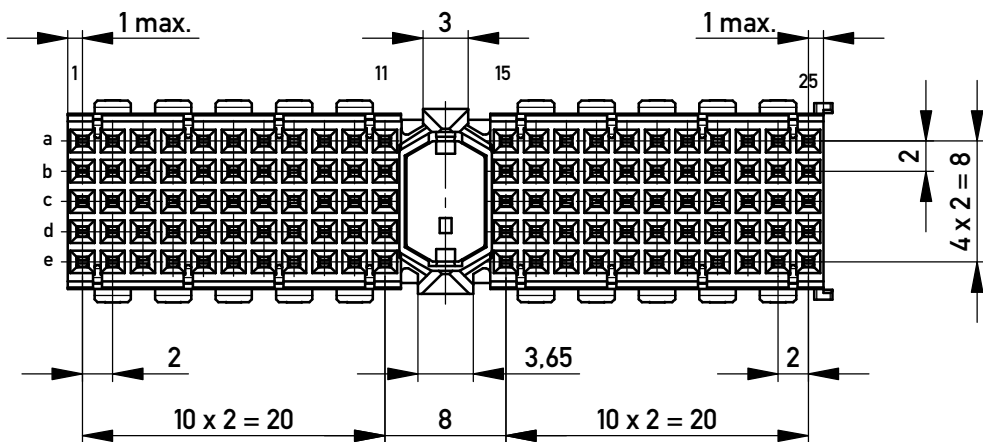
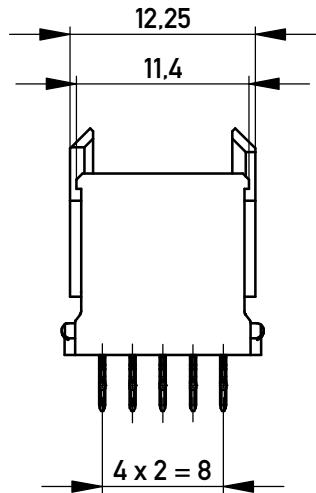
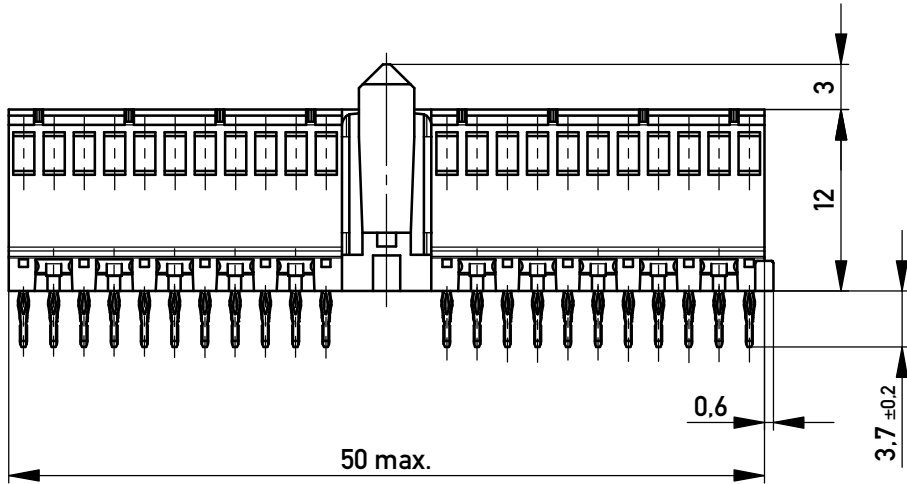
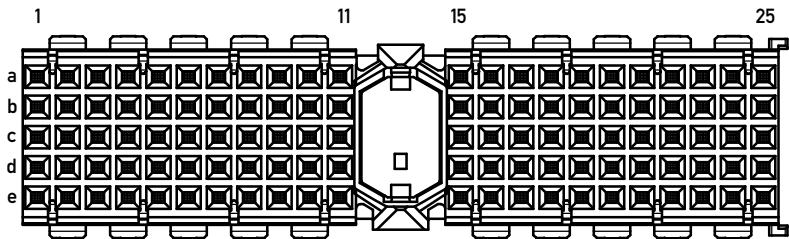


M / scale 1:1

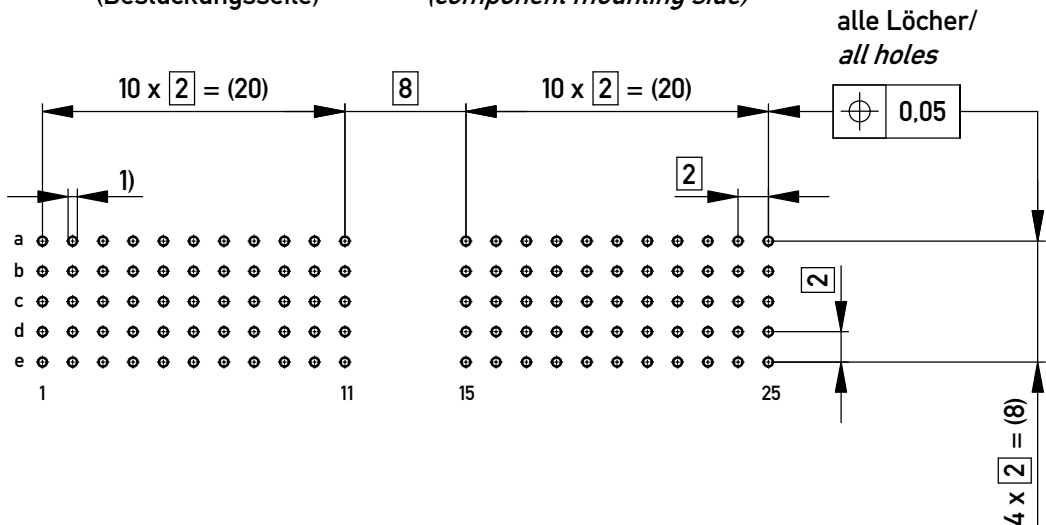
Bestückungsplan / contact layout



■ = bestückt / assemble

Lochbild für Leiterplatte
(Bestückungsseite)

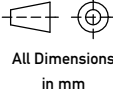
board hole pattern
(component mounting side)



- 1) $\varnothing 0,6 \pm 0,05$ Durchmesser des metallisierten Loches
 $\varnothing 0,6 \pm 0,05$ Diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 164062 Nr.1
Metal plating of plated-through hole according to 164062 No.1

Fehlende Maße und Angaben nach IEC 61076-4-101
Missing information and dimensions per IEC 61076-4-101

Information:		Tolerances		Scale	2:1
Anforderungsstufe 2 / class 2		All Dimensions in mm			
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				Class	ERMA

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