

Product Brief

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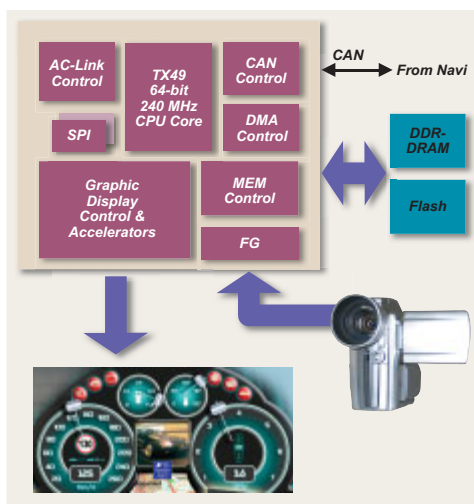
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- Line-Drawing Engine
- Frame Grabber
 - 16- or 8-bit RGB, or YCbCr
 - ITU-R 601/709 conversion
 - Frames up to 1024 x 512
 - Cropping and dotwise scaling
- External bus for 16-bit SRAM, ROM, NOR FLASH
- DDR SDRAM controller
 - 32/16-bit data, 2 channels
- DMA controller
- Interrupt controller
- NAND FLASH controller with HW ECC calculation
- Media-LB interface
- AC97-Link controller with 4-ch DMA
- I²C controller
- 3 high-speed UARTs
- 2 ESIE ports (SPI) with FIFOs
- 3 ch CAN-bus controller with 16 mailboxes each
- 30 general purpose IO pins
- 8 ch 10-bit ADC
- 6 x 16-bit PWM timers
- 6 x 16-bit complex timers
- 24-bit system timer
- Watchdog timer
- 4KB/4KB internal RAM/ROM
- On-chip clock and reset generators
- Halt, doze and standby power-down modes
- 130 nm process technology
- 456-pin PBGA, 1mm ball pitch
- -40°C to 85°C ambient operating temperature

Diagram



OS / Development Tools

- RBTX4961 Reference Board
- Greenhills Software
- Wind River Systems (under development)

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TOSHIBA

TX4961XBG-240 64-bit RISC Processor with Integrated Graphics and Display Controller

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