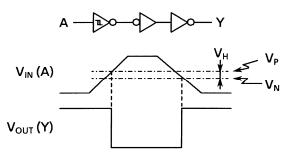
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Truth Table

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System Diagram, Waveform



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit			
Supply voltage range	V _{CC}	-0.5~7				
DC input voltage	V _{IN}	-0.5~V _{CC} + 0.5	y)			
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	\sim			
Input diode current	Ік	±20)) mA			
Output diode current	I _{OK}	±20	mA			
DC output current	lout	±25	mA			
DC V _{CC} /ground current	Icc		mA			
Power dissipation	PO	(180	mW			
Storage temperature	Tstg	-65-150	°C			

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40° C \sim 65°C. From Ta = 65°C to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2~6	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

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Electrical Characteristics

DC Characteristics

Test Condition		_	٢	Ta = 25°C			Ta = -40~85°C			
Characteristics	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Positive threshold V _P				2.0	1.0	1.25 <	1.50	1.0	1.50	
			—	4.5	2.3	2.70	3.15	2.3	3.15	V
5				6.0	3.0	3.50	4.20	3.0	4.20	
				2.0	0.30	0.65	0.9	0.30	0.9	
Negative threshold voltage	V _N		_	4.5	1.43	1.60	2.0	0.13	2.0	V
-				6.0	1.50	2.30	2.6	1.50	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis voltage V _H	V _H	_		4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0<	0.8	1.2	1.7	0.8	1,7	
				2.0	1.9	2.0		1.9	> —	
l Bach Jacob as das d		V _{IN} = V _{IL}	I _{OH} = -20 μA	4,5	()4)4	4.5		4.4	_	
High-level output V _C voltage	V _{OH}		6	6.0	5.9	6.0	$\langle \cdot \rangle$	5.9	/	V
			I _{OH} = -4 mA	4.5>	4.18	4.31	$\overline{\mathcal{P}}$	4.13	—	
			I _{OH} = -5.2 mA	6.0	5.68	5.80	()	5.63	_	
				2.0	—	0.0	0.1	—	0.1	
			I _{OL} = 20 µA	4.5	_	<u>\</u> 0.0	0.1		0.1	
Low-level output V _{OL}	V _{OL}	V _{IN} = V _{IH}	$\langle \langle \rangle \rangle$	6.0	\frown	0.0	0.1		0.1	V
			$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26		0.33	
	$I_{OL} = 5.2 \text{ mA}$		6.0	\searrow	0.18	0.26		0.33		
Input leakage current	I _{IN}	$V_{IN} \neq V_{CC}$ or GND		6.0	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	VIN = VCC or GND		6.0	$\rangle -$	—	1.0	_	10.0	μA

AC Characteristics (C) \neq 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	tтlн tтнг	-		4	8	ns
Propagation delay time	tpLH tpHL	_		11	21	ns

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AC Characteristics (C_L = 50 pF, input: $t_r = t_f = 6 \text{ ns}$)

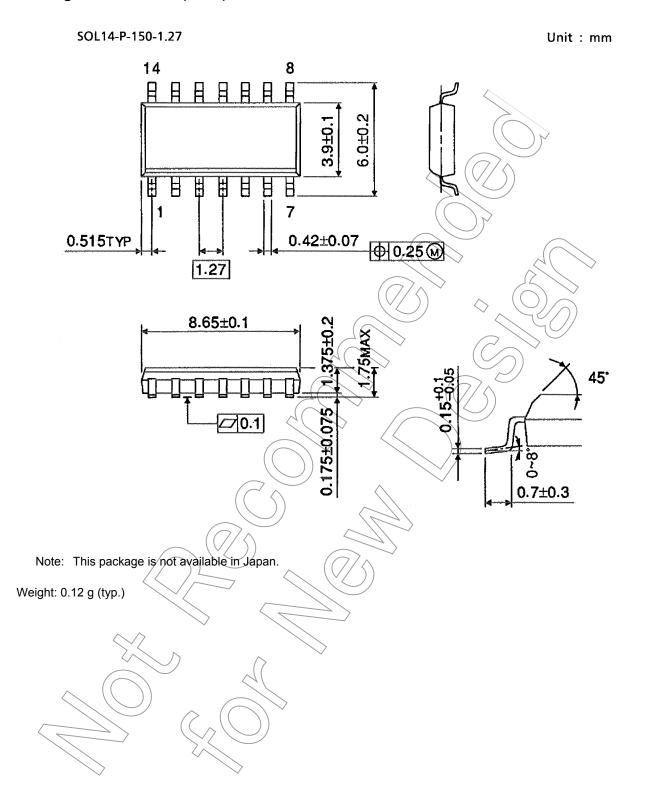
		Test Condition		Ta = 25°C			Ta = −40~85°C		
Characteristics	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
	t		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	—	4.5	—	8	15	—	19	ns
	t _{THL}		6.0	—	7	13	_	16	
			2.0	_	42	125	-	155	
Propagation delay time	t _{pLH}	_	4.5	—	14	25))	31	ns
	t _{pHL}		6.0	_	12	21	_	26	
Input capacitance	C _{IN}				5	_10	_	10	pF
Power dissipation	C _{PD}			((20	~			۳E
capacitance	(Note)				28				pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

Package Dimensions (Note)



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