Vishay Siliconix



Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit
Static				<u> </u>		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	2.0	3.0	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μΑ
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.025	0.031	Ω
		V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C			0.055	
		V _{GS} = 10 V, I _D = 15 A, T _J = 175 °C			0.069	
		$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.037	0.045	
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		20		S
Dynamic ^a						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		670		
Output Capacitance	C _{oss}			140		pF
Reverse Transfer Capacitance	C _{rss}			60		
Total Gate Charge ^c	Q_g	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 23 \text{ A}$		11	17	nC
Gate-Source Charge ^c	Q _{gs}			3		
Gate-Drain Charge ^c	Q_{gd}			3		
Turn-On Delay Time ^c	t _{d(on)}	V_{DD} = 30 V, R_L = 1.3 Ω I_D \cong 23 A, V_{GEN} = 10 V, R_g = 2.5 Ω		8	15	ns
Rise Time ^c	t _r			15	25	
Turn-Off Delay Time ^c	t _{d(off)}			30	45	
Fall Time ^c	t _f			25	40	
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C)			<u> </u>	
Pulsed Current	I _{SM}		_		50	Α
Diode Forward Voltage	V_{SD}	I _F = 15 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 15 A, di/dt = 100 A/μs		30	60	ns

Notes:

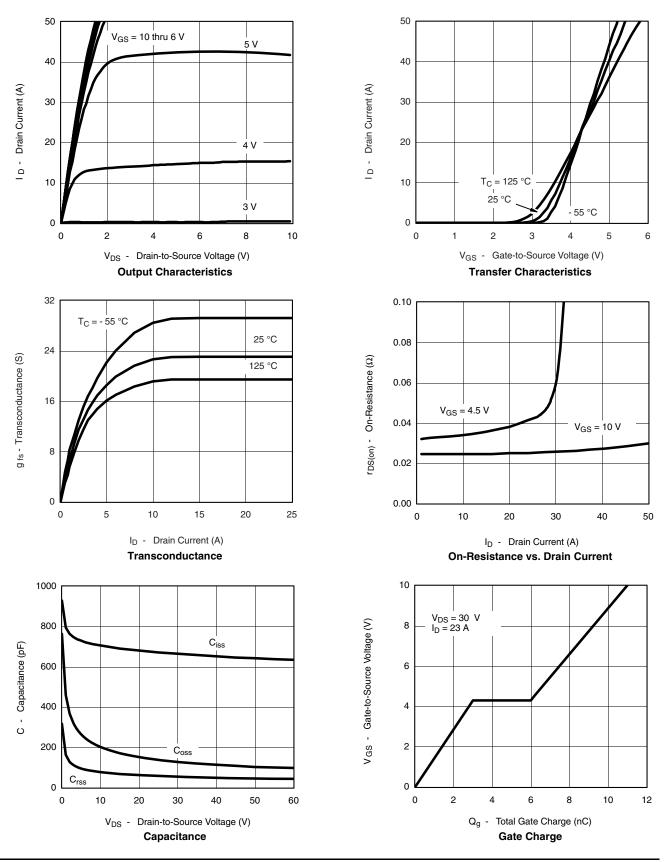
- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





TYPICAL CHARACTERISTICS 25 °C unless noted

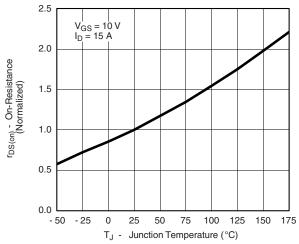


Document Number: 72145 S-71660-Rev. C, 06-Aug-07

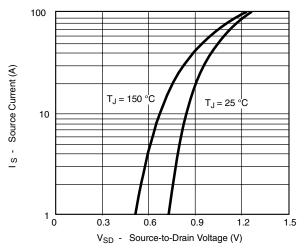
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TYPICAL CHARACTERISTICS 25 °C unless noted



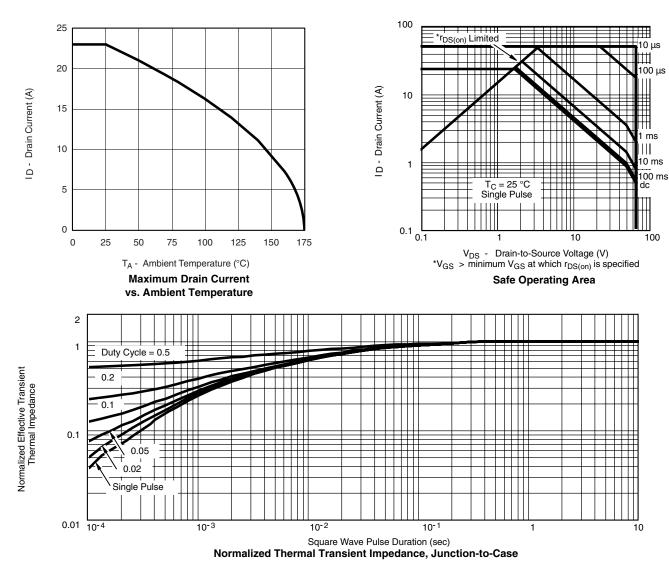
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



THERMAL RATINGS



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72145.



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Document Number: 91000 www.vishay.com Revision: 18-Jul-08