STPS30L30CTPbF

Vishay High Power Products Schottky Rectifier, 2 x 15 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	15 A	T _{.1} = 25 °C	0.46	V	
		30 A	1j=25 C	0.57		
		15 A	T. = 125 °C	0.37		
		30 A	1J=125 C	0.50		
Maximum reverse leakage current per leg	I _{RM}	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1.50	- mA	
Maximum reverse leakage current per leg		T _J = 125 °C	VR = nateu VR	350		
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1500	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MBOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation	1.5	°C/W	
Maximum thermal resistance, junction to case per package		R _{thJC}		0.8		
A source instance and a second				2	g	
Approximate weight				0.07	OZ.	
Mounting torque —	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf \cdot in)	
Marking device			Case style TO-220AB	STPS30	DL30CT	

For technical questions, contact: diodes-tech@vishay.com Document Number: 94328 Revision: 14-Aug-08



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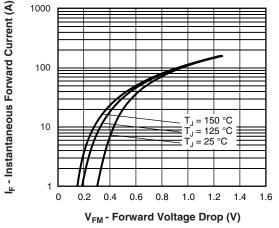


Fig. 1 - Maximum Forward Voltage Drop Characteristics

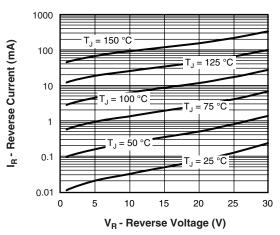


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

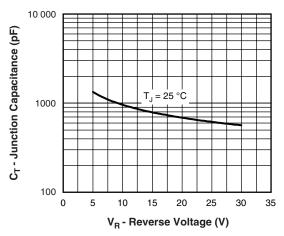


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

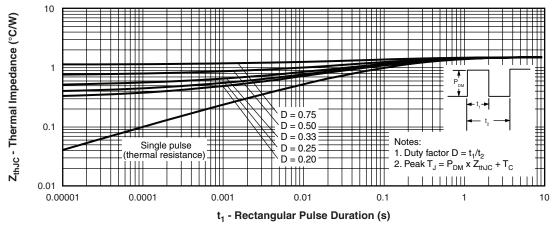


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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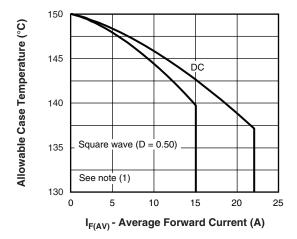


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

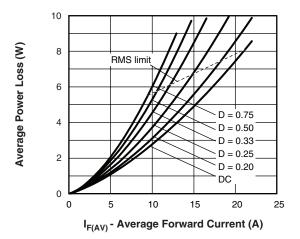


Fig. 6 - Forward Power Loss Characteristics

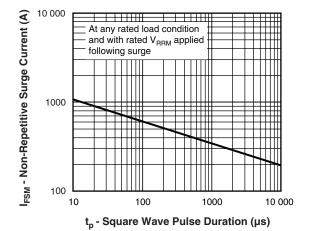


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

(1) Formula used: $T_C = T_J - Pd \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6)

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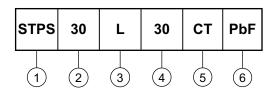
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ORDERING INFORMATION TABLE

Device code



- 1 Schottky STPS series
- 2 Current rating (30 = 30 A)
- 3 L = Low voltage drop
- 4 Voltage rating (30 = 30 V)
- 5 CT = Essential part number
- 6 • None = Standard production
 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			
SPICE model	http://www.vishay.com/doc?95287			

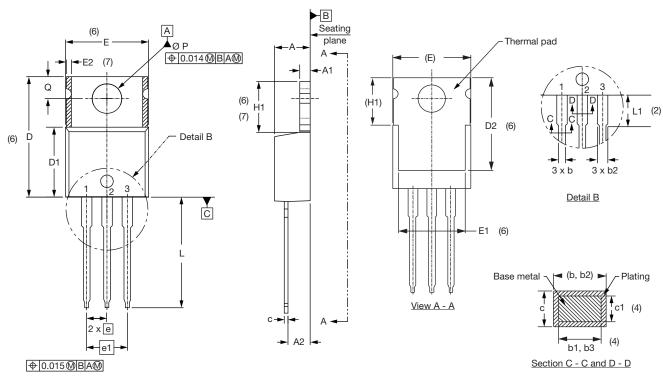
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Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches



Lead assignments

<u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIMETERS		INC	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip

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